

STEALTH™ Diode
50 A, 600 V**FFH50US60S****Description**

The FFH50US60S is a STEALTH™ diode optimized for low loss performance in output rectification. The STEALTH family exhibits low reverse recovery current (I_{RR}), low V_F and soft recovery under typical operating conditions. This device is intended for use as an output rectification diode in Telecom power supplies and other power switching applications. Lower V_F and I_{RR} reduces diode losses.

Features

- Stealth Recovery, $t_{rr} = 113$ ns (@ $I_F = 50$ A)
- Max Forward Voltage, $V_F = 1.54$ V (@ $T_C = 25^\circ\text{C}$)
- 600 V Reverse Voltage and High Reliability
- Operating Temperature = 175°C
- Avalanche Energy Rated
- This Device is Pb-Free and is RoHS Compliant

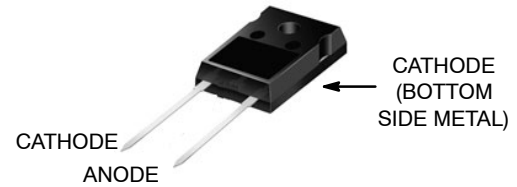
Applications

- SMPS, Welders
- Power Factor Correction
- Uninterruptible Power Supplies
- Motor Drives

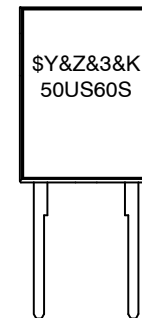
ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	600	V
Working Peak Reverse Voltage	V_{RWM}	600	V
DC Blocking Voltage	V_R	600	V
Average Rectified Forward Current ($T_C = 120^\circ\text{C}$)	$I_{F(AV)}$	50	A
Repetitive Peak Surge Current (20 kHz Square Wave)	I_{FRM}	100	A
Nonrepetitive Peak Surge Current (Halfwave, 1 Phase, 60 Hz)	I_{FSM}	500	A
Power Dissipation	P_D	200	W
Avalanche Energy (1 A, 40 mH)	E_{AVL}	20	mJ
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 175	$^\circ\text{C}$
Maximum Temperature for Soldering Leads at 0.063 in (1.6 mm) from Case for 10 s	T_L	300	$^\circ\text{C}$
Maximum Temperature for Soldering Package Body for 10 s	T_{PKG}	260	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



TO-247
JEDEC STYLE
2 LEAD
CASE 340 CL

MARKING DIAGRAM

\$Y = ON Semiconductor Logo
&Z = Assembly Plant Code
&3 = Numeric Date Code
&K = Lot Code
50US60S = Specific Device Code

K
|
▲
|
A

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

FFH50US60S

PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Package	Packing Methode	Reel Size	Tape Width	Quantity
FFH50US60S	FFH50US60S	TO247-2L	Tube	N/A	N/A	30

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
--------	-----------	-----------------	------	------	------	------

OFF STATE CHARACTERISTICS

I_R	Instantaneous Reverse Current	$V_R = 600\text{ V}$	$T_C = 25^\circ\text{C}$	–	–	100	μA
			$T_C = 125^\circ\text{C}$	–	–	1	mA

ON CHARACTERISTICS

V_F	Instantaneous Forward Voltage	$I_F = 50\text{ A}$	$T_C = 25^\circ\text{C}$	–	1.38	1.54	V
			$T_C = 125^\circ\text{C}$	–	1.37	1.53	V

DYNAMIC CHARACTERISTICS

C_J	Junction Capacitance	$V_R = 10\text{ V}, I_F = 0\text{ A}$	–	110	–	pF
-------	----------------------	---------------------------------------	---	-----	---	----

SWITCHING CHARACTERISTICS

T_{rr}	Reverse Recovery Time	$I_F = 1\text{ A}, di_F/dt = 100\text{ A}/\mu\text{s}, V_R = 15\text{ V}$	–	47	80	ns
		$I_F = 50\text{ A}, di_F/dt = 100\text{ A}/\mu\text{s}, V_R = 15\text{ V}$	–	75	124	ns
T_{rr}	Reverse Recovery Time	$I_F = 50\text{ A}, di_F/dt = 200\text{ A}/\mu\text{s}, V_R = 390\text{ V}, T_C = 25^\circ\text{C}$	–	113	–	ns
I_{RR}	Reverse Recovery Current		–	9.6	–	A
Q_{RR}	Reverse Recovered Charge		–	0.9	–	μC
T_{rr}	Reverse Recovery Time		–	235	–	ns
S	Softness Factor (t_b/t_a)	$I_F = 50\text{ A}, di_F/dt = 200\text{ A}/\mu\text{s}, V_R = 390\text{ V}, T_C = 125^\circ\text{C}$	–	1.5	–	–
I_{RR}	Reverse Recovery Current		–	15	–	A
Q_{RR}	Reverse Recovered Charge		–	2.3	–	μC
T_{rr}	Reverse Recovery Time		–	110	–	ns
S	Softness Factor (t_b/t_a)	$I_F = 50\text{ A}, di_F/dt = 1000\text{ A}/\mu\text{s}, V_R = 390\text{ V}, T_C = 125^\circ\text{C}$	–	0.8	–	–
I_{RR}	Reverse Recovery Current		–	46	–	A
Q_{RR}	Reverse Recovered Charge		–	3.1	–	μC
di_M/dt	Maximum di/dt during t_b		–	1000	–	A/ μs

THERMAL CHARACTERISTICS

$R_{\theta JC}$	Thermal Resistance Junction to Case		–	–	0.75	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-247	–	–	30	$^\circ\text{C}/\text{W}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL PERFORMANCE CURVES

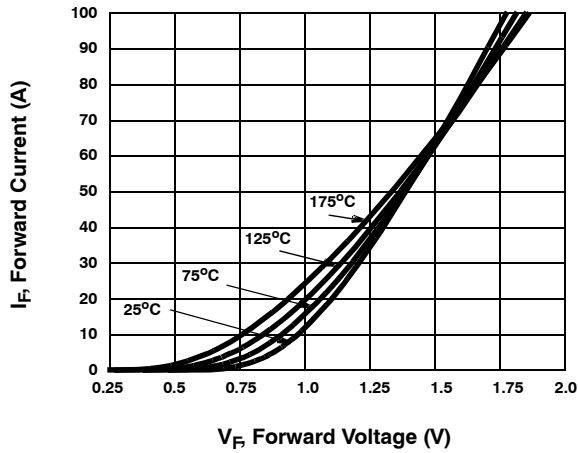


Figure 1. Forward Current vs. Forward Voltage

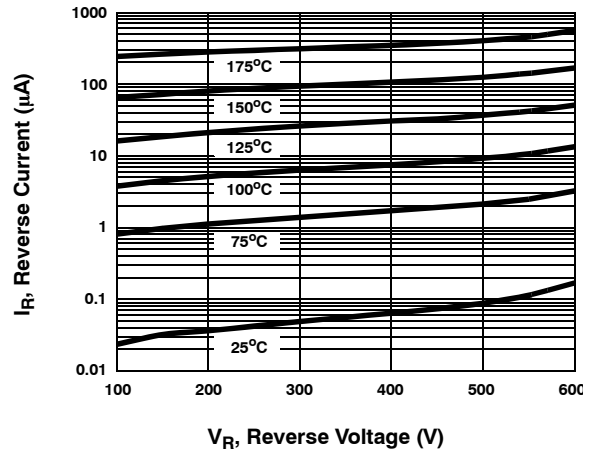


Figure 2. Reverse Current vs. Reverse Voltage

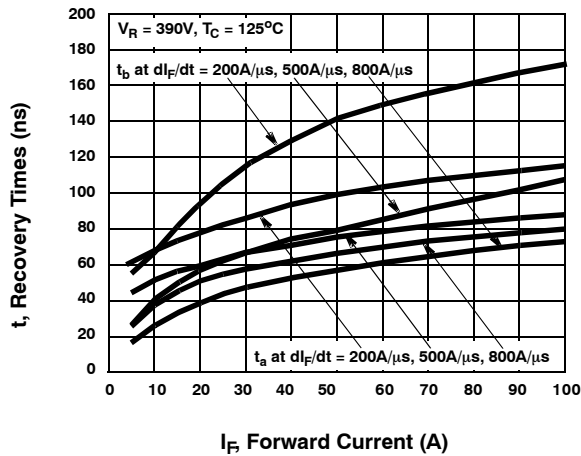


Figure 3. t_a and t_b Curves vs. Forward Current

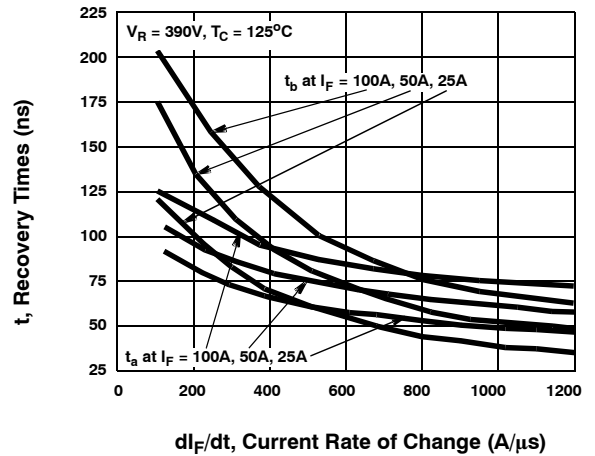


Figure 4. t_a and t_b Curves vs. dl_F/dt

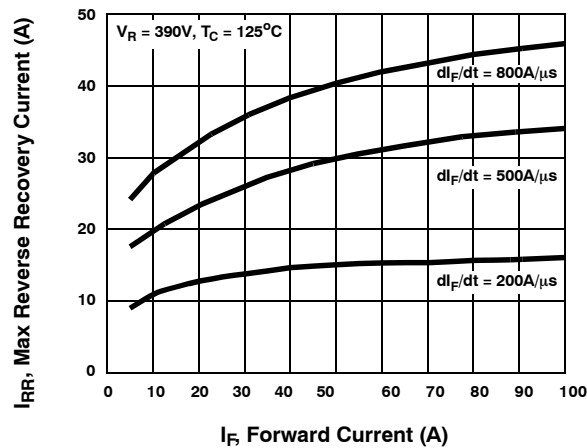


Figure 5. Maximum Reverse Recovery Current vs. Forward Current

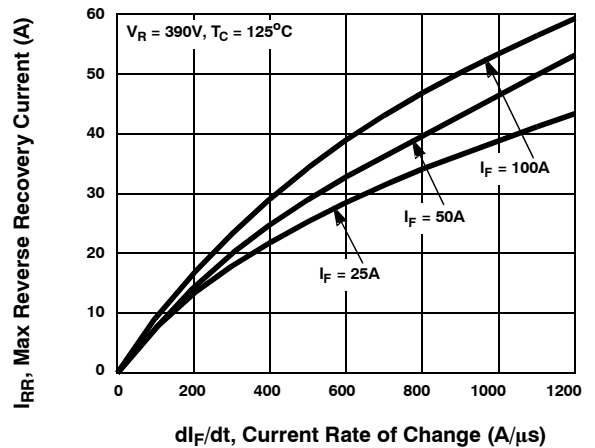


Figure 6. Maximum Reverse Recovery Current vs. dl_F/dt

TYPICAL PERFORMANCE CURVES

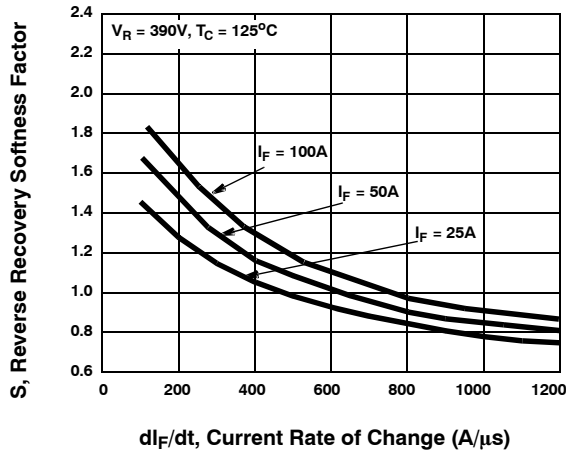


Figure 7. Reverse Recovery Softness Factor vs. dI_F/dt

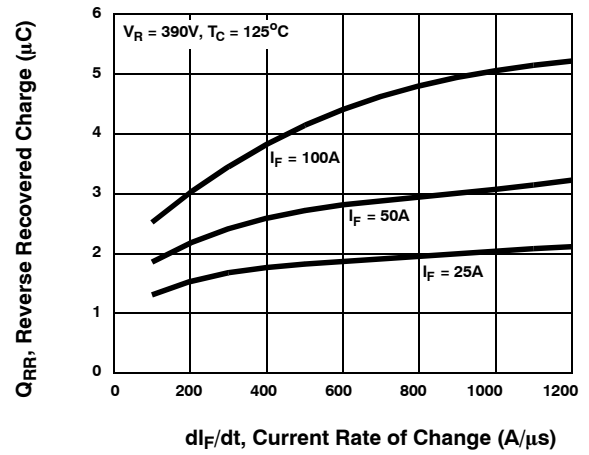


Figure 8. Reverse Recovery Charge vs. dI_F/dt

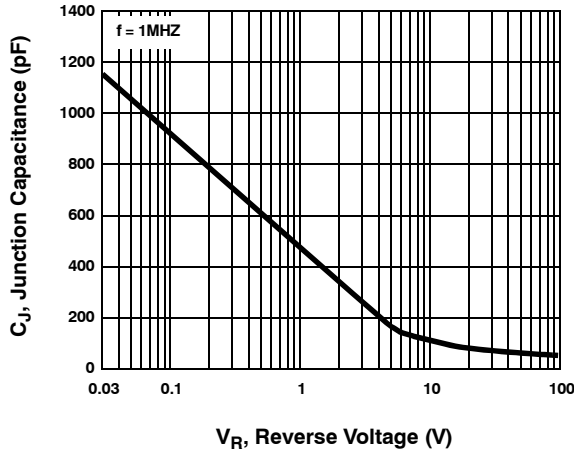


Figure 9. Junction Capacitance vs. Reverse Voltage

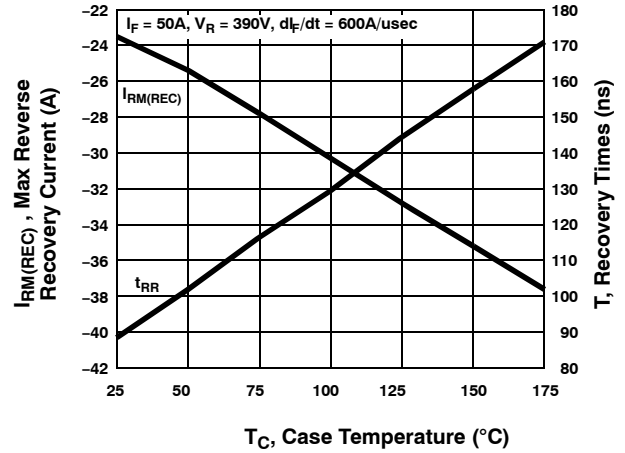


Figure 10. Maximum Reverse Recovery Current and t_{rr} vs. Case Temperature

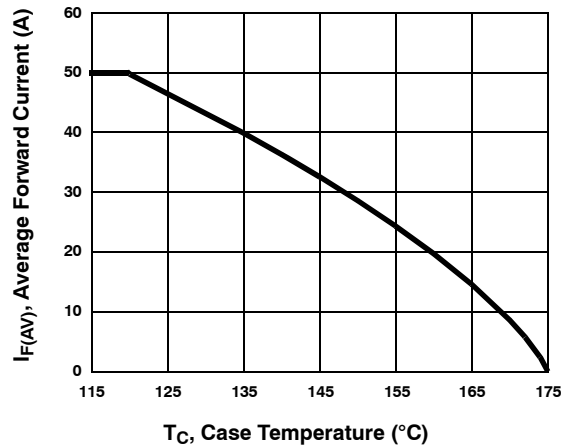


Figure 11. DC Current Derating Curve

TYPICAL PERFORMANCE CURVES

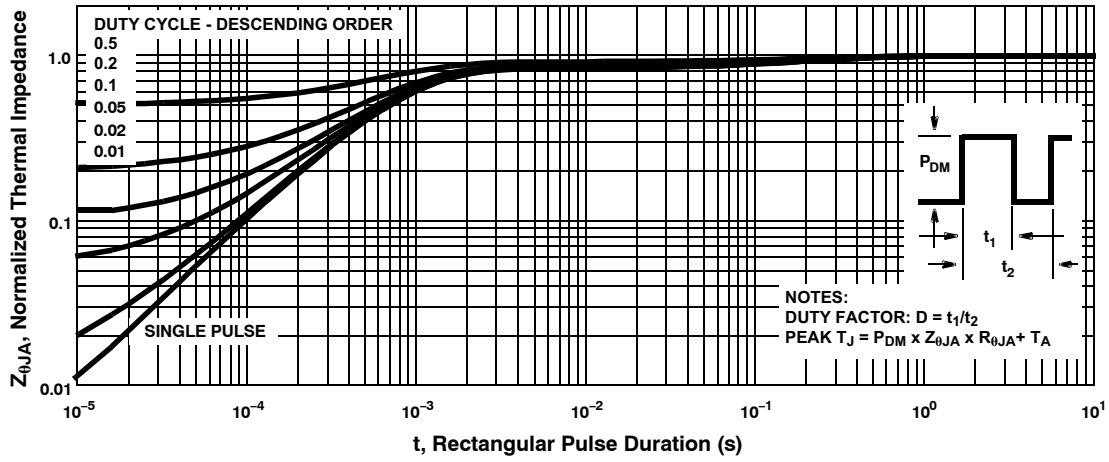


Figure 12. Normalized Maximum Transient Thermal Impedance

TEST CIRCUITS AND WAVEFORMS

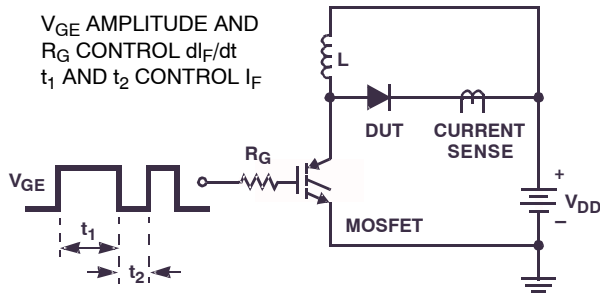


Figure 13. T_{rr} Test Circuit

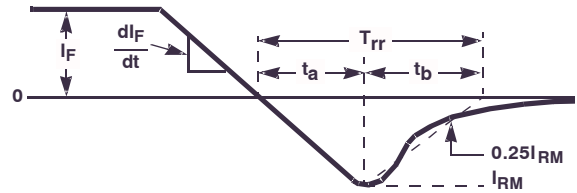


Figure 14. T_{rr} Waveforms and Definitions

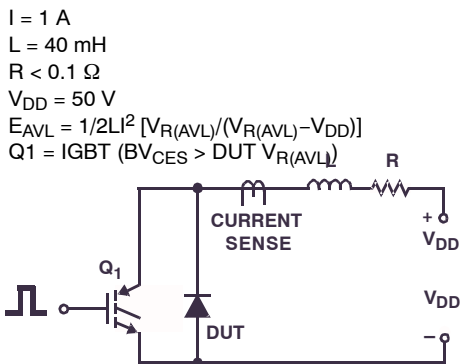


Figure 15. Avalanche Energy Test Circuit

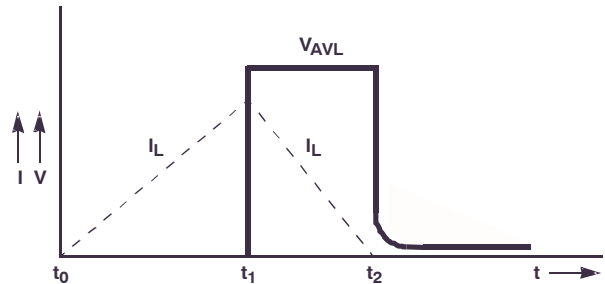


Figure 16. Avalanche Current and Voltage Waveforms

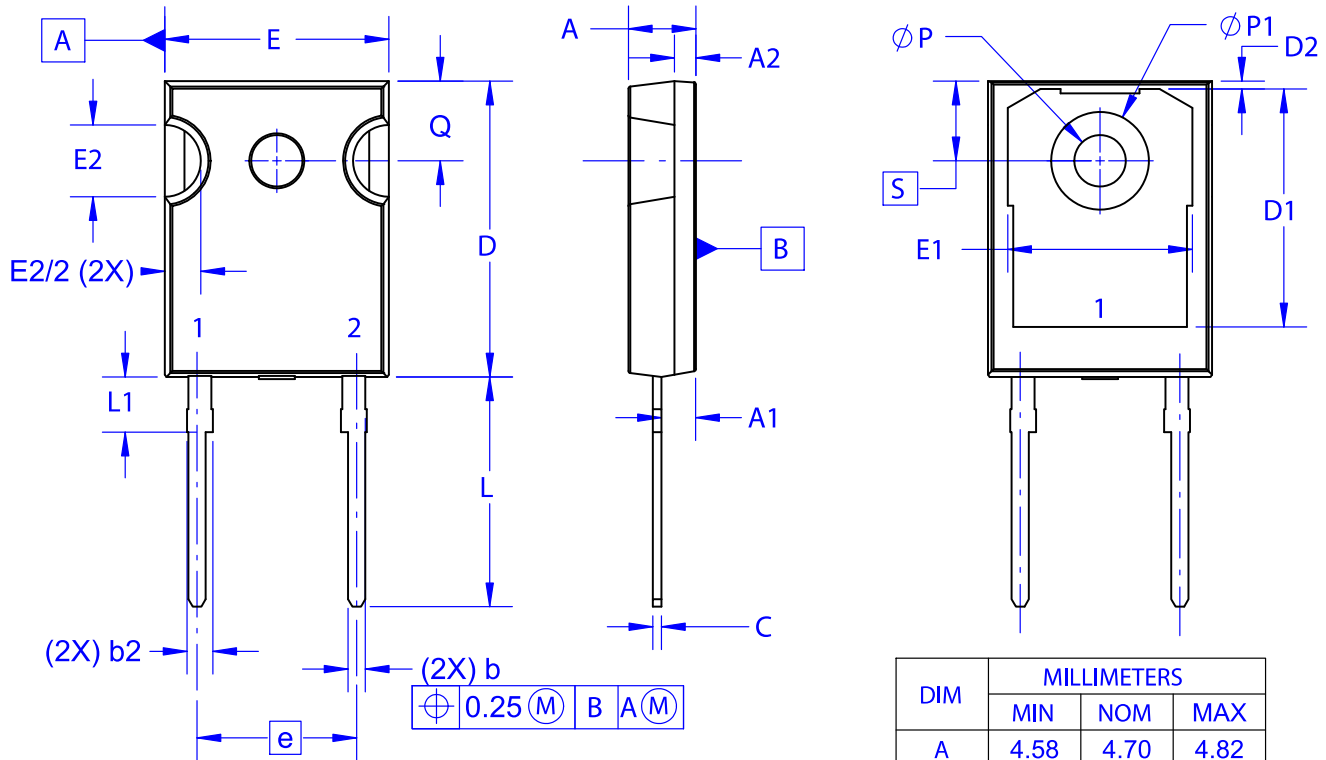
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®

ON

TO-247-2LD
CASE 340CL
ISSUE A

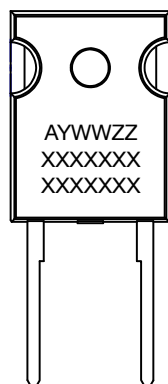
DATE 03 DEC 2019



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON13850G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	TO-247-2LD	PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at
www.onsemi.com/support/sales