

Silicon Carbide (SiC) Schottky Diode – EliteSiC, 40 A, 1200 V, D3, TO-247-3L

NDSH40120CDN

Description

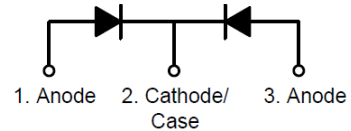
Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

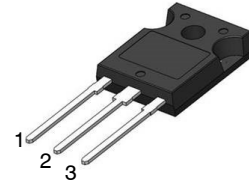
- Max Junction Temperature 175°C
- Avalanche Rated 166 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- This Device is Halide Free and RoHS Compliant with Exemption 7a, Pb-Free 2LI (on second level interconnection)

Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

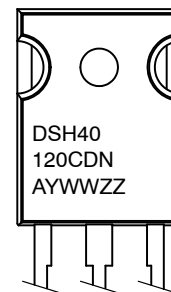


Schottky Diode



TO-247-3LD
CASE 340CX

MARKING DIAGRAM



DSH40120CDN	= Specific Device Code
A	= Assembly Plant Code
YWW	= Date Code (Year & Week)
ZZ	= Lot Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

NDSH40120CDN

ABSOLUTE MAXIMUM RATINGS (T_J = 25°C unless otherwise noted) (per leg)

Symbol	Parameter	Value	Unit
V _{RRM}	Peak Repetitive Reverse Voltage	1200	V
E _{AS}	Single Pulse Avalanche Energy (Note 1)	166	mJ
I _F	Continuous Rectified Forward Current @ T _C < 149°C	20*/40**	A
	Continuous Rectified Forward Current @ T _C < 135°C	26*/52**	
I _{F, Max}	Non-Repetitive Peak Forward Surge Current	T _C = 25°C, 10 μs	A
		T _C = 150°C, 10 μs	A
I _{F, SM}	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t _p = 8.3 ms	A
I _{F, RM}	Repetitive Forward Surge Current	Half-Sine Pulse, t _p = 8.3 ms	A
P _{tot}	Power Dissipation	T _C = 25°C	W
		T _C = 150°C	W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

*Per leg.

** Per device.

1. E_{AS} of 166 mJ is based on starting T_J = 25°C, L = 0.5 mH, I_{AS} = 25.8 A, V = 50 V.

THERMAL CHARACTERISTICS (per leg)

Symbol	Parameter	Value	Unit
R _{θJC}	Thermal Resistance, Junction to Case, Max	0.69*/0.39**	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient, Max	40	°C/W

*Per leg.

** Per device.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted) (per leg)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V _F	Forward Voltage	I _F = 20 A, T _J = 25°C	–	1.36	1.75	V
		I _F = 20 A, T _J = 125°C	–	1.60	–	
		I _F = 20 A, T _J = 175°C	–	1.81	–	
I _R	Reverse Current	V _R = 1200 V, T _J = 25°C	–	2.39	200	μA
		V _R = 1200 V, T _J = 125°C	–	6.91	200	
		V _R = 1200 V, T _J = 175°C	–	16.6	200	
Q _C	Total Capacitive Charge	V = 800 V	–	95	–	nC
C	Total Capacitance	V _R = 1 V, f = 100 kHz	–	1494	–	pF
		V _R = 400 V, f = 100 kHz	–	80	–	
		V _R = 800 V, f = 100 kHz	–	60	–	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
NDSH40120CDN	DSH40120CDN	TO-247-3LD (Pb-Free / Halogen Free)	30 Units / Tube

TYPICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted) (per leg)

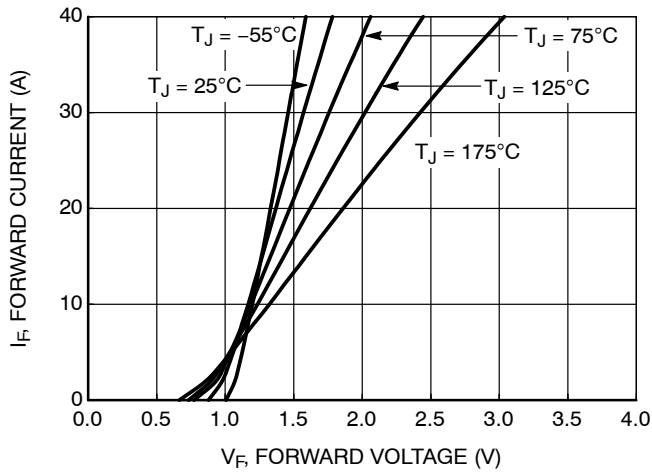


Figure 1. Forward Characteristics

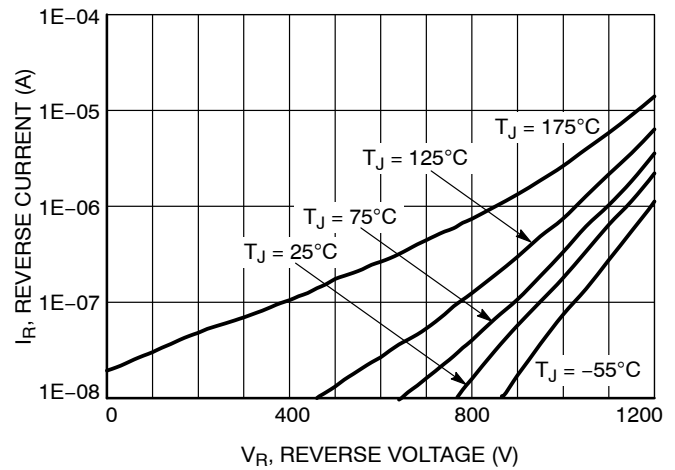


Figure 2. Reverse Characteristics

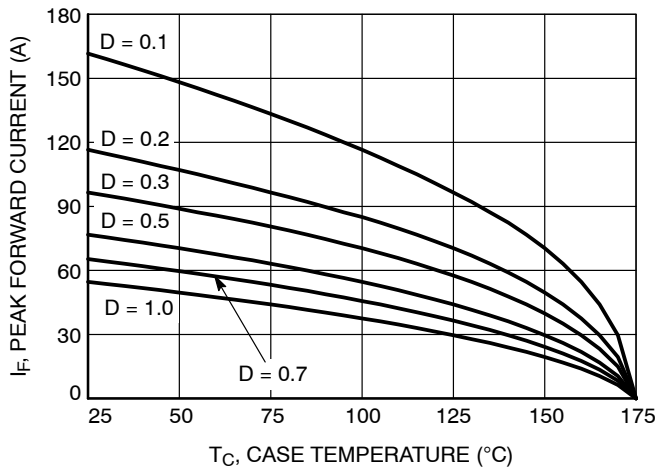


Figure 3. Current Derating

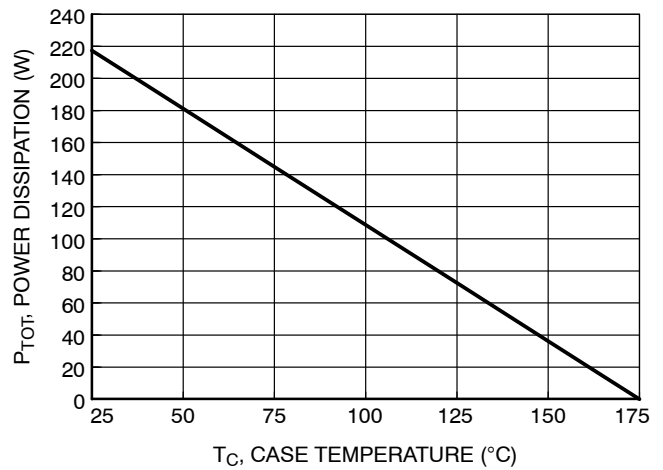


Figure 4. Power Derating

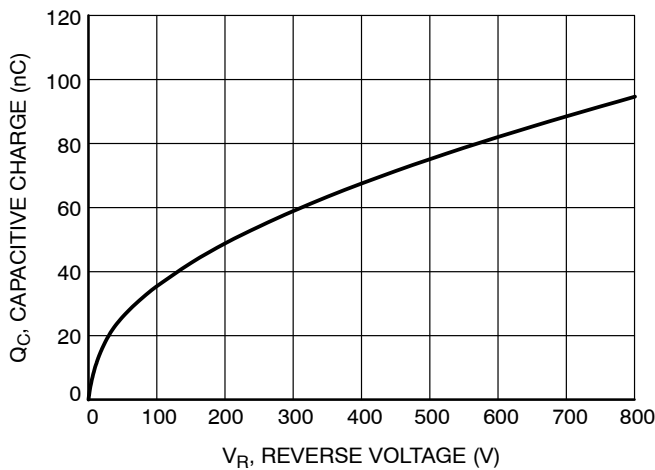


Figure 5. Capacitive Charge vs. Reverse Voltage

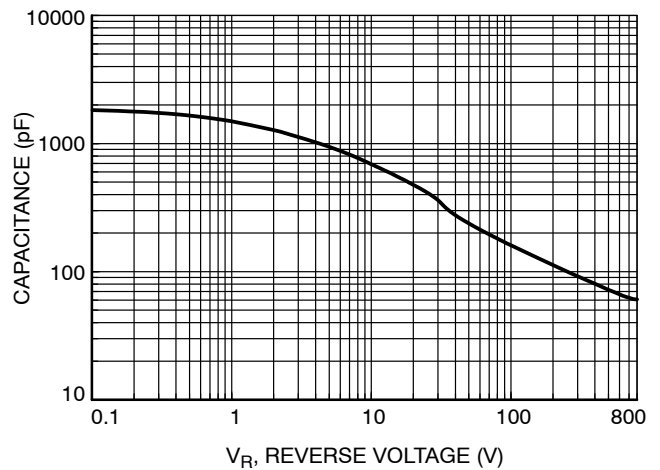


Figure 6. Capacitive vs. Reverse Voltage

NDSH40120CDN

TYPICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted) (per leg)

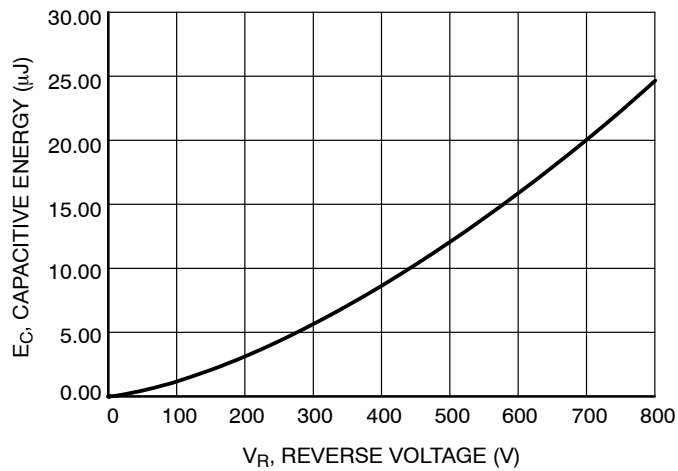


Figure 7. Capacitance Stored Energy

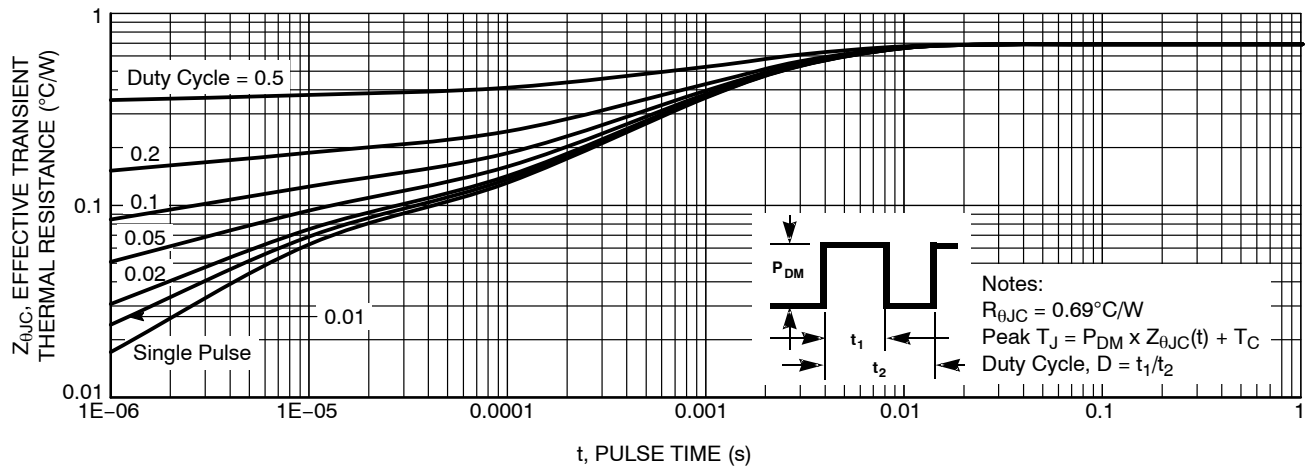


Figure 8. Junction-to-Case Transient Thermal Response Curve

MECHANICAL CASE OUTLINE

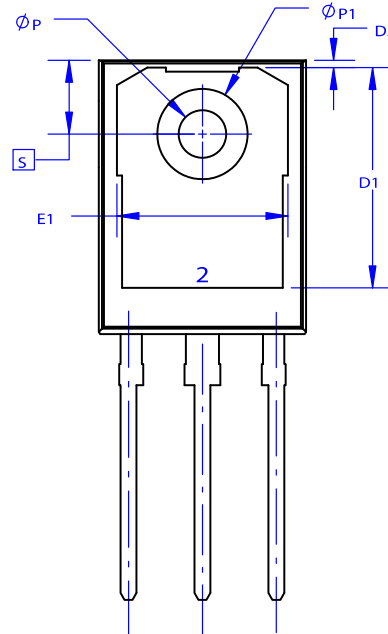
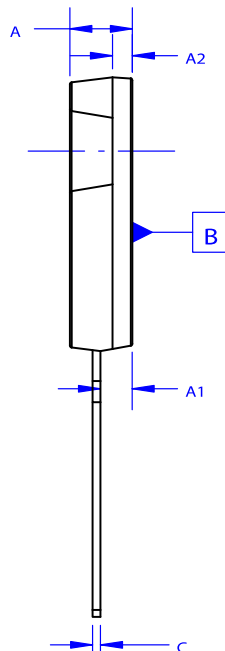
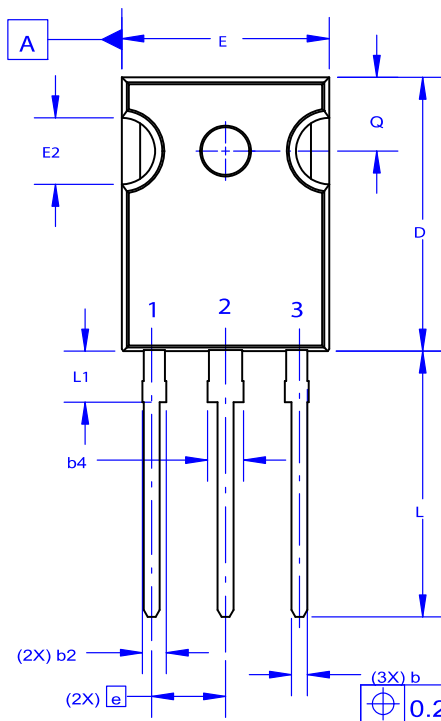
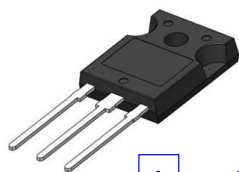
PACKAGE DIMENSIONS

ON Semiconductor®



TO-247-3LD
CASE 340CX
ISSUE A

DATE 06 JUL 2020



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.58	4.70	4.82
A1	2.20	2.40	2.60
A2	1.40	1.50	1.60
D	20.32	20.57	20.82
E	15.37	15.62	15.87
E2	4.96	5.08	5.20
e	~	5.56	~
L	19.75	20.00	20.25
L1	3.69	3.81	3.93
ØP	3.51	3.58	3.65
Q	5.34	5.46	5.58
S	5.34	5.46	5.58
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
c	0.51	0.61	0.71
D1	13.08	~	~
D2	0.51	0.93	1.35
E1	12.81	~	~
ØP1	6.60	6.80	7.00

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