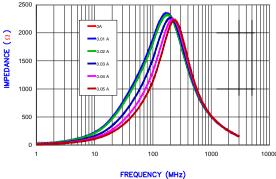
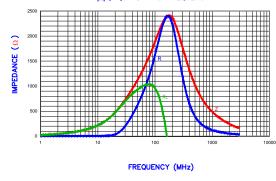


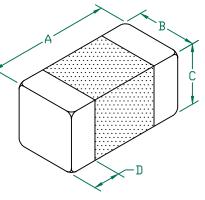
PHYSICAL DIMENSIONS:

Z vs. FREQUENCY
IMPEDANCE UNDER DC BIAS









ELECTRICAL CHARACTERISTICS:										
Z @ 100MHz (<u>Ω</u>)		DCR (Ω)	Rated Current							
Nominal	1800									
Minimum	1350									
Maximum	2250	1.5	50 mA							

NOTES: UNLESS OTHERWISE SPECIFIED

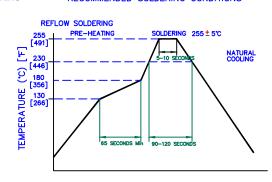
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER CARRIER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING

0.60 0.80 0.031 0.80 0.0313

(For wave soldering, add 0.762 [.030] to this dimension)

RECOMMENDED SOLDERING CONDITIONS



	DIMENSIONS ARE IN mm [INCHES]. This print is the property of Laird									
				Tech. and is loaned in confidence subject to return upon request with the understanding that no copies shall be made without the written consent of Laird Tech. A rights to design or invention are reserved.	L	Laird				
D	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER:	- 1	REV D	PART TO	TYPE:	DRAWN BY: JRK	
o	UPDATE COMPANY LOGO	02/22/08	JRK	HZ0603A182R-10			OO TIKE		000	
В	ADD DC BIAS CURVE	07/25/06	JRK	DATE: 03/30/06	SCA	LE:		SHEET:		
A	ORIGINAL DRAFT	03/30/06	JRK	CAD #	TOO	TOOL #		1	of 1	
REV	DESCRIPTION	DATE	INT	" "HZ0603A182R-10-D		- ' "' '			•	