

## Features

- Very Low FOM  $R_{DS(on)} \times Q_g$
- Moisture Sensitivity Level 3
- Halogen Free. "Green" Device <sup>(Note 1)</sup>
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## Maximum Ratings

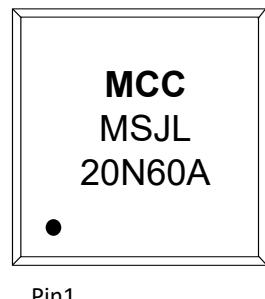
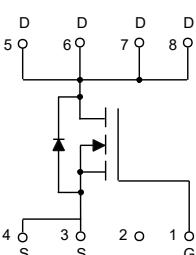
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient <sup>(Note2)</sup>
- Thermal Resistance: 0.7°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	600	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current $T_c=25^\circ\text{C}$	$I_D$	20	A
$T_c=100^\circ\text{C}$		12.6	
Pulsed Drain Current <sup>(Note 3)</sup>	$I_{DM}$	80	A
Total Power Dissipation <sup>(Note4)</sup>	$P_D$	178	W
Avalanche Energy <sup>(Note 5)</sup>	$E_{AS}$	120	mJ

Note:

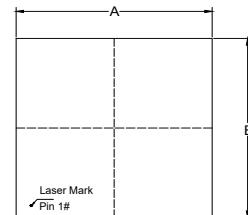
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .
3. Repetitive rating; pulse width limited by max. junction temperature.
4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
5.  $T_J=25^\circ\text{C}$ ,  $V_{DD}=50\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $R_G=25\Omega$ ,  $L=79\text{mH}$ .

## Internal Structure and Marking Code

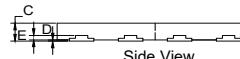


## N-CHANNEL MOSFET

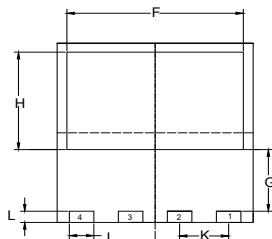
DFN8080A



Top View



Side View



Bottom View

DIMENSIONS

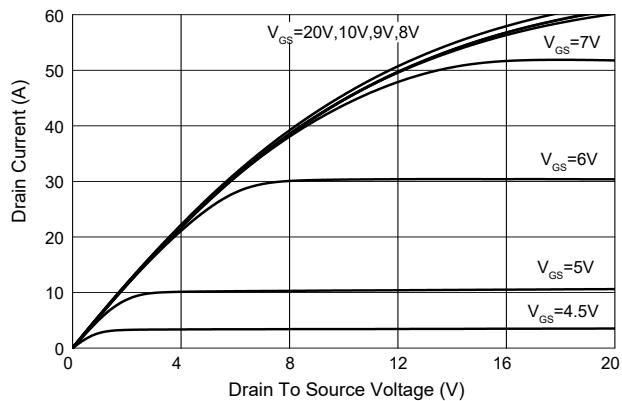
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.311	0.319	7.90	8.10	
B	0.311	0.319	7.90	8.10	
C	0.030	0.037	0.75	0.95	
D	0.000	0.002	0.00	0.05	
E	0.004	0.012	0.10	0.30	
F	0.280	0.287	7.10	7.30	
G	0.104	0.112	2.65	2.85	
H	0.167	0.175	4.25	4.45	
J	0.035	0.043	0.90	1.10	
K	0.079		2.00		BSC
L	0.016	0.024	0.40	0.60	

**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

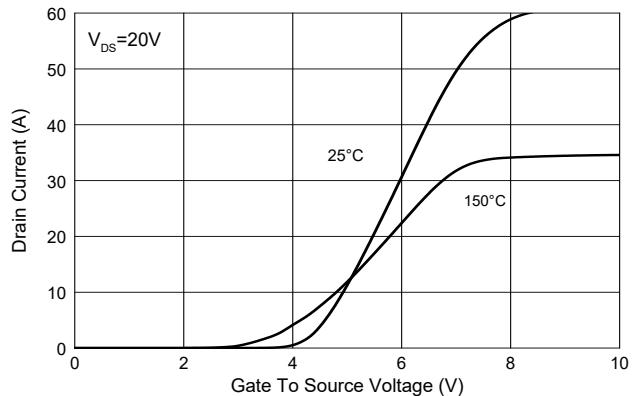
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	600			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 30V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=600V, V_{GS}=0V$			1	$\mu A$
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=7.3A$		175	219	$m\Omega$
Gate Resistance	$R_g$	f=1MHz, Open drain		8		$\Omega$
<b>Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$				20	A
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=20A$			1.4	V
Reverse Recovery Time	$t_{rr}$	$I_F=20A, dI_F/dt=100A/\mu s$		363		ns
Reverse Recovery Charge	$Q_{rr}$			5.6		$\mu C$
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V, f=1MHz$		1468		pF
Output Capacitance	$C_{oss}$			1503		
Reverse Transfer Capacitance	$C_{rss}$			39		
Total Gate Charge	$Q_g$	$V_{DS}=480V, V_{GS}=10V, I_D=20A$		39		nC
Gate-Source Charge	$Q_{gs}$			9		
Gate-Drain Charge	$Q_{gd}$			16		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=480V, V_{GS}=10V, I_D=10A, R_G=25\Omega$		31		ns
Turn-On Rise Time	$t_r$			61		
Turn-Off Delay Time	$t_{d(off)}$			176		
Turn-Off Fall Time	$t_f$			64		

## Curve Characteristics

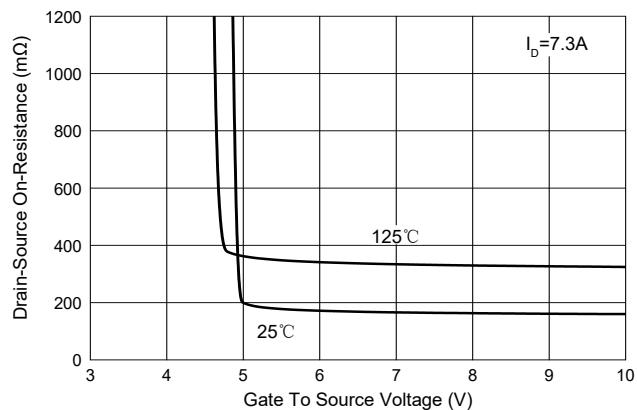
**Fig.1 - Typical Output Characteristics**



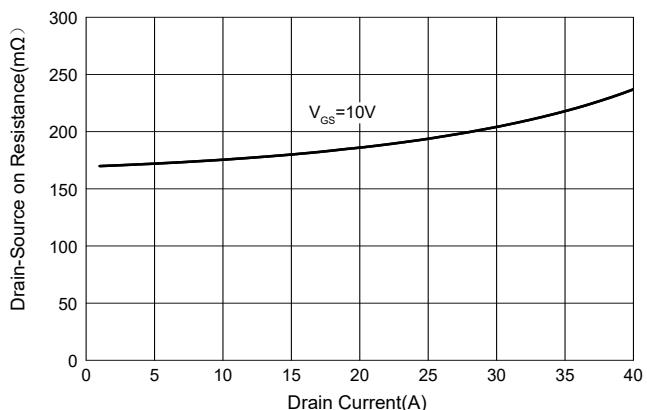
**Fig.2 - Transfer Characteristic**



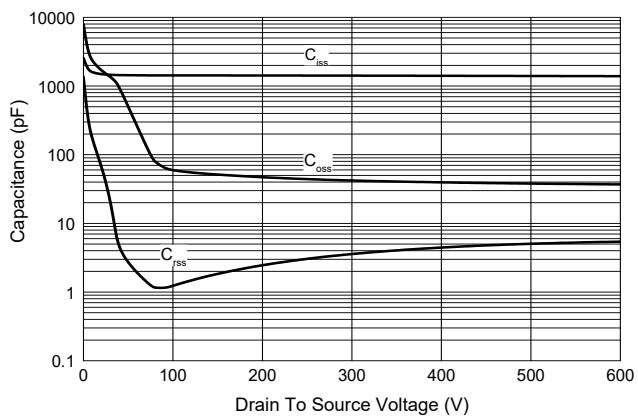
**Fig.3 -  $R_{DS(ON)}$  -  $V_{GS}$**



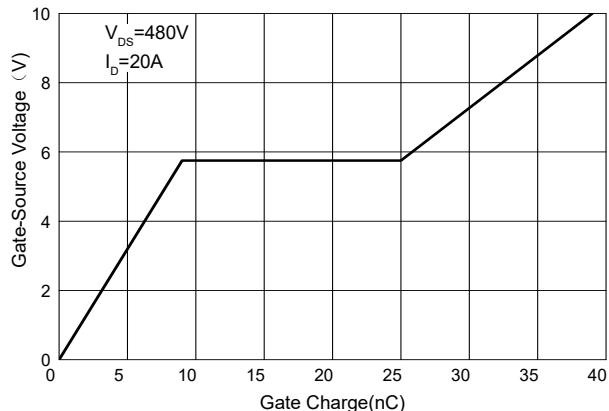
**Fig.4 -  $R_{DS(ON)}$  -  $I_D$**



**Fig.5 - Capacitance Characteristics**

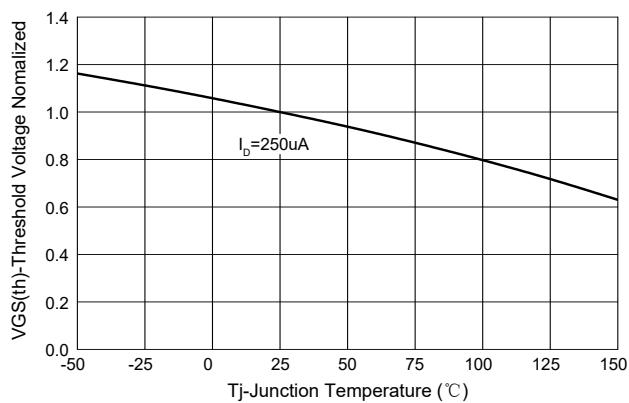


**Fig.6 - Gate Charge**

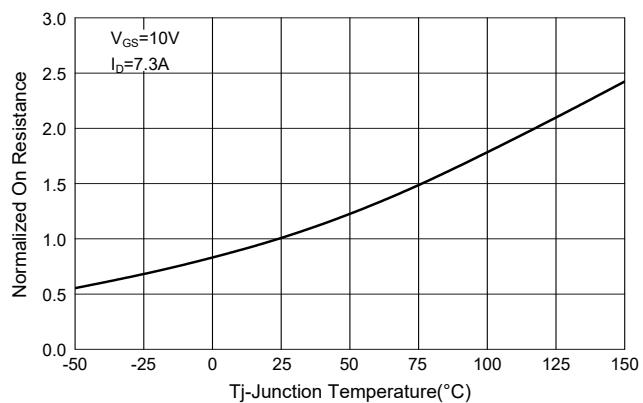


## Curve Characteristics

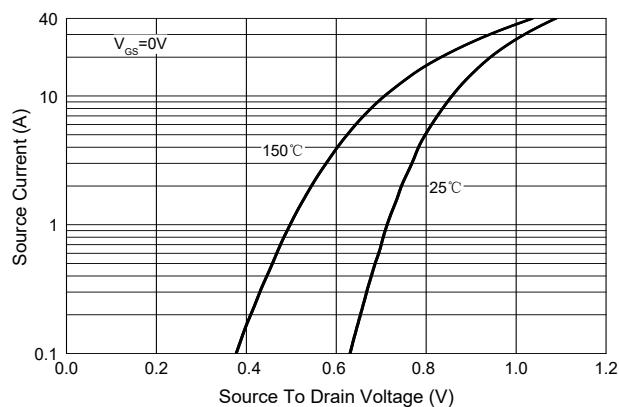
**Fig.7 - Normalized Threshold Voltage**



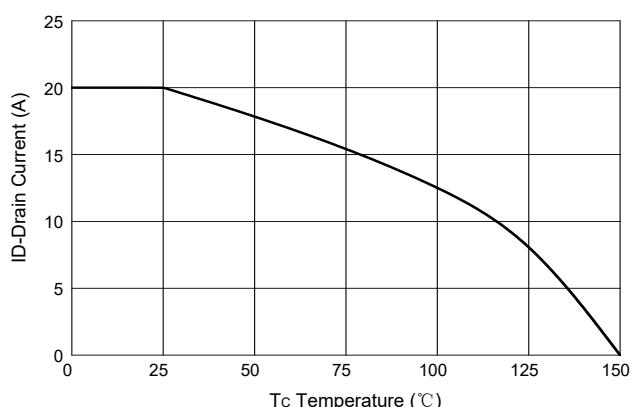
**Fig.8 - Normalized On Resistance Characteristics**



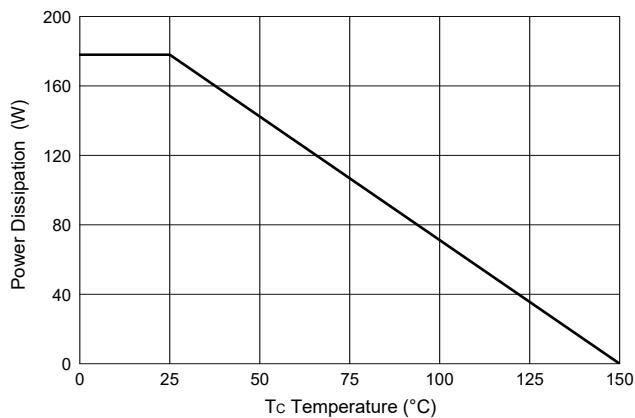
**Fig.9 - I<sub>S</sub> - V<sub>SD</sub>**



**Fig.10 - Drain Current**



**Fig.11 - PD Dissipation**



## Curve Characteristics

Fig. 12 - Safe Operation Area

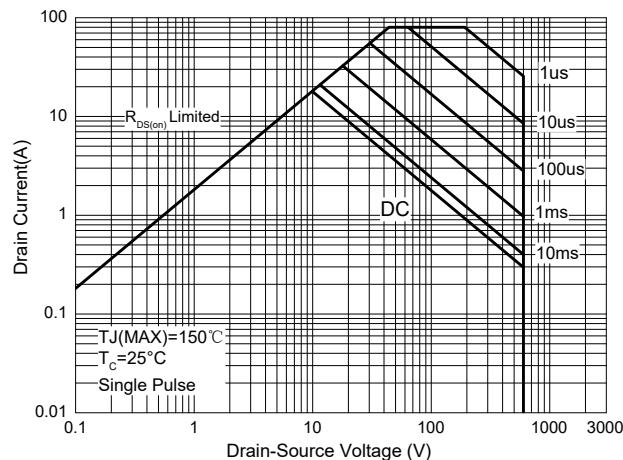
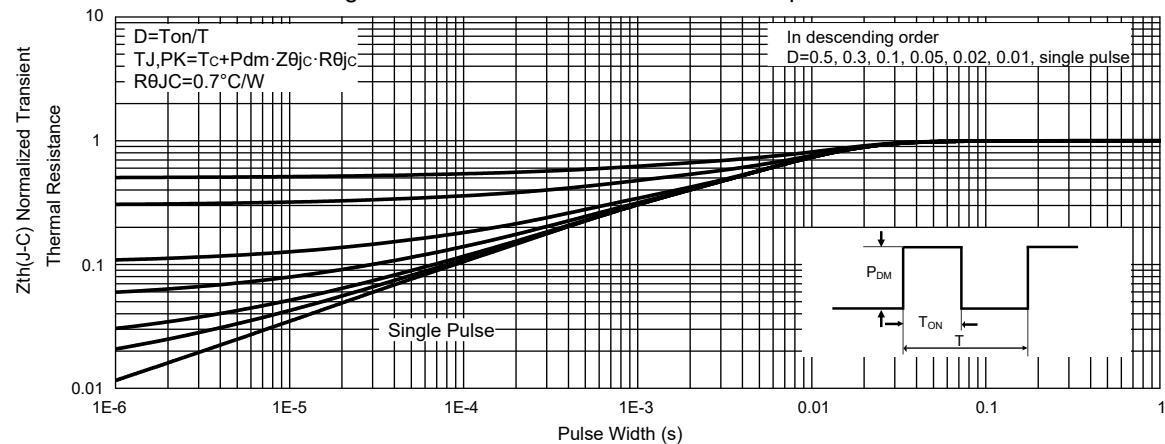


Fig. 13 -Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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