

E-Series Automotive 650 V, 40 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Part Number	Package	Marking
E6D40065H	TO-247-2	E6D40065H

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Automotive Qualified (AEC Q101) and PPAP Capable

Applications

- Interleaved or Bridgless PFC
- DC/DC On Board Battery Chargers
- Boost for PFC & DC-DC Stages
- AC/DC On Board Chargers
- PFC Output Rectification

Maximum Ratings ($T_c = 25^{\circ}$ C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes
Repetitive Peak Reverse Voltage	V _{RRM}	650			
Surge Peak Reverse Voltage	V _{RSM}	650	V		
DC Blocking Voltage	V _{DC}	650			
		109		T _c = 25 °C	
Continuous Forward Current	I _F	54		T _c = 125 °C	Fig. 3
		33	Α	T _c = 150 °C	
Repetitive Peak Forward Surge		144		T _c = 25 °C, t _p = 10 ms, Half Sine Wave	
Current	FRM	83		$T_c = 110 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave	
Non-Repetitive Forward Surge		240		T _c = 25 °C, t _p = 10 ms, Half Sine Wave	
Current	FSM	191	Α	$T_c = 110 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave	
B		273	,,,	T _c = 25 °C	<u>-</u>
Power Dissipation	P _{tot}	118	W	T _c = 110 °C	Fig. 4
21	6 2.41	288	A2-	$T_{c} = 25 {}^{\circ}\text{C}, t_{p} = 10 \text{ms}$	
i²t value	∫i²dt	182	A ² s	$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \text{ms}$	

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Famous ad Malda as	V	1.35	1.5		I _F = 40 A, T _j = 25 °C	F:_ 1
Forward Voltage	V _F	1.47	1.6	V	$I_F = 40 \text{ A}, T_j = 175 ^{\circ}\text{C}$	Fig. 1
Reverse Current		10	150	μА	$V_R = 650 \text{ V}, T_j = 25 \text{ °C}$	Fig. 2
	I _R	50	500		V _R = 650 V, T _j = 175 °C	
Total Capacitive Charge	Q _c	136		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}$	Fig. 5
		2460			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	С	262		pF	$V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		203			$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	20.5		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	$R_{\theta, JC (TYP)}$	0.38	°C/W	
Thermal Resistance, Junction to Case (Max)	R _{0, JC (MAX)}	0.55	°C/W	
Junction Temperature	T _j	-55 to +175	0.5	
Case & Storage Temperature	T _c	-55 to +175	°C	
	-	1	Nm	M3 Screw
TO-247 Mounting Torque		8.8	lbf-in	6-32 Screw

Typical Performance

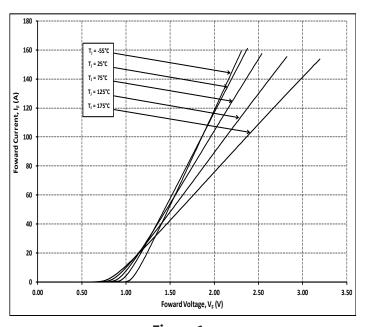
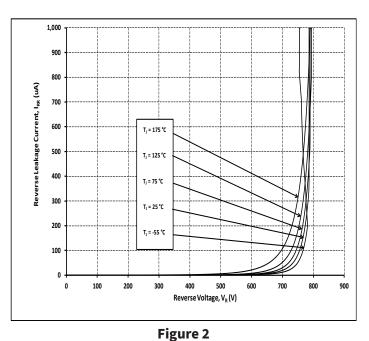


Figure 1Forward Characteristics



Reverse Characteristics

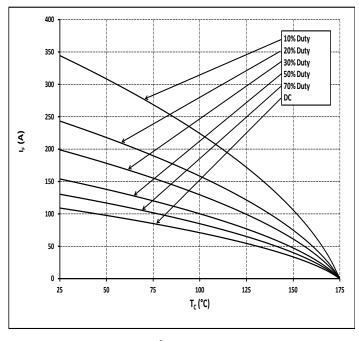


Figure 3Current Derating

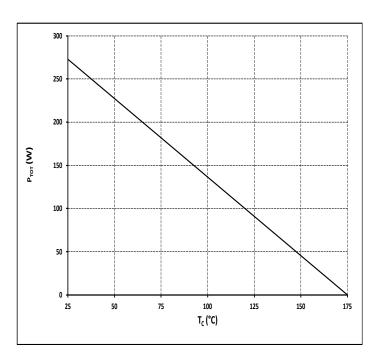


Figure 4Power Derating

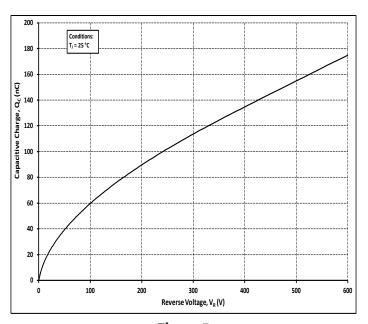


Figure 5Total Capacitance vs. Reverse Voltage

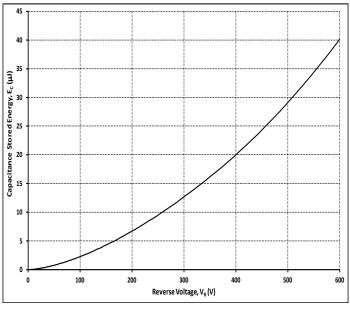


Figure 7Capacitance Stored Energy

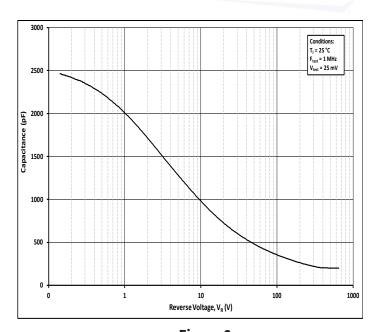


Figure 6Capacitace vs. Reverse Voltage

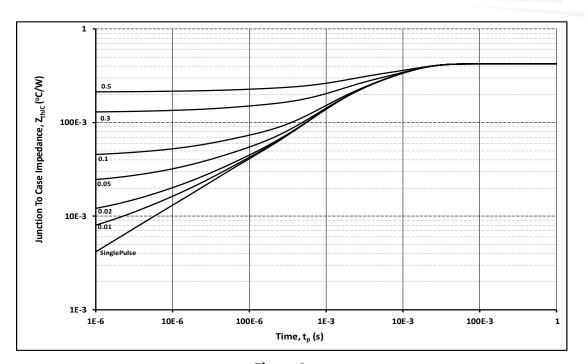
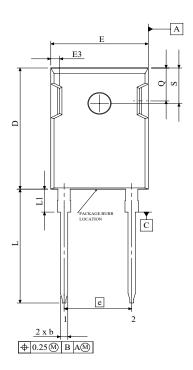
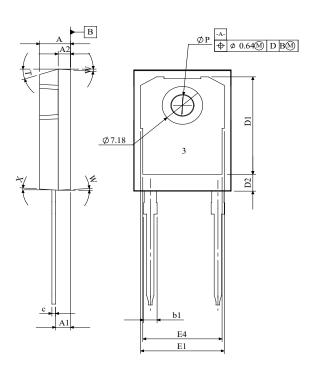


Figure 8Transient Thermal Impedance

Package Dimensions & Pin-Out

Package: TO-247-2





SYMBOL	MIN (mm)	MAX (mm)	
A	4.86	5.21	
A1	2.29	2.54	
A2	1.91	2.16	
ь	1.07	1.33	
b1	1.91	2.41	
b2	1.91	2.16	
с	0.55	0.68	
D	20.80	21.10	
D1	16.25	17.35	
D2	2.86	3.16	
E	15.75	16.13	
E1	13.10	14.15	
E2	3.68	5.10	
E3	1.00	1.90	
E4	12.38	13.43	
e	10.88 BSC		
L	19.81	20.32	
L1	4.10	4.40	
ØΡ	3.51	3.65	
Q	5.49	6.00	
S	6.04	6.30	
T	17.5 ° REF		
W	3.5° REF		
X	4° REF		

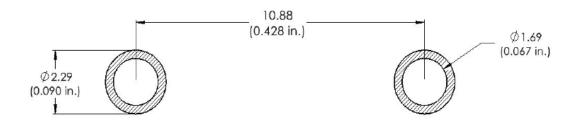
1	CATHODE	
2	ANODE	
3	CATHODE	

NOTE

- $1. \ \, \text{ALL METAL SURFACES ARE TIN PLATED (MATTE),} \\ \ \, \text{EXCEPT AREA OF CUT.}$
- 2. DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994.
- 3. ALL DIMENSIONS ARE LISTED IN MILLIMETERS. ANGLES ARE IN DEGREES.
- 4. PACKAGE BURR FLASH SIZE (0.5 mm) IS NOT INCLUDED IN THE DIMENSIONS

Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
E6D40065H	Tube

Revision History

Document Version	Date of Release	Description of Changes
1	January 2024	Initial Release

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