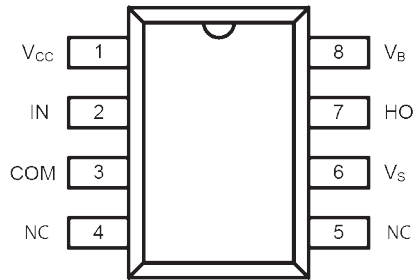
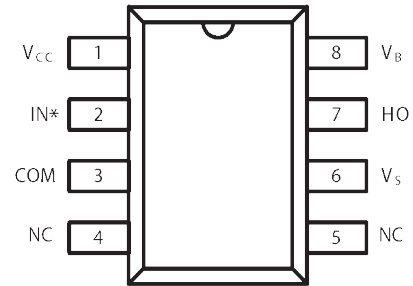


Pin Diagrams



Top View: PDIP-8, SOIC-8

TF2117



Top View: PDIP-8, SOIC-8

TF2118

Pin Descriptions

PIN NAME	PIN DESCRIPTION
VCC	Logic and gate drive supply
IN	TF2117 Logic input for gate driver output (HO), in phase with HO
IN*	TF2118 Logic input for gate driver output (HO), out of phase with HO
COM	Logic ground
NC	No Connect
V_S	High-side floating supply return
HO	High-side gate drive output
V_B	High-side floating supply

Revision History

Rev.	Change	Owner	Date
1.0	First release datasheet	D. Walton	12/11/2022

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