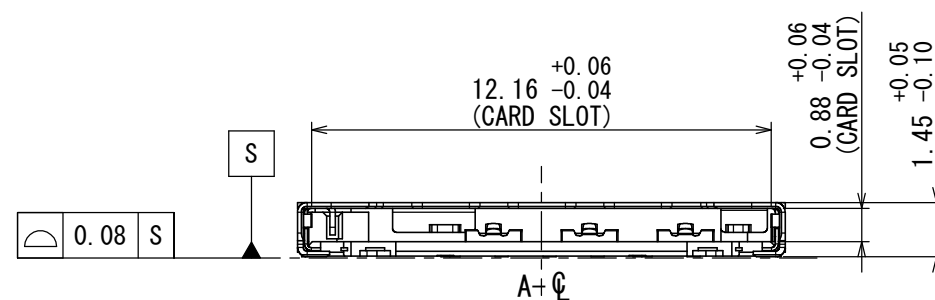
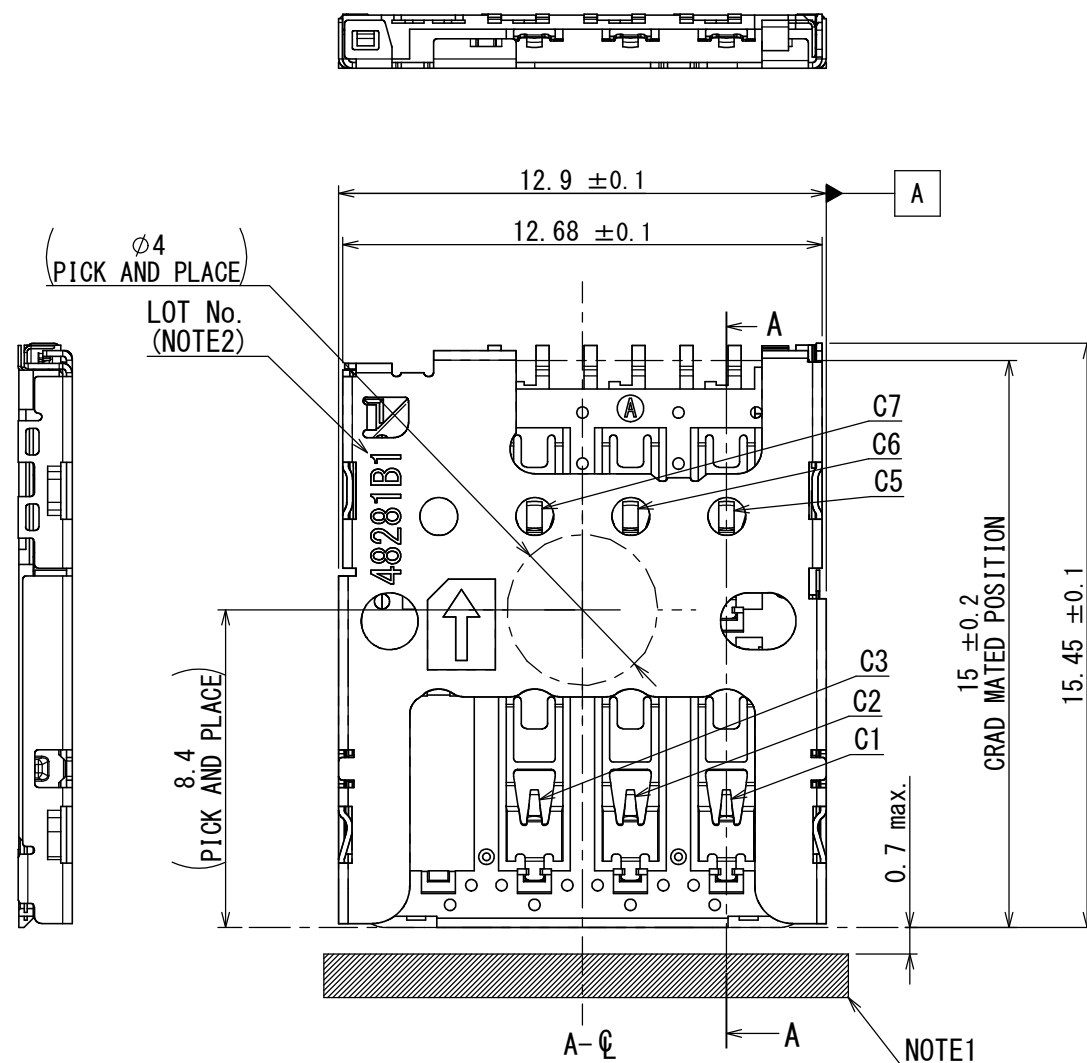
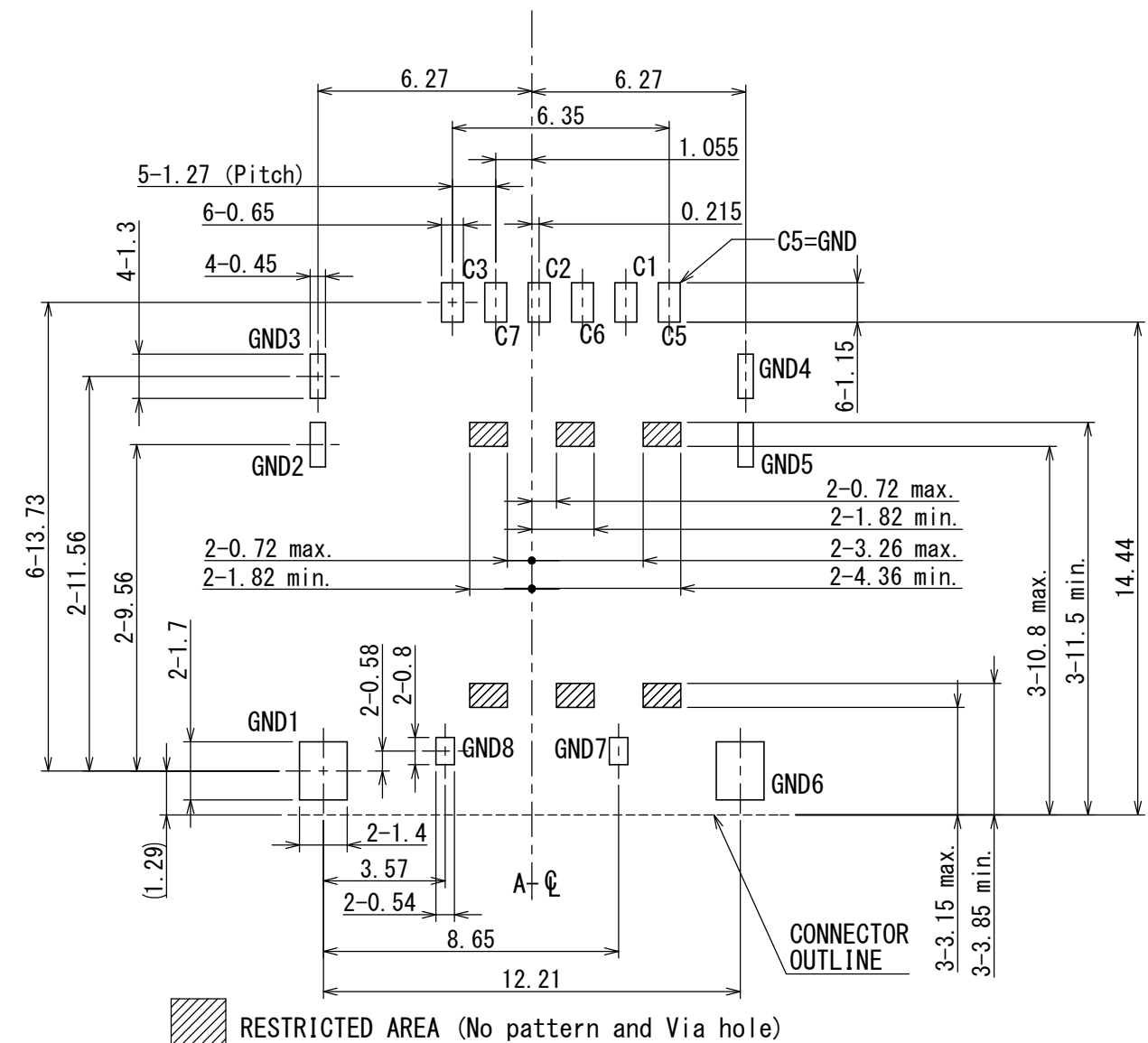
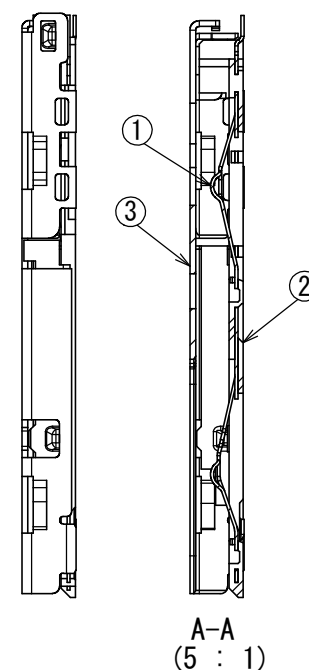


版数 VER.	年月日 D A T E	CN NO.	変 更 内 容 DESCRIPTION	製 図 DR.	担 当 CHK.	査 閲 APPD.	承 認 APPD.
2	29/JAN/2015	011160	Version No. added		K. MIKAWA	M. SHIMADA	A. NATORI



NOTE1. CARD STOPPER POSITION
NOTE2. LOT INFORMATION IS SHOWN AS BELOW.
i. e. 4 8 2 8 1 B 1

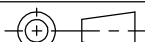
VERSION NO. (B1 or V*) $\triangle 2$
PRODUCTION LINE NO.
PRODUCT DATE (YMDD)



All patterns and Via holes under the connector should be coated by resist.

Recommended P. C. B Layout

3	COVER	1	STAINLESS	TERMINAL AREA :Au OVER Ni	-
2	INSULATOR	1	GLASS FILLED LCP	-	-
1	CONTACT	6	COPPER ALLOY	CONTACT AREA :Au OVER Ni TERMINAL AREA :Au OVER Ni	-
符号 NO.	名 称 DESCRIPTION	個 数 QTY	材 料 MATERIAL	仕 上 FINISH	備 考 REMARKS

NO.		DESCRIPTION	QTY.	MATERIAL	FINISH	REMARKS
仕様書 (SPECIFICATION)		第1版 (ORIGINAL DATE)		尺度 (SCALE)	シリーズ (SERIES)	日本航空電子工業株式会社  JAPAN AVIATION ELECTRONICS INDUSTRY, LTD.
JACS-11066*		06/OCT/2014		5:1	SF53	
一般公差 (GENERAL TOLERANCE)		製図 DR.		名称 (TITLE)		
寸法 (DIMENSION)		担当 CHK.	K. MIKAWA	SF53S006VCB		
角度 (ANGLES)		査閲 APPD.	M. SHIMADA			図面番号 (DRAWING NO.) SJ115712
±0.8		承認 APPD.	A. NATORI			
×° ±						
× × ±0.4				質量 (MASS)		版数 (VER.) 2
× × × ±0.1				-		
× × × × ±		単位 (UNIT) : mm				