

# High Frequency Ceramic Solutions

## 2.45 GHz Low Pass Filter

P/N 2450LP15B050

Detail Specification: 05/04/2011

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### General Specifications

|                    |                |
|--------------------|----------------|
| Part Number        | 2450LP15B050   |
| Frequency (MHz)    | 2400 - 2500    |
| Attenuation (min.) | 32 dB @ 2 x fo |
| Attenuation (min.) | 30 dB @ 3 x fo |
| Attenuation (min.) | 30 dB @ 4 x fo |

|                       |              |
|-----------------------|--------------|
| Insertion Loss        | 0.50 dB max. |
| Return Loss           | 10.9 dB min. |
| Input Power           | 1 Watt max.  |
| Operating Temperature | -40 to +85°C |

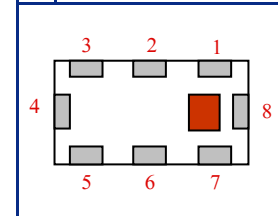
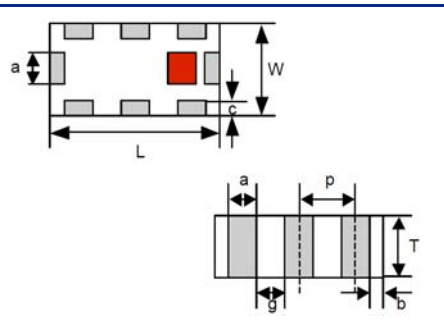
|               |                   |            |               |                             |
|---------------|-------------------|------------|---------------|-----------------------------|
| P/N<br>Suffix | Packaging Style   | Bulk       | Suffix = S    | Eg. 2450LP15B050            |
|               |                   | T & R      | Suffix = E    | Eg. 2450LP15B050            |
|               | Termination Style | 100% Tin   | Suffix = None | Eg. 2450LP15B050(E or S)    |
|               |                   | Tin / Lead | Suffix = /Pb  | Eg. 2450LP15B050(E or S)/Pb |

### Terminal Configuration

| No. | Function |
|-----|----------|
| 1   | GND      |
| 2   | NC       |
| 3   | GND      |
| 4   | I/O*     |
| 5   | GND      |
| 6   | NC       |
| 7   | GND      |
| 8   | I/O*     |

### Mechanical Dimensions

|   | In                 | mm             |
|---|--------------------|----------------|
| L | 0.079 ± 0.004      | 2.00 ± 0.10    |
| W | 0.049 ± 0.004      | 1.25 ± 0.10    |
| T | 0.037 ± 0.004      | 0.95 ± 0.10    |
| a | 0.012 ± 0.004      | 0.30 ± 0.10    |
| b | 0.008 ± 0.004      | 0.20 ± 0.10    |
| c | 0.012 +.004/-0.008 | 0.30 +0.1/-0.2 |
| g | 0.014 ± 0.004      | 0.35 ± 0.10    |
| p | 0.026 ± 0.002      | 0.65 ± 0.05    |



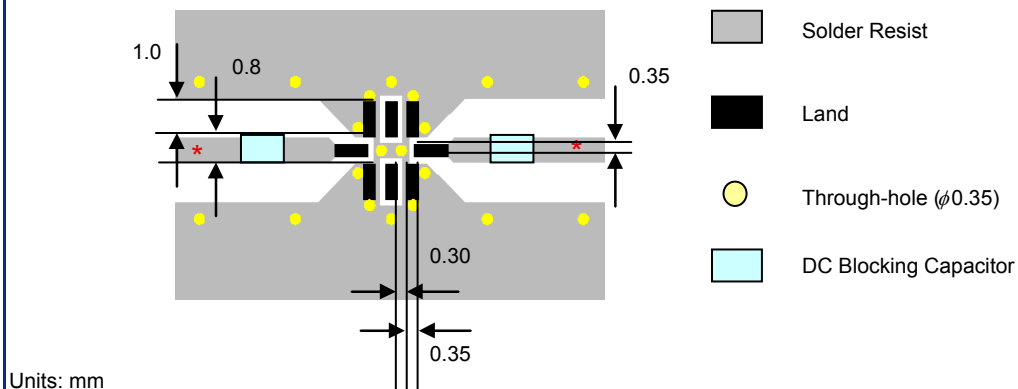
\*Pins 4 and 8 interchangeable

### Mounting Considerations

Mount these devices with brown mark facing up.

\* Line width should be designed to provide 50Ω impedance matching characteristics.

\* DC Blocking capacitor should be connected in series at each In/Out Port if DC blocking desired



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# "High Frequency Ceramic Solutions"

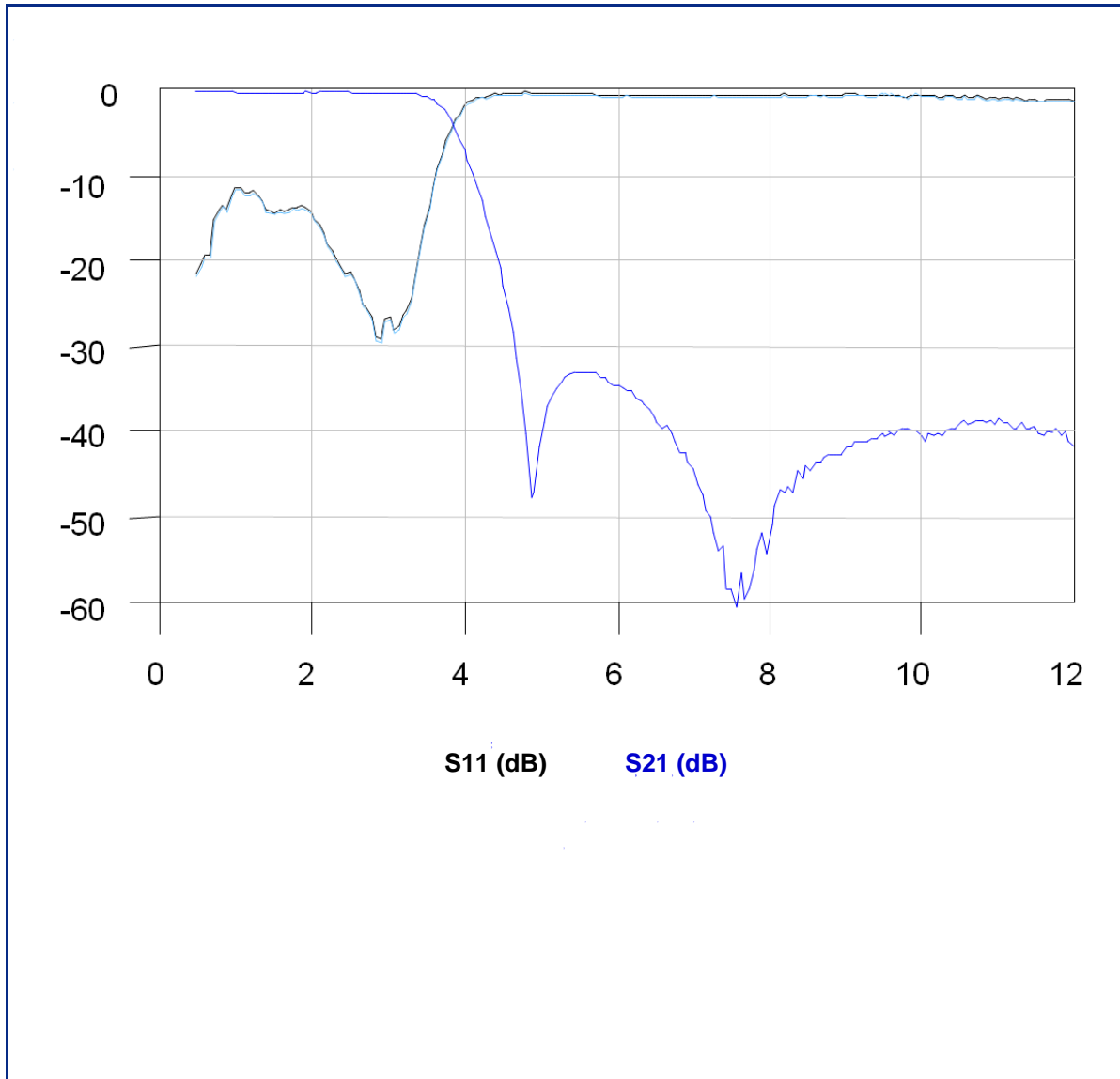
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Typical Electrical Performance (T=25°C)



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