TOSHIBA

TOSHIBA Photocoupler IRED & Photo-IC

TLP557

Industrial Inverter Inverter for Air Conditioner Power Transistor Base Drive

The TOSHIBA TLP557 consists of an infrared emitting diode and an integrated photodetector. This unit is 8-lead DIP package. TLP557 is suitable for base driving circuit of power transistor module up to

20A. External resistor needs to connect between pin 6 and pin 7. This is for constant current driving.

- Input threshold current: IF = 5 mA (max)
- Guaranteed performance temperature range: -30 to 70°C
- Supply voltage: 16 V (max)
- Output current: ±0.3 A (max)
- Switching time (tpLH/tpHL): 5 µs (max)
- Isolation voltage: 2500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349

Schematic



Truth Table

		Tr1	Tr2
Input LED	ON	ON	OFF
	OFF	OFF	ON



Weight: 0.54 g (typ.)

Pin Configuration (top view)



- 1 : N.C.
- 2 : Anode
- 3 : Cathode
- 4 : N.C. 5 : GND
- 6 : V_{O2}(Output)
- 7 : V_{O1}(Rex Terminal)
- 8 : V_{CC}

Start of commercial production 1987-06

Absolute Maximum Ratings

	Characteristic	Symbol	Rating	Unit	
	Forward current	lF	25	mA	
	Peak transient forward current (Pw ≤ 1 µs, 300 pps)	IFPT	1	А	
LED	Reverse voltage	VR	5	V	
	Diode power dissipation	PD	45	mW	
	Junction temperature	Tj	125))∕ °c	
	Output current (f \leq 5kHz, Duty \leq 50%)	lo	+0.32/-0.32	A	
	Peak output current (P _W \leq 10µs, f \leq 5kHz)	IOP	+2/-0.5	А	
	Output voltage	Vo	16	V	
Detector	Supply voltage	Vcc	16	V	
	O_1 terminal to O_2 terminal (pin 7-pin 6) voltage	V1-2	1.5	V	
	O2 terminal to O1 terminal (pin 6-pin 7) voltage	V2-1	5	V	
	Power dissipation	Po	0.5	\geq	
	Power dissipation derating (Ta > 50 °C)	ΔΡο/ΔΤα	-6.7	mW/°C	
	Junction temperature		125	e e	
Total package power dissipation		Pot	Рот 0.55		
Total	package power dissipation derating (Ta > 50 °C)	ΔΡΟΤ/ΔΤα	ΔΡΟΤ/ΔΤα -7.4		
Oper	ating temperature range	Topr	Topr -30 to 70		
Stora	ige temperature range	Tstg	-55 to 125	°C	
Lead	solder temperature (10 s)	Tsol	260	°C	
Isolat	tion voltage (AC, 60 s, R.H.≤ 60 %, Ta=25°C) (Note 1)	BVs	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal device: Pins 1, 2, 3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Input current on	I _F (ON)	7	8	20	mA
Input voltage off	V _F (OFF)	0	_	0.8	V
Supply voltage	Vcc	5	6	13	V
IB1 Drive current	lo1	—	0.15	0.25	А
IB2 Drive current	l _{O2}	—	_ \	0.5	А
External resistance	Rex	2.7	4.3		Ω
V _{CC} -V _{O2} (pin 8-pin 6) ON voltage (Note 1)	V ₈₋₆	2.3	3 (lo1 = 0.15A)	2.5 (I _{O1} = 0.25A)	V
Operating temperature	Topr	-30	(25)	70	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



Electrical Characteristics (Ta = -30 to 70°C, unless otherwise specified)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input forward voltage	VF	_	I _F = 5 mA , Ta = 25 °C	—	1.55	1.7	V
Temperature coefficient of forward voltage	∆V _F /∆Ta	_	I _F = 5 mA	- <	-2.0	_	mV/°C
Input reverse current	I _R		V _R = 5 V, Ta = 25 °C	_	\geq	10	μA
Input capacitance	CT		V = 0 V, f = 1 MHz , Ta = 25 °C	_ ((-)	250	pF
O1 Output leakage current	I _{O1L}	1	V _{CC} = 16 V, V _{O1} = 0 V, V _F = 0.8 V	76	0.01	200	μA
O2 Output leakage current	I _{O2L}	2	V_{CC} = 16 V, V_{O2} = 16 V, I _F = 5 mA		0.2	200	μA
		3	V ₈₋₆ = 2.3 V V _{CC} = 6 V	0.22	0.27	0.32	A
O ₁ Output current	IO		Rex = 2.7 Ω $I_F = 5$ mA, Ta = 25 °C $V_{CC} = 16$	0.22	0.27	0.32	
O ₂ High level output voltage	V _{OH}	4	$V_{CC} = 6 V$, Rex = 2.7 Ω I _F = 5 mA	3.5	5.5	\sim	V
		5	$V_{\rm F} = 0.8 \text{V}, \text{Rex} = 2.7 \Omega$ $V_{\rm CC} = 6 \text{V}$		0.2	0.4	
O ₂ Low level output voltage	V _{OL}		I _O = 0.25 A, Ta = 25 °C		0.2	0.4	V
			$V_{F} = 0.8 \text{ V}, \text{ Rex} = 2.7 \Omega$ $V_{CC} = 6 \text{ V}$ $I_{O} = 0.5 \text{ A} (\text{Note 1})$ $Ta = 25 ^{\circ}\text{C}$ $V_{CC} = 16 \text{ V}$		0.4	(\mathcal{A})	
				-6	0.4	<u> </u>	V
	Іссн		V _{CC} = 6 V, I _F = 5 mA Rex = 2.7 Ω, Ta = 25 °C		3.8	10	
High level supply current			V_{CC} = 6 V, I _F = 5 mA, Rex = 2.7 Ω	$(0/\uparrow)$	_	13	mA
			$V_{CC} = 16 \text{ V}, \text{ I}_{\text{F}} = 5 \text{ mA}, \text{ Rex} = 2.7 \Omega$		5.2	17	
	ICCL	_	V _{CC} = 6 V, IF = 0 mA Rex = 2.7 Ω, Ta = 25 °C))-	11	17	mA
Low level supply current			$V_{CC} = 6 V$, $I_F = 0 mA$, Rex = 2.7 Ω	V –	—	22	
			V_{CC} = 16 V, I _F = 0 mA, Rex = 2.7 Ω	_	13	25	
"Output L \rightarrow H" threshold	IFLH	6	$\operatorname{Rex} = 2.7 \Omega \qquad $	—	2.5	5	
input current		77^	V _{O2} > 3 V V _{CC} = 16 V	—	—	5	mA
"Output H→L" threshold input current	VFHL	\bigcirc	Rex = 2.7Ω V _{CC} = 6 V	0.8	_	—	V
		\sim	IO = 0.25A VO2 < 0.4V	0.8	_	_	
Input current hysteresis	Invs	_	Vcc = 6 V, Rex = 2.7 Ω, Ta = 25 °C	—	0.05	_	mA
Supply voltage	Vcc	_	-	5	—	16	V
Capacitance (input-output)	Cs	_	V _S = 0 V, f = 1 MHz, Ta = 25 °C	_	1.0	2.0	pF
Resistance (input-output)	2 Rs	— (Vs = 500 V , Ta = 25 °C, R.H.≤ 60 %	5×10 ¹⁰	10 ¹²	—	Ω

Note: All typical values are at Ta = 25 °C

Note 1: Duration of IO time ≤ 100 µs

Switching Characteristics (Ta = -30 to 70°C unless otherwise specified)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Propagation delay time, $L \rightarrow H$	t _{pLH}			—	1	5	μs
Propagation delay time, $H \rightarrow L$	t _{pHL}	6	$V_{CC} = 6 V, I_F = 8 mA$	—	1	5	μs
Output rise time	tr		0	f = 5 kHz, Duty = 10 %	$\langle \rangle$	0.05	
Output fall time	tf			- Â	0.05	-	μs
Common mode transient immunity at high level output	СМн	7	V_{CM} = 600 V, I _F = 8 mA V _{CC} = 6 V, Rex = 270 Ω R = 1 kΩ, Ta = 25 °C	-2000	\mathcal{Y}		V/µs
Common mode transient immunity at low level output	CML	7	$V_{CM} = 600 \text{ V}, \text{ I}_{\text{F}} = 0 \text{ mA}$ $V_{CC} = 6 \text{ V}, \text{ Rex} = 270 \Omega$ $\text{R} = 1 \text{ k}\Omega, \text{ Ta} = 25 ^{\circ}\text{C}$	2000	_	_	V/µs

Note: All typical values are at Ta = 25 °C.



Note: CM_L (CM_H) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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