IGBT

TRENCHSTOP™ IGBT4 Low Power Chip

IGC18T120T8L

Data Sheet

Industrial Power Control

Table of Contents

IGC18T120T8L

TRENCHSTOP™ IGBT4 Low Power Chip

Features:

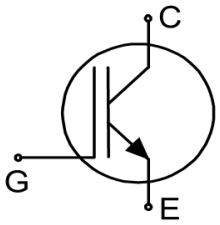
- 1200V trench & field stop technology
- Low switching losses
- Positive temperature coefficient
- Easy paralleling

Recommended for:

- Low / medium power modules

Applications:

- Low / medium power drives



Chip Type	V_{CE}	I_{Cn} [1]	Die Size	Package
IGC18T120T8L	1200V	15A	4.16mm x 4.34mm	Sawn on foil

Mechanical Parameters

Die size		4.16 x 4.34	mm^2
Emitter pad size		See chip drawing	
Gate pad size		1.185 x 0.702	
Area total		18.05	
Thickness		115	µm
Wafer size		200	mm
Maximum possible chips per wafer		1510	
Passivation frontside		Photoimide	
Pad metal		3200nm AlSiCu	
Backside metal		Ni Ag – system To achieve a reliable solder connection it is strongly recommended not to consume the Ni layer completely during production process	
Die bond		Electrically conductive epoxy glue and soft solder	
Wire bond		Al, ≤500µm	
Reject ink dot size		∅ 0.65mm; max. 1.2mm	
Storage environment	for original and sealed MBB bags	Ambient atmosphere air, temperature 17°C – 25°C, <6 months	
	for open MBB bags	Acc. to IEC62258-3: atmosphere >99% Nitrogen or inert gas, humidity <25%RH, temperature 17°C – 25°C, <6 months	

[1] Nominal collector current at T_C=100°C for chip packaged in power modules, see application example cited on page 5.

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T_{vj}=25°C	V_{CE}	1200	V
DC collector current, limited by $T_{vj\,max}$ [2]	I_C	-	A
Pulsed collector current, t_p limited by $T_{vj\,max}$ [3]	$I_{C,puls}$	45	A
Gate-emitter voltage	V_{GE}	±20	V
Operating junction temperature	T_{vj}	-40 ... +175	°C
Short circuit data [3] / [4] V_{GE}=15V, V_{CC}=800V, T_{vj}=150°C	t_{sc}	10	µs

Static Characteristics (tested on wafer), T_{vj}=25°C

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-emitter breakdown voltage	$V_{(BR)CES}$	V_{GE}=0V, I_C=0.5mA	1200	-	-	V
Collector-emitter saturation voltage	V_{CEsat}	V_{GE}=15V, I_C=15A	1.58	1.85	2.07	
Gate-emitter threshold voltage	$V_{GE(th)}$	I_C=0.5mA, V_{GE}=V_{CE}	5.3	5.8	6.3	
Zero gate voltage collector current	I_{CES}	V_{CE}=1200V, V_{GE}=0V	-	-	2	µA
Gate-emitter leakage current	I_{GES}	V_{CE}=0V, V_{GE}=20V	-	-	120	nA
Integrated gate resistor	r_G		none			Ω

Electrical Characteristics [3]

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-emitter saturation voltage	V_{CEsat}	V_{GE}=15V, I_C=15A, T_{vj}=150°C	-	2.25	-	V
Input capacitance	C_{ies}	V_{CE}=25V, V_{GE}=0V, f=1MHz T_{vj}=25°C	-	890	-	pF
Reverse transfer capacitance	C_{res}		-	30	-	

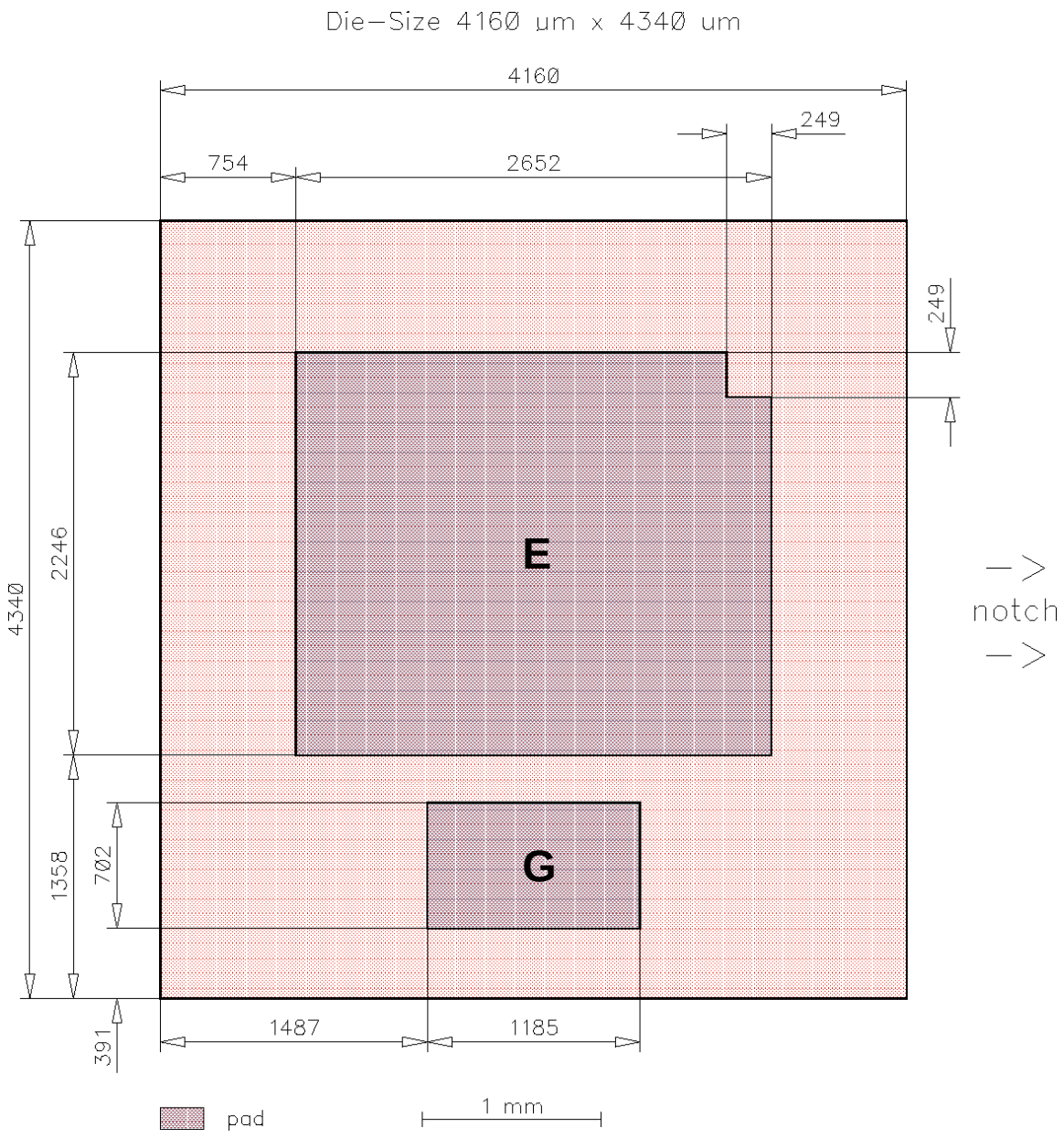
[2] Depending on thermal properties of assembly.
[3] Not subject to production test - verified by design/characterization.
[4] Allowed number of short circuits: <1000; time between short circuits: >1s.

Further Electrical Characteristics

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

Application example	FP15R12W1T4_B3	Rev. 2.3

Chip Drawing



E = Emitter
G = Gate

Bare Die Product Specifics
Test coverage at wafer level cannot cover all application conditions. Therefore it is recommended to test all characteristics which are relevant for the application at package level, including RBSOA and SCSOA.

Description

AQL 0.65 for visual inspection according to failure catalogue
Electrostatic Discharge Sensitive Device according to MIL-STD 883

Revision History

Revision	Subjects (major changes since last revision)	Date
2.0	Final data sheet	18.02.2015
2.1	Update disclaimer	20.08.2015

Relevant Application Notes

Published by
Infineon Technologies AG
81726 München, Germany

IMPORTANT NOTICE

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie"). With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office (**www.infineon.com**).

Please note that this product is not qualified according to the AEC Q100 or AEC Q101 documents of the Automotive Electronics Council.

WARNINGS

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.

w w w . i n f i n e o n . c o m

Published by Infineon Technologies AG