

May 2018

GENERAL DESCRIPTION

The SP330 is an advanced multiprotocol transceiver supporting RS-232, RS-485, and RS-422 serial standards and featuring a variable low voltage logic interface, down to 1.65V. Full operation requires only four external charge pump capacitors.

The RS-485/RS-232 mode pin selects RS-485 mode when high, and <u>RS-232</u> mode when low. In RS-485 mode the HALF/FULL pin configures the transceiver as either half or full duplex.

The high speed drivers operate up to 20Mbps in RS-485/422 modes, and up to 1Mbps in RS-232 mode. All drivers can be slew limited to 250kbps in any mode to minimize electromagnetic interference (EMI) by setting the dedicated SLEW pin low.

All transmitter outputs and receiver inputs feature robust electrostatic discharge (ESD) protection to ± 15 kV IEC 61000-4-2 Airgap, ± 15 kV Human Body Model (HBM) and ± 8 kV IEC 61000-4-2 Contact. Each receiver output has full fail-safe protection to avoid system lockup, oscillation, or indeterminate states by defaulting to logic-high output level when the inputs are open, shorted, or terminated but undriven. No external biasing resistors are required.

The RS-232 receiver inputs include a $5k\Omega$ pull-down to ground when in RS-232 mode. The RS-485/422 receiver inputs are high impedance (>96k Ω), allowing up to 256 devices on a single communication bus (1/8th unit load).

The SP330 operates from a single power supply, either 3.3V or 5V, with low idle current. The shutdown mode consumes less than 1μ A in low power standby operation with RS-232 receivers enabled.

FEATURES

- Robust ESD Protection:
 - ±15kV IEC 61000-4-2 Air Gap Discharge

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- ± 8kV IEC 61000-4-2 Contact Discharge
- ±15kV Human Body Model (HBM)
- 20Mbps RS-485 and 1Mbps RS-232 Data Rates
- Pin-Selectable 250kbps Slew Limiting
- Single Supply Operation from +3V to +5.5V
- 1.65V to 5.5V Logic Interface V_L pin
- 2 Drivers, 2 Receivers RS-232/V.28
- 1 Driver, 1 Receiver RS-485/422
 - Full or Half Duplex Configuration
 - 1/8th Unit Load, up to 256 receivers on bus
- RS-485/422 Enhanced Receiver Fail-safe for open, shorted, or terminated but idle inputs
- 10nA Shutdown Supply Current (typical)
- Small 24 TSSOP package

TYPICAL APPLICATIONS

- Software Programmable Serial Ports (RS-232, RS-422, RS-485)
- Industrial and Single Board Computers
- Industrial and Process Control Equipment
- Point-Of-Sale Equipment
- HVAC Controls and Networking Equipment
- Building Security and Automation

PART NUMBER	OPERATING TEMPERATURE RANGE	LEAD-FREE	PACKAGE	PACKAGING METHOD		
SP330EEY-L	-40°C to +85°C	Yes ⁽²⁾	24-pin TSSOP	Tube		
SP330EEY-L/TR		ies. /	24-911 10001	Tape and Reel		
SP330EEY-0A-EB	SP330E Evaluation Board					

ORDERING INFORMATION⁽¹⁾

Notes:

- 1. Refer to www.exar.com/SP330E for most up-to-date Ordering Information.
- 2. Visit www.exar.com for additional information on Environmental Rating.

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RS-232/RS-485/RS-422 TRANSCEIVER WITH 1.65V-5.5V INTERFACE

ABSOLUTE MAXIMUM RATINGS

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections to the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability and cause permanent damage to the device.

Supply Voltage V _{CC}	-0.3V to +6.0V		
Logic Interface Voltage V _L	$V_L \le V_{CC}$		
Voltage at TTL Input Pins	-0.3V to +6.0V		
Receiver Input Voltage (from Ground)	±18V		
Driver Output Voltage (from Ground)	±18V		
Short Circuit Duration, TX out to Ground	Continuous		
Storage Temperature Range	-65°C to +150°C		
Lead Temperature (soldering, 10s)	+300°C		
Power Dissipation 24-pin TSSOP (derate 26.0mW/°C above +70°C)	900mW		

CAUTION:

ESD (ElectroStatic Discharge) sensitive device. Permanent damage may occur on unconnected devices subject to high energy electrostatic fields. Unused devices must be stored in conductive foam or shunts. Personnel should be properly grounded prior to handling this device. The protective foam should be discharged to the destination socket before devices are removed.

ESD RATINGS

HBM - Human Body Model (TX Output & RX Input Pins)	±15kV
HBM - Human Body Model (all other pins)	± 3kV
IEC 61000-4-2 Airgap Discharge (TX Output & RX Input Pins)	±15kV
IEC 61000-4-2 Contact Discharge (TX Output & RX Input Pins)	± 8kV



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ELECTRICAL CHARACTERISTICS

UNLESS OTHERWISE NOTED:

 V_{CC} = +3.0V to +5.5V, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_L = V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	PARAMETERS	Min.	Typ.	MAX.	UNITS	CONDITIONS			
DC CHARAC	DC CHARACTERISTICS								
Icc	Supply Current (RS-232)		1	2.5	mA	No load, Idle inputs, RS-485/RS-232 = 0V			
I _{CC}	Supply Current (RS-485/422)		1.8	4.5	mA	No load, Idle inputs, RS-485/RS-232 = V _{CC}			
I _{CC}	Vcc Shutdown Current		0.01	1	μΑ	SHDN = 0V, Receiver inputs open or grounded			
TRANSMITT	TRANSMITTER and LOGIC INPUTS (PINS 11 - 14 & 18 - 20)								
V _{IL}	Logic Input Voltage Low			$\frac{V_L}{3}$	V				
V _{IH}	Logic Input Voltage High	$\frac{2V_L}{3}$			V				
I _{INL}	Logic Input Leakage Current		±0.01	±1	μA				
I _{INPD}	Logic Input Pulldown Current		10	50	μA	RE pin 18, V _{IN} = V _L			
V _{HYS}	Logic Input Hysteresis		200		mV				
RS-232 and	RS-232 and RS-485/422 RECEIVER OUTPUTS (PINS 8 & 9)								
V _{OL}	Receiver Output Voltage Low			0.4	V	I _{OUT} = 1.5mA			
V _{OH}	Receiver Output Voltage High	V _L -0.6			V	I _{OUT} = -1.5mA			
I _{OSS}	Receiver Output Short Circuit Current		±20	±85	mA	$0 \le V_O \le V_L$			
I _{OZ}	Receiver Output Leakage Current		±0.05	±1	μA	$0 \le V_O \le V_{L,}$ Receivers disabled			



ELECTRICAL CHARACTERISTICS (Continued)

UNLESS OTHERWISE NOTED:

 V_{CC} = +3.0V to +5.5V, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX} . Typical values are at V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	PARAMETERS	MIN.	Typ.	MAX.	Units	Conditions
S-232 SIN	GLE-ENDED RECEIVER INPUTS (PI	NS 16 & 17))			
V _{IN}	Input Voltage Range	-15		+15	V	
V _{IL}	Input Threshold Low	0.6	1.2		V	V _{CC} = 3.3V
۹Ľ		0.8	1.5		V	V _{CC} = 5.0V
V _{IH}	Input Threshold High		1.5	2.0	V	V _{CC} = 3.3V
чн	Input Threshold High		1.8	2.4	V	V _{CC} = 5.0V
V _{HYS}	Input Hysteresis		0.5		V	
R _{IN}	Input Resistance	3	5	7	kΩ	$-15V \le V_{IN} \le +15V$
S-232 SIN	GLE-ENDED TRANSMITTER OUTPL	JTS (PINS 6	& 7)			
V _{OUT}	Output Voltage Swing	±5.0	±5.5		V	Outputs loaded with $3k\Omega$ to G
R _{OFF}	Output Power Off Impedance	300	10M		Ω	V_{CC} = 0V, V_{OUT} = ±2V
I _{SC}	Output Short Circuit Current		±30	±60	mA	V _{OUT} = 0V
Ι _Ο	Output Leakage Current			±125	μA	$\overline{\text{SHDN}} = 0\text{V}, \text{ V}_{\text{OUT}} = \pm 9\text{V},$ $\text{V}_{\text{CC}} = 0\text{V or } 5.5\text{V}$



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RS-232/RS-485/RS-422 TRANSCEIVER WITH 1.65V-5.5V INTERFACE

ELECTRICAL CHARACTERISTICS (Continued)

UNLESS OTHERWISE NOTED:

 V_{CC} = +3.0V to +5.5V, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	PARAMETERS	Min.	Typ.	Max.	UNITS	CONDITIONS		
RS-485/422 DIFFERENTIAL RECEIVER INPUTS (A,B)								
R _{IN}	Receiver Input Resistance	96			kΩ	$-7V \le V_{CM} \le +12V$		
I _{IN}	Receiver Input Current			125	μA	V _{IN} = +12V		
'IN				-100	μA	V _{IN} = -7V		
V _{TH}	Receiver Differential Threshold Voltage	-200	-125	-50	mV	$-7V \le V_{CM} \le +12V$		
ΔV_{TH}	Receiver Input Hysteresis		25		mV			
V _{OD}	Differential Driver Output	1.5 1.5		V _{CC} V _{CC}	V V	R _L = 54Ω (RS-485), Figure 4 -7V \leq V _{CM} \leq +12V, Figure 5		
RS-485/422	DIFFERENTIAL DRIVER OUTPUTS (Y,)	•		Vaa	V	$P_{c} = 540 (P_{S}, 485)$ Figure 4		
V OD		2			V V	$R_{L} = 100\Omega$ (RS-422), Figure 4		
		2		V _{CC}	V	$R_{L} = 10052 (RS-422), Figure 4$		
$\left \Delta V_{OD}\right $	Change In Magnitude of Differential Output Voltage			0.2	V	R _L = 54Ω or 100Ω, Figure 4		
V_{CM}	Driver Common Mode Output Voltage			3	V	$R_L = 54\Omega$ or 100 Ω , Figure 4		
$ \Delta V_{CM} $	Change In Magnitude of Common Mode Output Voltage			0.2	V	$R_L = 54\Omega$ or 100Ω, Figure 4		
I _{OSD}	Driver Output Short Circuit Current			±250	mA	-7V \leq V _Y or V _Z \leq +12V, Figure (
Ι _Ο	Driver Output Leakage Current			±125	μA	DE = 0V or \overline{SHDN} = 0V, V _Y or V _Z = -7V or +12V, V _{CC} = 0V or 5.5V		



TIMING CHARACTERISTICS

UNLESS OTHERWISE NOTED:

 V_{CC} = +3.0V to +5.5V, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	PARAMETERS	Min.	Түр.	MAX.	UNITS	CONDITIONS
ALL MODES						
t _{ENABLE}	Enable from Shutdown		1000		ns	
t _{SHUTDOWN}	Enable to Shutdown		1000		ns	
RS-232, DAT	A RATE = 250kbps (SLEW = 0V), ONE	TRANS	MITTER	SWITCH	ING	
	Maximum Data Rate	250			kbps	$R_L = 3k\Omega$, $C_L = 1000 pF$
t _{RHL} , t _{RLH}	Receiver Propagation Delay		100		ns	C _I = 150pF, Figure 7
t _{RHL} -t _{RLH}	Receiver Propagation Delay Skew			100	ns	
t _{DHL} , t _{DLH}	Driver Propagation Delay		1400		ns	R _L = 3kΩ, C _L = 2500pF,
t _{DHL} -t _{DLH}	Driver Propagation Delay Skew			600	ns	Figure 8
		1				
t _{SHL,} t _{SLH}	Transition Region Slew Rate from +3.0V to -3.0V or -3.0V to +3.0V	6		30	V/µs	V_{CC} = +3.3V, R _L = 3kΩ to 7kΩ, C _L = 150pF to 2500pF, T _A = 25°C, Figure 8
t _{SHL,} t _{SLH}	Transition Region Slew Rate from +3.0V to -3.0V or -3.0V to +3.0V	4		30	V/µs	V_{CC} = +3.3V, R _L = 3kΩ to 7kΩ, C _L = 150pF to 2500pF, Figure 8
RS-232, DAT	A RATE = 1Mbps (SLEW = V _{CC}), ONE	TRANS		витсн	ING	
	Maximum Data Rate	1			Mbps	$R_L = 3k\Omega$, $C_L = 250pF$
t _{RHL} , t _{RLH}	Receiver Propagation Delay		100		ns	
t _{RHL} -t _{RLH}	Receiver Propagation Delay Skew			100	ns	. C _L = 150pF, <mark>Figure 7</mark>
t _{DHL} , t _{DLH}	Driver Propagation Delay		300		ns	R _L = 3kΩ, C _L = 1000pF,
t _{DHL} -t _{DLH}	Driver Propagation Delay Skew			150	ns	Figure 8
	I					
t _{SHL,} t _{SLH}	Transition Region Slew Rate from +3.0V to -3.0V or -3.0V to +3.0V	13		150	V/µs	V_{CC} = +3.3V, R_L = 3k Ω to 7k Ω , C _L = 150pF to 1000pF, Figure 8
t _{SHL,} t _{SLH}	Transition Region Slew Rate from +3.0V to -3.0V or -3.0V to +3.0V	24		150	V/µs	V_{CC} = +3.3V, R _L = 3kΩ to 7kΩ, C _L = 150pF to 1000pF, T _A = 25°C, Figure 8



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RS-232/RS-485/RS-422 TRANSCEIVER WITH 1.65V-5.5V INTERFACE

TIMING CHARACTERISTICS (Continued)

UNLESS OTHERWISE NOTED: V_{CC} = +3.0V to +5.5V, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	Parameters	Min.	Typ.	MAX.	UNITS	CONDITIONS
RS-485/RS-42	2, DATA RATE = 250kbps (SLEW = 0	V), ONE 1	RANSM	ITTER S	witchi	NG
	Maximum Data Rate	250			kbps	$R_L = 54\Omega$, $C_L = 50pF$
t _{RPHL} , t _{RPLH}	Receiver Propagation Delay		50	150	ns	C _I = 15pF, Figure 9
t _{RPHL} -t _{RPLH}	Receiver Propagation Delay Skew			10	ns	C _L – Topr, rigule 9
t _{DPHL} , t _{DPLH}	Driver Propagation Delay		500	1000	ns	
t _{DPHL} -t _{DPLH}	Driver Propagation Delay Skew			100	ns	$R_L = 54\Omega$, $C_L = 50pF$, Figure 10
t _{DR,} t _{DF}	Driver Rise and Fall Time	300	650	1200	ns	
	1		T	T	T	I
t _{RZH} , t _{RZL}	Receiver Output Enable Time			200	ns	C _L = 15pF, Figure 11
t _{RHZ} , t _{RLZ}	Receiver Output Disable Time			200	ns	
t _{DZH} , t _{DZL}	Driver Output Enable Time			1000	ns	R _L = 500Ω, C _L = 50pF,
t _{DHZ} , t _{DLZ}	Driver Output Disable Time			200	ns	Figure 12
RS-485/RS-42	2, DATA RATE = 20Mbps (SLEW = V ₀	_{CC}), ONE	TRANS	AITTER \$	вwitch	ING
	Maximum Data Rate	20			Mbps	$R_L = 54\Omega$, $C_L = 50pF$
t _{RPHL} , t _{RPLH}	Receiver Propagation Delay		50	150	ns	C _L = 15pF, <mark>Figure 9</mark>
t _{RPHL} -t _{RPLH}	Receiver Propagation Delay Skew			10	ns	
t _{DPHL} , t _{DPLH}	Driver Propagation Delay		30	100	ns	
t _{DPHL} -t _{DPLH}	Driver Propagation Delay Skew			10	ns	$R_L = 54\Omega$, $C_L = 50$ pF, Figure 10
t _{DR,} t _{DF}	Driver Rise and Fall Time		10	20	ns	
t _{RZH} , t _{RZL}	Receiver Output Enable Time			200	ns	C ₁ = 15pF, Figure 11
t _{RHZ} , t _{RLZ}	Receiver Output Disable Time			200	ns	
t _{DZH} , t _{DZL}	Driver Output Enable Time			200	ns	R _L = 500Ω, C _L = 50pF,
t _{DHZ} , t _{DLZ}	Driver Output Disable Time			200	ns	Figure 12



PIN DESCRIPTIONS

Pin	Name	RS-232	RS-485 Full Duplex	RS-485 Half Duplex					
1	C1+	Charge pump cap 1 positive lead, 0.1µF							
2	VL	Logic Supply for TTL Inputs and Outputs, V _L = +1.65V to +5.5V or tie to V _{CC}							
3	VCC	Main Supply, V _{CO}	$_{\rm C}$ = +3.0V to +5.5V, bypass to g	pround with 1.0μF					
4	C1-	C	harge pump cap 1 negative lea	ad					
5	GND		Ground						
6	T1OUT, B/Z	Transmitter 1 Output	Z Driver Neg Output	B/Z Neg Input/Output					
7	T2OUT, A/Y	Transmitter 2 Output	Y Driver Pos Output	A/Y Pos Input/Output					
8	R10UT	Receiver 1 Output	Х	Х					
9	R2OUT, RO	Receiver 2 Output	Receiver TTL Output	Receiver TTL Output					
10			·						
11	SHDN	Lov	v power shutdown mode when	low					
12	SLEW	Dat	a rate limited to 250kbps when	low					
13	RS-485/RS-232	0	1	1					
14	HALF/FULL	Х	0	1					
15	GND		Ground						
16	R2IN, A	Receiver 2 Input	A Pos Receiver Input	Х					
17	R1IN, B	Receiver 1 Input	B Neg Receiver Input	Х					
18	RE	Х	Receiver ena	bled when low					
19	T2IN, DE	Transmitter 2 Input	Driver enabled when high						
20	T1IN, DI	Transmitter 1 Input	Driver T	TL Input					
21	V-	Charge p	ump negative supply, 0.1µF fro	om ground					
22	C2-	C	harge pump cap 2 negative lea	ad					
23	C2+	Chai	ge pump cap 2 positive lead, 0).1µF					
24	V+	Charge	pump positive supply, 0.1µF to	ground					



RS-232/RS-485/RS-422 TRANSCEIVER WITH 1.65V-5.5V INTERFACE

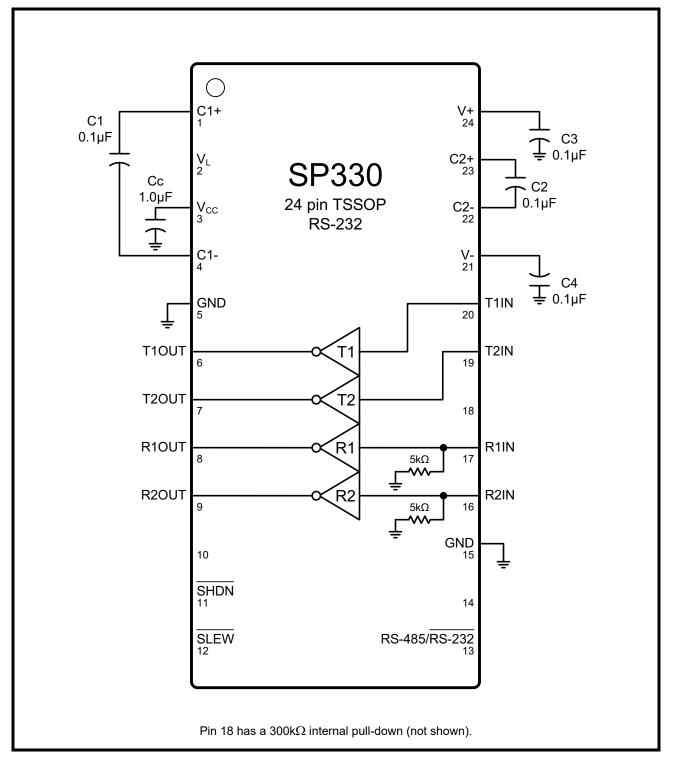
SUGGESTED DB9 CONNECTOR PINOUT

DB9 Pin	RS-232	RS-485 Full Duplex	RS-485 Half Duplex				
1							
2	RXD	RX+					
3	TXD	TX-	Data-				
4							
5	Ground						
6							
7	RTS	TX+	Data+				
8	CTS	RX-					
9							



BLOCK DIAGRAMS

FIGURE 1. RS-232 MODE





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FIGURE 2. RS-485 FULL DUPLEX MODE

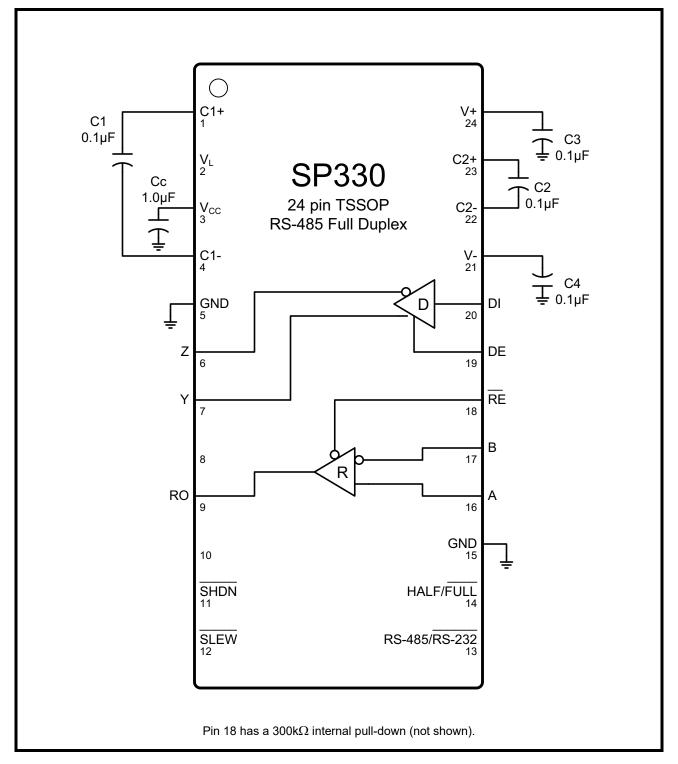
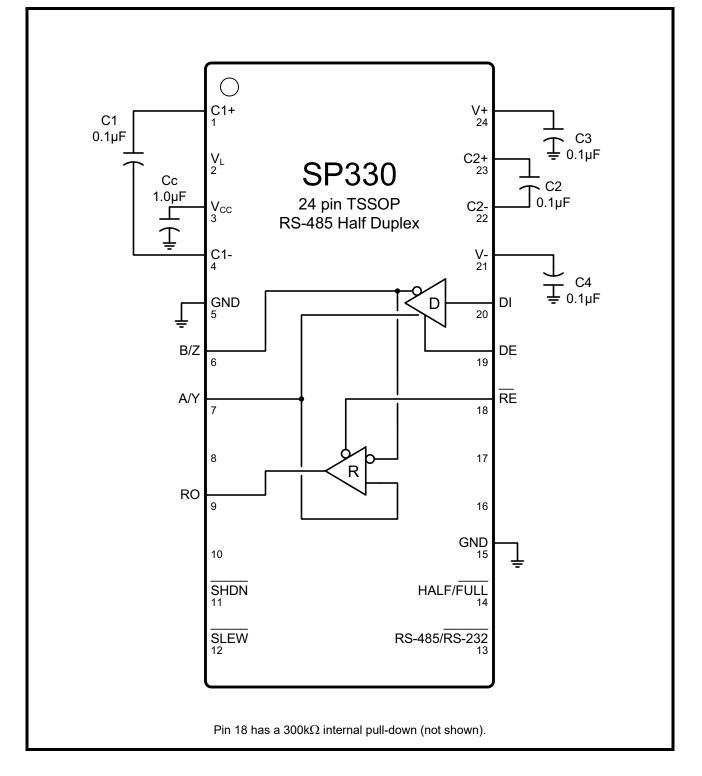




FIGURE 3. RS-485 HALF DUPLEX MODE





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TEST CIRCUITS

FIGURE 4. RS-485/422 DIFFERENTIAL DRIVER OUTPUT VOLTAGE

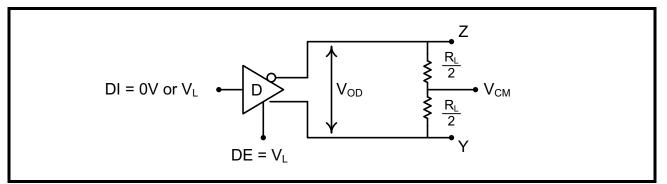


FIGURE 5. RS-485/422 DIFFERENTIAL DRIVER OUTPUT VOLTAGE OVER COMMON MODE

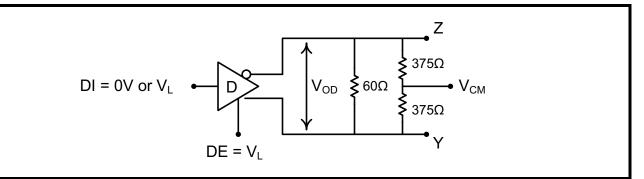
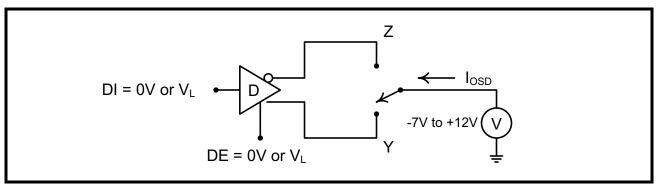


FIGURE 6. RS-485/422 DRIVER OUTPUT SHORT CIRCUIT CURRENT



RS-232/RS-485/RS-422 TRANSCEIVER WITH 1.65V-5.5V INTERFACE



FIGURE 7. RS-232 RECEIVER PROPAGATION DELAY

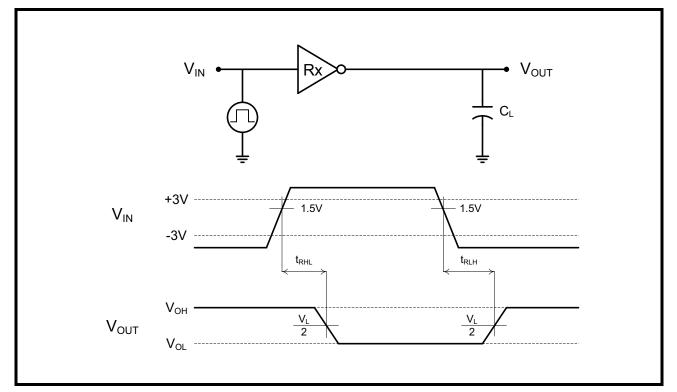
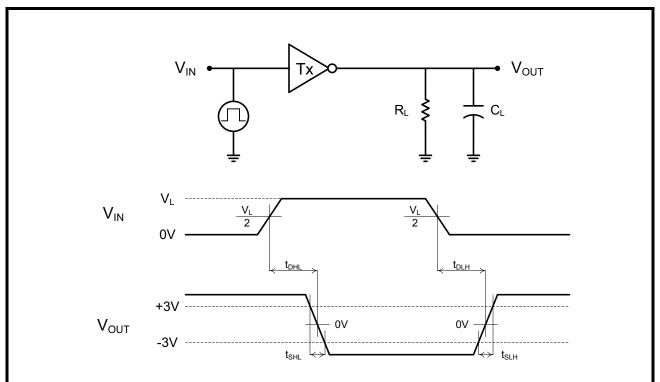


FIGURE 8. RS-232 DRIVER PROPAGATION DELAY





RS-232/RS-485/RS-422 TRANSCEIVER WITH 1.65V-5.5V INTERFACE

FIGURE 9. RS-485/422 RECEIVER PROPAGATION DELAY

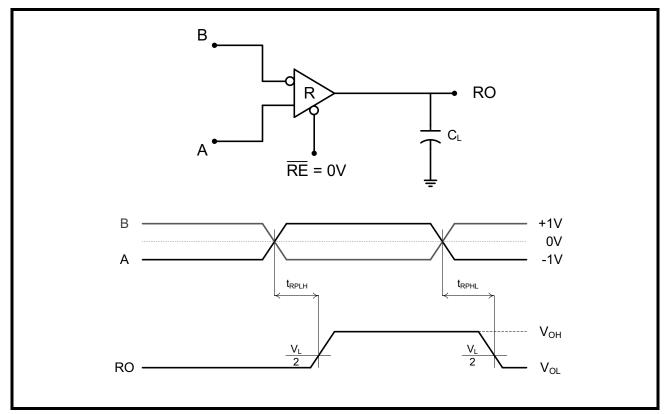
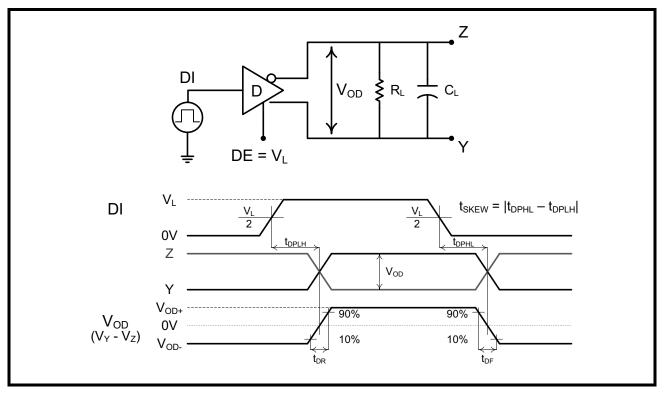
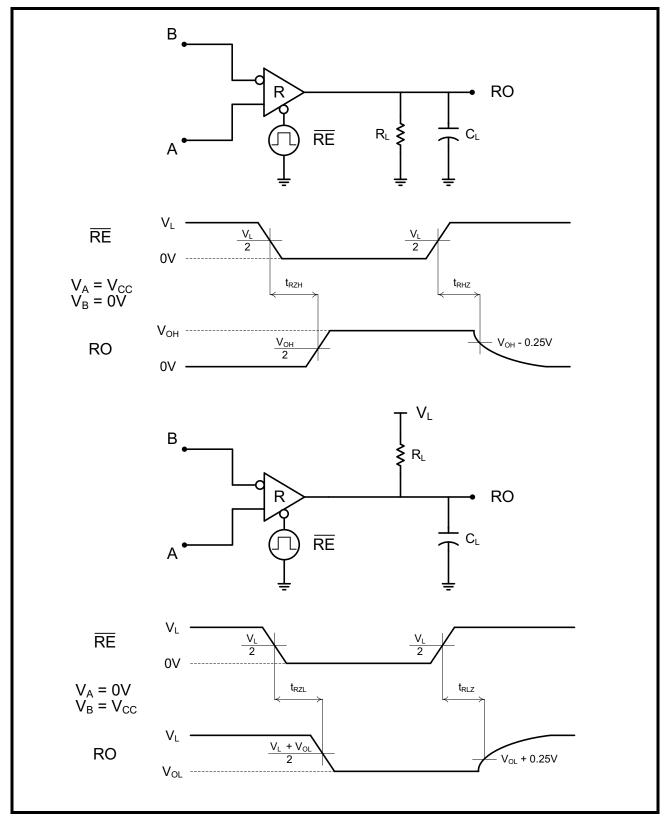


FIGURE 10. RS-485/422 DRIVER PROPAGATION DELAY AND RISE/FALL TIMES



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FIGURE 11. RS-485/422 RECEIVER OUTPUT ENABLE/DISABLE TIMES

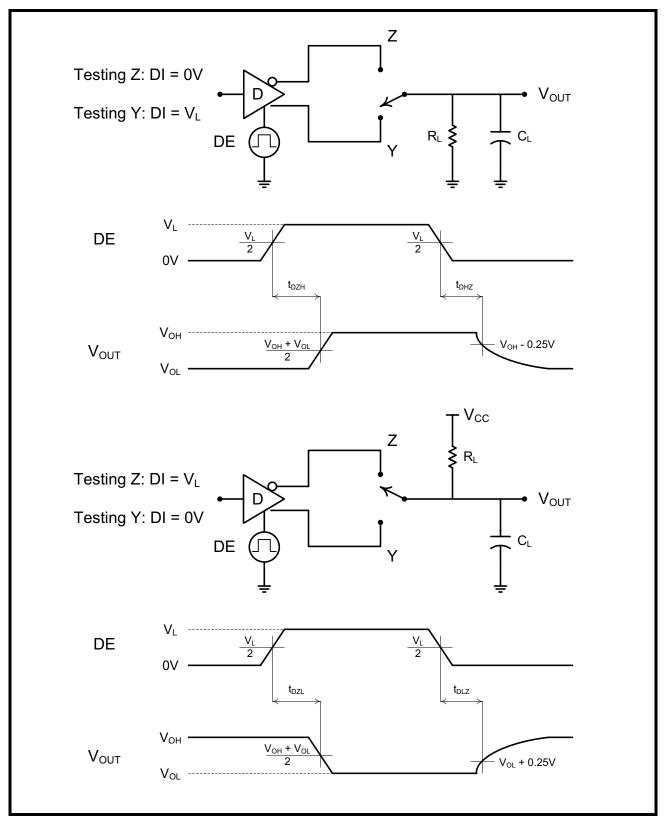




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FIGURE 12. RS-485/422 DRIVER OUTPUT ENABLE/DISABLE TIMES



PRODUCT SUMMARY



The SP330 is an advanced multiprotocol transceiver supporting RS-232, RS-485, and RS-422 serial standards. The multiple configuration modes allow all three protocols to be used interchangeably over a single cable or connector with no additional switching components. Full operation requires only four external charge pump capacitors.

ENHANCED FAILSAFE

The enhanced failsafe feature of the SP330 guarantees a logic-high receiver output when the receiver inputs are open, shorted, or terminated but idle/undriven. The enhanced failsafe interprets 0V differential as a logic high with a minimum 50mV noise margin, while maintaining compliance with the EIA/TIA-485 standard of ±200mV. No external biasing resistors are required, further easing the usage of multiple protocols over a single connector.

±15kV ESD PROTECTION

ESD protection structures are incorporated on all pins to protect against electrostatic discharges encountered during handling and assembly. The bus pins (driver outputs and receiver inputs) have extra protection structures, which have been tested up to ± 15 kV without damage. These structures withstand high ESD in all states: normal operation, in shutdown, and when powered off.

ESD protection is be tested in various ways. MaxLinear uses the following methods to qualify the protection structures designed into SP330:

±8kV using IEC 61000-4-2 Contact Discharge

±15kV using IEC 61000-4-2 Airgap Discharge

±15kV using the Human Body Model (HBM)

The IEC 61000-4-2 standard is more rigorous than HBM, resulting in lower voltage levels compared with HBM for the same level of ESD protection. Because IEC 61000-4-2 specifies a lower series resistance, the peak current is higher than HBM. The SP330 has passed both HBM and IEC 61000-4-2 testing without damage.

VARIABLE LOGIC LEVEL VOLTAGE

The SP330 includes a V_L pin which reduces the logic level thresholds to interface with processors operating at reduced supply voltages. This pin should be connected to the supply voltage of the processor or UART block, or can be connected to V_{CC} for typical logic levels.



RS-232/RS-485/RS-422 TRANSCEIVER WITH 1.65V-5.5V INTERFACE

SP330E

TRUTH TABLES

TABLE 1: RS-232 TX TRUTH TABLE

	INPUTS					
SHDN	RS-485/RS-232	DI/T1IN, DE/T2IN	Z(B)/T1OUT, Y(A)/T2OUT			
0	Х	X	1/8th unit load			
1	0	0	1			
1	0	1	0			
1	1	Х	RS-485 Mode			

TABLE 2: RS-232 RX TRUTH TABLE

	INPUTS					
SHDN	RS-485/RS-232	B/R1IN, A/R2IN	R1OUT, RO/R2OUT			
Х	0	0	1			
Х	0	1	0			
Х	0	Inputs open	1			
x	1	x	R1OUT High-Z, RO/R2OUT in RS-485 Mode			



TABLE 3: RS-485/422 TX TRUTH TABLE

	INPU	OUTPUTS				
SHDN	RS-485/RS-232	DE/T2IN	DI/T1IN	Z(B)/T1OUT	Y(A)/T2OUT	
0	Х	Х	х	1/8th unit load	1/8th unit load	
1	1	0	Х	1/8th unit load	1/8th unit load	
1	1	1	0	1	0	
1	1	1	1	0	1	
Х	0	Х	Х	RS-232 Mode		

TABLE 4: RS-485/422 RX TRUTH TABLE

INPUTS						
RS-485/RS-232	SHDN	HALF/FULL	RE	(A-B)	(Y-Z)	RO/R2OUT
1	0	Х	Х	Х	Х	High-Z
1	1	0	0	≥ -50mV	Х	1
1	1	0	0	≤ - 200mV	х	0
1	1	0	0	Floating	Х	1
1	1	1	0	х	≥ -50mV	1
1	1	1	0	Х	≤ - 200mV	0
1	1	1	0	Х	Floating	1
1	1	Х	1	Х	Х	High-Z
0	Х	Х	Х	Х	Х	RS-232 Mode

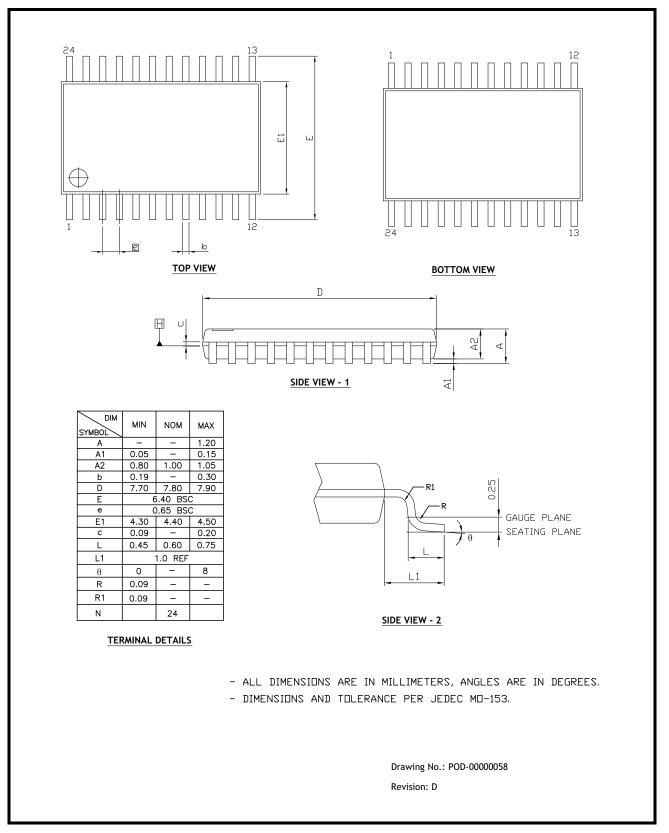


RS-232/RS-485/RS-422 TRANSCEIVER WITH 1.65V-5.5V INTERFACE

SP330E

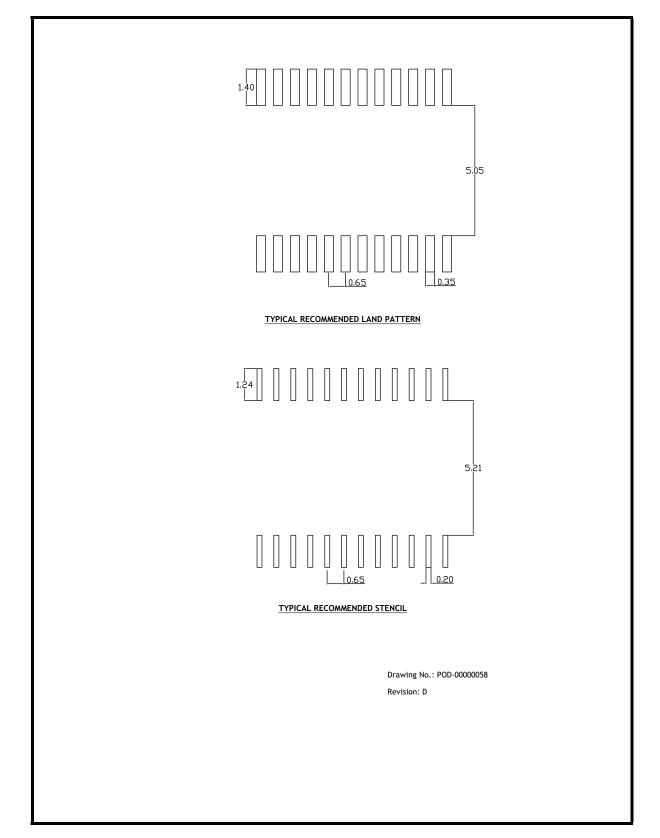
MECHANICAL DIMENSIONS

FIGURE 13. TSSOP 24 DRAWING



RECOMMENDED LAND PATTERN AND STENCIL

FIGURE 14. TSSOP 24







REVISION HISTORY

DATE	REVISION	DESCRIPTION
Nov 2013	1.0.0	Production Release
May 2018	1.0.1	Update to MaxLinear logo. Update format and ordering information. Update ESD protection / ratings table.



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