Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR 2SK1581

SWITCHING N-CHANNEL MOS FET

DESCRIPTION

The 2SK1581, N-channel vertical type MOS FET, can be driven by 2.5 V power supply.

As the 2SK1581 is driven by low voltage and does not require consideration of driving current, it is suitable for appliances including VCR cameras and headphone stereos which need power saving.

FEATURES

- Directly driven by ICs having a 3 V power supply.
- Not necessary to consider driving current because of its high input impedance.
- Possible to reduce the number of parts by omitting the bias resistor.

★ ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK1581	SC-59 (Mini Mold)

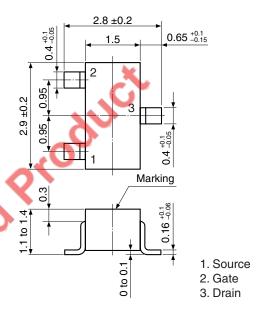
Marking: G14

★ ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

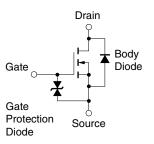
Drain to Source Voltage (V _G s ∈ 0 V)	VDSS	16	V	
Gate to Source Voltage (Vps = 0 V)	Vgss	±16	V	
Drain Current (DC)	ID(DC)	±200	mA	
Drain Current (pulse) Note	D(pulse)	±400	mA	
Total Power Dissipation	Рт	200	mW	
Channel Temperature	Tch	150	°C	
Storage Temperature	Tstg	-55 to +150	°C	

Note PW \leq 10 ms, Duty Cycle \leq 50%

PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT



★ Remark The diode connected between the gate and source of the transistor serves as a protector against ESD.
When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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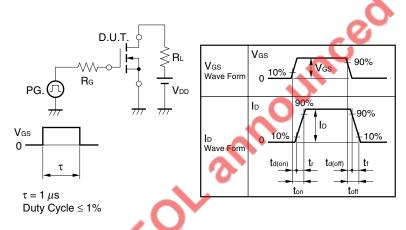


ELECTRICAL CHARACTERISTICS (TA = 25°C)

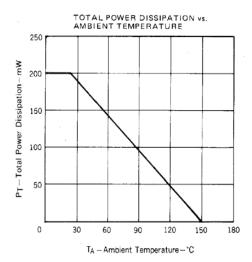
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Ipss	V _{DS} = 16 V, V _{GS} = 0 V			1.0	μΑ
Gate Leakage Current	Igss	V _{GS} = ±3.0 V, V _{DS} = 0 V			±5.0	μΑ
Gate Cut-off Voltage	V _{GS(off)}	$V_{DS} = 3.0 \text{ V}, I_{D} = 10 \mu\text{A}$	0.8	1.1	1.6	V
Forward Transfer Admittance Note	yfs	V _{DS} = 3.0 V, I _D = 10 mA	20	70		mS
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 2.5 V, I _D = 1.0 mA		3.2	5.0	Ω
	RDS(on)2	V _{GS} = 4.0 V, I _D = 1.0 mA		2.2	3.0	Ω
Input Capacitance	Ciss	V _{DS} = 3.0 V		27		pF
Output Capacitance	Coss	V _{GS} = 0 V		37		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		8.0		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 3.0 V, I _D = 10 mA		100		ns
Rise Time	t r	V _{GS} = 3.0 V	. (300		ns
Turn-off Delay Time	t _{d(off)}	R _G = 10 Ω		210		ns
Fall Time	tf)	240		ns

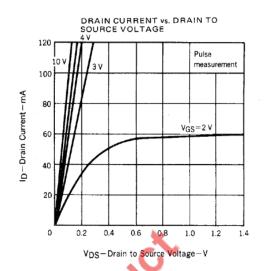
Note Pulsed

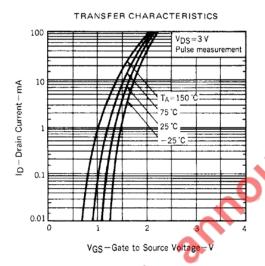
TEST CIRCUIT SWITCHING TIME

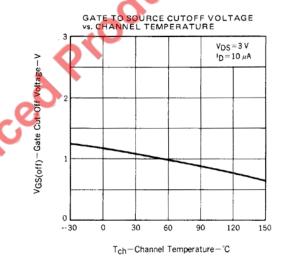


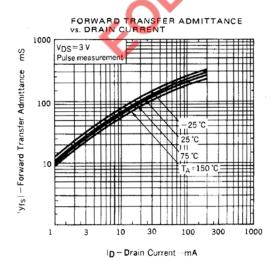
★ TYPICAL CHARACTERISTICS (T_A = 25°C)

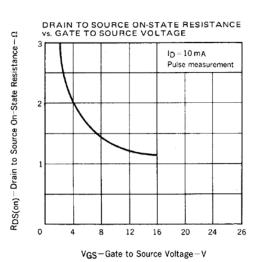


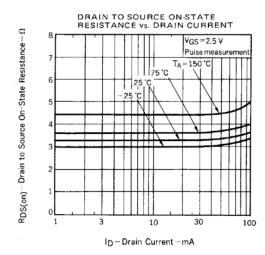


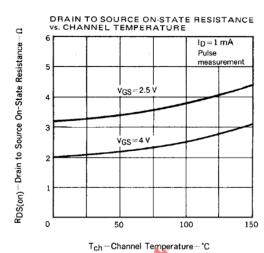


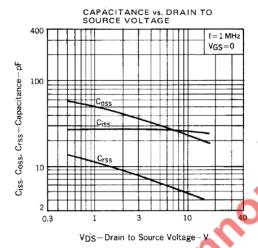


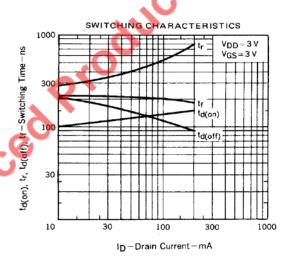


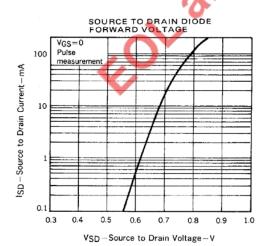












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