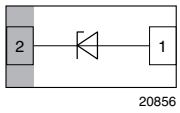
Single-Line ESD-Protection Diode in DFN1006-2A





FEATURES

- Compact DFN1006-2A package
- Low package height < 0.5 mm
- 1-line unidirectional ESD-protection
- AEC-Q101 qualified available
- Working range 1 V to 33 V
- ESD immunity acc. IEC 61000-4-2
  ±15 kV to ±30 kV contact discharge
  ±15 kV to ±30 kV air discharge
- Lead plating: Sn (e3)
  - soldering can be checked by standard vision inspection
  - AOI = Automated Optical Inspection
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free

e3

RoHS COMPLIANT

HALOGEN FREE GREEN (5-2008)

MARKING (example only)



Bar = cathode marking
X = date code
YY = type code (see table below)

LINKS TO ADDITIONAL RESOURCES

3D Models | SPICE Models | Application Notes

ORDERING INFORMATION

PART NUMBER (EXAMPLE)	AEC-Q101 QUALIFIED	ENVIRONMENTAL AND QUALITY CODE			ORDERING CODE (EXAMPLE)
		RoHS COMPLIANT + LEAD (Pb)-FREE TERMINATIONS	TIN PLATED	10K PER 7" REEL (8 mm TAPE)	
		GREEN		MOQ = 10K/BOX	
VESD05C1-HD1	-	G	3	-08	VESD05C1-HD1-G3-08
VESD05C1-HD1	H	G	3	-08	VESD05C1-HD1HG3-08

PACKAGE DATA

DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VESD01C1-HD1	DFN1006-2A	2A	0.83 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C
VESD03C1-HD1		2B				
VESD05C1-HD1		2G				
VESD08C1-HD1		2D				
VESD12C1-HD1		2E				
VESD16C1-HD1		2H				
VESD26C1-HD1		2J				
VESD33C1-HD1		2K				

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ABSOLUTE MAXIMUM RATINGS VESD01C1-HD1 (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	**TEST CONDITIONS**	**SYMBOL**	**VALUE**	**UNIT**
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I_{PPM}	11	A
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P_{PP}	100	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		30	kV
Operating temperature	Junction temperature	T_J	-55 to +150	°C
Storage temperature		T_{stg}	-65 to +150	°C

ABSOLUTE MAXIMUM RATINGS VESD03C1-HD1 (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	**TEST CONDITIONS**	**SYMBOL**	**VALUE**	**UNIT**
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I_{PPM}	11.6	A
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P_{PP}	100	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		30	kV
Operating temperature	Junction temperature	T_J	-55 to +150	°C
Storage temperature		T_{stg}	-65 to +150	°C

ABSOLUTE MAXIMUM RATINGS VESD05C1-HD1 (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	**TEST CONDITIONS**	**SYMBOL**	**VALUE**	**UNIT**
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I_{PPM}	8.7	A
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P_{PP}	100	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		30	kV
Operating temperature	Junction temperature	T_J	-55 to +150	°C
Storage temperature		T_{stg}	-65 to +150	°C

ABSOLUTE MAXIMUM RATINGS VESD08C1-HD1 (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	**TEST CONDITIONS**	**SYMBOL**	**VALUE**	**UNIT**
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I_{PPM}	6.60	A
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P_{PP}	100	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		30	kV
Operating temperature	Junction temperature	T_J	-55 to +150	°C
Storage temperature		T_{stg}	-65 to +150	°C

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ABSOLUTE MAXIMUM RATINGS VESD12C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I_{PPM}	4.4	A
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P_{PP}	100	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		30	kV
Operating temperature	Junction temperature	T_J	-55 to +150	°C
Storage temperature		T_{stg}	-65 to +150	°C

ABSOLUTE MAXIMUM RATINGS VESD16C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I_{PPM}	3.6	A
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P_{PP}	100	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		30	kV
Operating temperature	Junction temperature	T_J	-55 to +150	°C
Storage temperature		T_{stg}	-65 to +150	°C

ABSOLUTE MAXIMUM RATINGS VESD26C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I_{PPM}	2.1	A
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P_{PP}	100	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	20	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		20	kV
Operating temperature	Junction temperature	T_J	-55 to +150	°C
Storage temperature		T_{stg}	-65 to +150	°C

ABSOLUTE MAXIMUM RATINGS VESD33C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I_{PPM}	1.6	A
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P_{PP}	100	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	15	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		15	kV
Operating temperature	Junction temperature	T_J	-55 to +150	°C
Storage temperature		T_{stg}	-65 to +150	°C

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ELECTRICAL CHARACTERISTICS VESD01C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	1	V
Reverse voltage	at I_R = 100 µA	V_R	1	1.2	-	V
Reverse current	at V_R = 1 V	I_R	-	20	100	µA
Reverse breakdown voltage	at I_R = 5 mA	V_{BR}	2.55	2.7	2.85	V
Reverse clamping voltage	at $I_{PP} = I_{PPM}$ = 11 A, t_p = 8/20 µs	V_C	-	6.4	6.9	V
Forward clamping voltage	at I_{PP} = 1 A, t_p = 300 µs	V_F	0.9	1.1	1.2	V
	at $I_{PP} = I_{PPM}$ = 11 A, t_p = 8/20 µs	V_F	-	3.2	3.92	V
Dynamic resistance	t_p = 100 ns (TLP; pin 2-1)	r_{dyn}	-	0.13	-	Ω
Capacitance	at V_R = 0 V; f = 1 MHz	C_D	153	192	230	pF

ELECTRICAL CHARACTERISTICS VESD03C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	3	V
Reverse voltage	at I_R = 20 µA	V_R	3	-	-	V
Reverse current	at V_R = 3 V	I_R	-	8	20	µA
Reverse breakdown voltage	at I_R = 5 mA	V_{BR}	4.45	4.7	4.95	V
Reverse clamping voltage	at $I_{PP} = I_{PPM}$ = 11.6 A, t_p = 8/20 µs	V_C	-	7.8	8.70	V
Forward clamping voltage	at I_{PP} = 1 A, t_p = 300 µs	V_F	0.9	1.1	1.2	V
	at $I_{PP} = I_{PPM}$ = 11.6 A, t_p = 8/20 µs	V_F	-	2.6	3.32	V
Dynamic resistance	t_p = 100 ns (TLP; pin 2-1)	r_{dyn}	-	0.19	-	Ω
Capacitance	at V_R = 0 V; f = 1 MHz	C_D	89	112	135	pF

ELECTRICAL CHARACTERISTICS VESD05C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	5	V
Reverse voltage	at I_R = 1 µA	V_R	5	-	-	V
Reverse current	at V_R = 5 V	I_R	-	0.01	0.1	µA
Reverse breakdown voltage	at I_R = 1 mA	V_{BR}	7.1	7.5	7.9	V
Reverse clamping voltage	at $I_{PP} = I_{PPM}$ = 8.7 A, t_p = 8/20 µs	V_C	-	10.3	11.5	V
Forward clamping voltage	at I_{PP} = 1 A, t_p = 300 µs	V_F	0.9	1.1	1.2	V
	at $I_{PP} = I_{PPM}$ = 8.7 A, t_p = 8/20 µs	V_F	-	2.2	2.74	V
Dynamic resistance	t_p = 100 ns (TLP; pin 2-1)	r_{dyn}	-	0.2	-	Ω
Capacitance	at V_R = 0 V; f = 1 MHz	C_D	53	67	81	pF

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ELECTRICAL CHARACTERISTICS VESD08C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	8	V
Reverse voltage	at I_R = 0.1 µA	V_R	8	-	-	V
Reverse current	at V_R = 8 V	I_R	-	0.01	0.1	µA
Reverse breakdown voltage	at I_R = 1 mA	V_{BR}	9.4	9.85	10.3	V
Reverse clamping voltage	at $I_{PP} = I_{PPM}$ = 6.6 A, t_p = 8/20 µs	V_C	-	13.7	15.3	V
Forward clamping voltage	at I_{PP} = 1 A, t_p = 300 µs	V_F	0.9	1.1	1.2	V
	at $I_{PP} = I_{PPM}$ = 6.6 A, t_p = 8/20 µs	V_F	-	1.9	2.32	V
Dynamic resistance	t_p = 100 ns (TLP; pin 2-1)	r_{dyn}	-	0.23	-	Ω
Capacitance	at V_R = 0 V; f = 1 MHz	C_D	37	47	57	pF

ELECTRICAL CHARACTERISTICS VESD12C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	12	V
Reverse voltage	at I_R = 0.1 µA	V_R	12	-	-	V
Reverse current	at V_R = 12 V	I_R	-	0.01	0.1	µA
Reverse breakdown voltage	at I_R = 1 mA	V_{BR}	14.3	15	15.8	V
Reverse clamping voltage	at $I_{PP} = I_{PPM}$ = 4.4 A, t_p = 8/20 µs	V_C	-	20.5	22.7	V
Forward clamping voltage	at I_{PP} = 1 A, t_p = 300 µs	V_F	0.9	1.1	1.2	V
	at $I_{PP} = I_{PPM}$ = 4.4 A, t_p = 8/20 µs	V_F	-	1.6	1.88	V
Dynamic resistance	t_p = 100 ns (TLP; pin 2-1)	r_{dyn}	-	0.4	-	Ω
Capacitance	at V_R = 0 V; f = 1 MHz	C_D	26	33	40	pF

ELECTRICAL CHARACTERISTICS VESD16C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	16	V
Reverse voltage	at I_R = 0.1 µA	V_R	16	-	-	V
Reverse current	at V_R = 16 V	I_R	-	0.01	0.1	µA
Reverse breakdown voltage	at I_R = 1 mA	V_{BR}	17.1	18	18.9	V
Reverse clamping voltage	at $I_{PP} = I_{PPM}$ = 3.6 A, t_p = 8/20 µs	V_C	-	25.3	28	V
Forward clamping voltage	at I_{PP} = 1 A, t_p = 300 µs	V_F	0.9	1.1	1.2	V
	at $I_{PP} = I_{PPM}$ = 3.6 A, t_p = 8/20 µs	V_F	-	1.5	1.72	V
Dynamic resistance	t_p = 100 ns (TLP; pin 2-1)	r_{dyn}	-	0.53	-	Ω
Capacitance	at V_R = 0 V; f = 1 MHz	C_D	21	27	33	pF

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ELECTRICAL CHARACTERISTICS VESD26C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	26	V
Reverse voltage	at I_R = 0.1 µA	V_R	26	-	-	V
Reverse current	at V_R = 26 V	I_R	-	< 0.01	0.1	µA
Reverse breakdown voltage	at I_R = 1 mA	V_{BR}	28.5	30	31.5	V
Reverse clamping voltage	at $I_{PP} = I_{PPM}$ = 2.1 A, t_p = 8/20 µs	V_C	-	43	48	V
Forward clamping voltage	at I_{PP} = 1 A, t_p = 300 µs	V_F	0.9	1.1	1.2	V
	at $I_{PP} = I_{PPM}$ = 2.1 A, t_p = 8/20 µs	V_F	-	1.3	1.42	V
Dynamic resistance	t_p = 100 ns (TLP; pin 2-1)	r_{dyn}	-	1.9	-	Ω
Capacitance	at V_R = 0 V; f = 1 MHz	C_D	14	17.5	21	pF

ELECTRICAL CHARACTERISTICS VESD33C1-HD1
(T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	33	V
Reverse voltage	at I_R = 0.1 µA	V_R	33	-	-	V
Reverse current	at V_R = 33 V	I_R	-	< 0.01	0.1	µA
Reverse breakdown voltage	at I_R = 1 mA	V_{BR}	37	39	41	V
Reverse clamping voltage	at $I_{PP} = I_{PPM}$ = 1.6 A, t_p = 8/20 µs	V_C	-	56	62.5	V
Forward clamping voltage	at I_{PP} = 1 A, t_p = 300 µs	V_F	0.9	1.1	1.2	V
	at $I_{PP} = I_{PPM}$ = 1.6 A, t_p = 8/20 µs	V_F	-	1.22	1.32	V
Dynamic resistance	t_p = 100 ns (TLP; pin 2-1)	r_{dyn}	-	3.6	-	Ω
Capacitance	at V_R = 0 V; f = 1 MHz	C_D	12	15	18	pF

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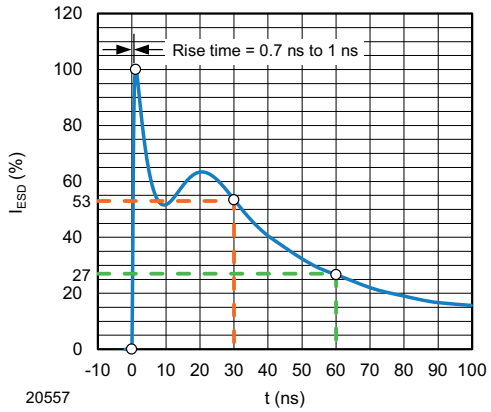


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω / 150 pF)

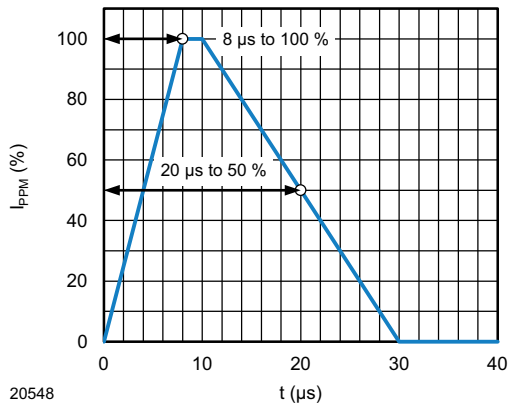


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

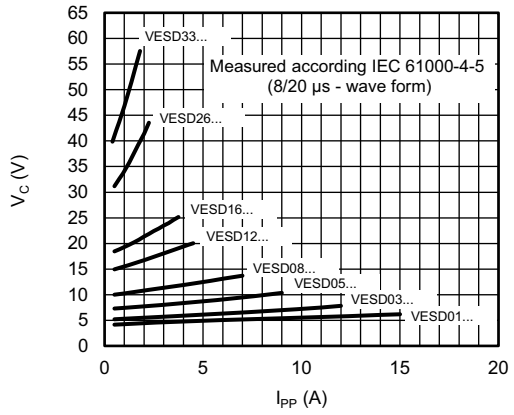


Fig. 3 - Typical Peak Clamping Voltage vs. Peak Pulse Current

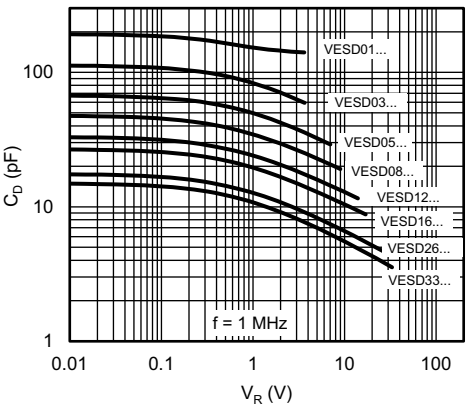


Fig. 4 - Typical Capacitance vs. Reverse Voltage

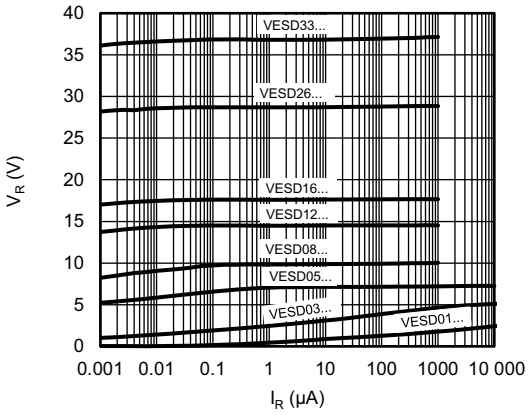


Fig. 5 - Typical Reverse Voltage vs. Reverse Current

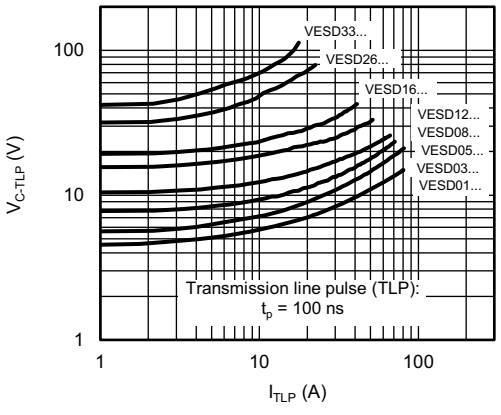


Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current

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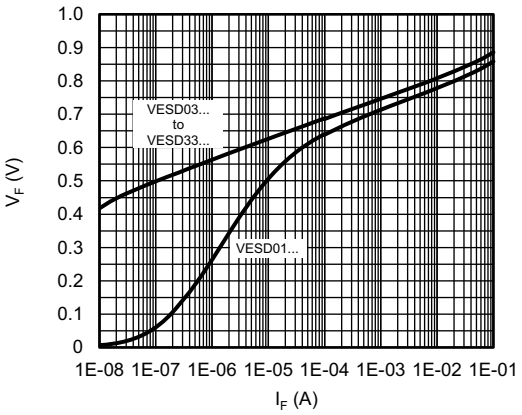


Fig. 7 - Typical Forward Voltage vs. Forward Current

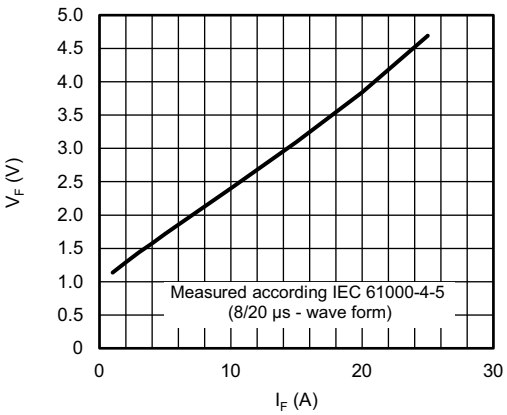
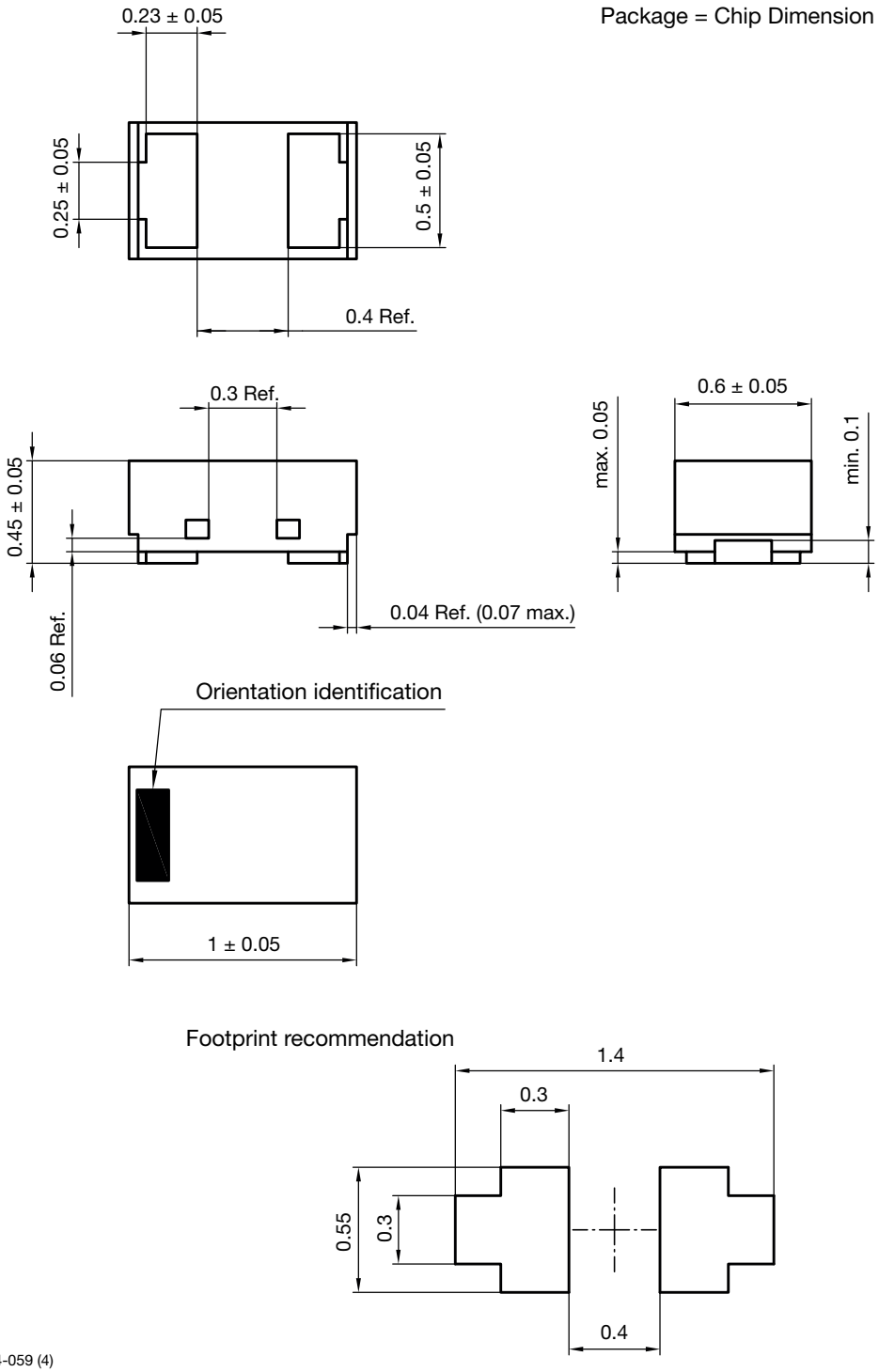


Fig. 8 - Typical Forward Voltage vs. Forward Current

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PACKAGE DIMENSIONS in millimeters (Inches): **DFN1006-2A**

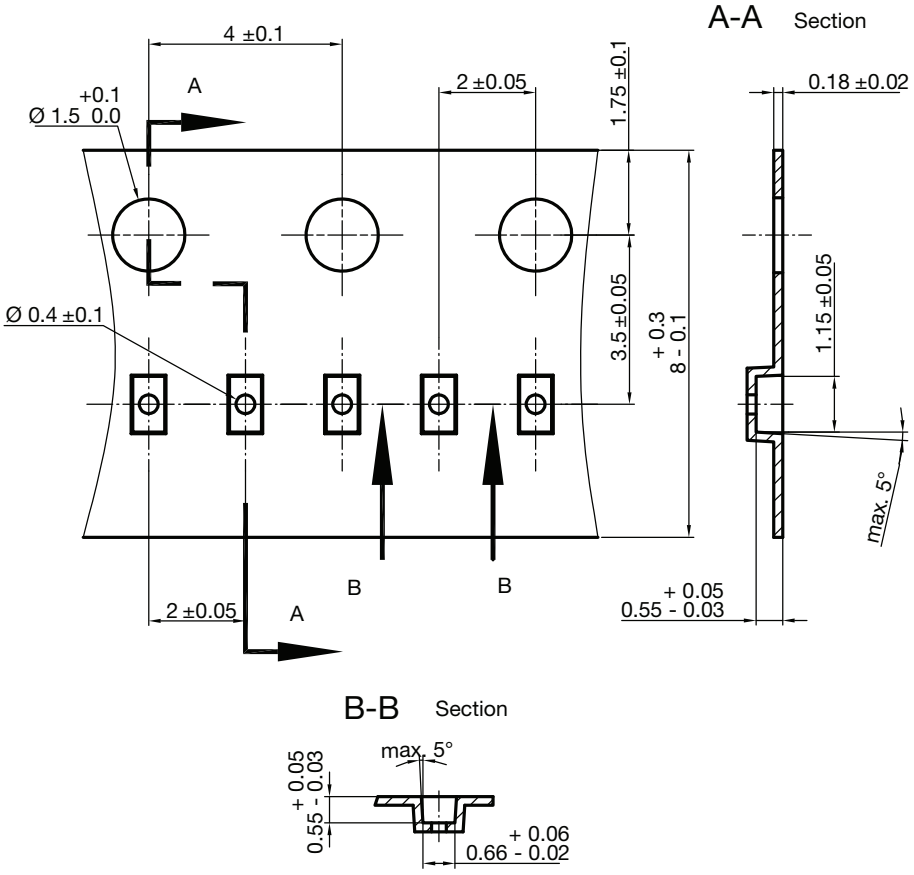


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Rev.5 - Date: 17-Sep-2021

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CARRIER TAPE DFN1006-2A

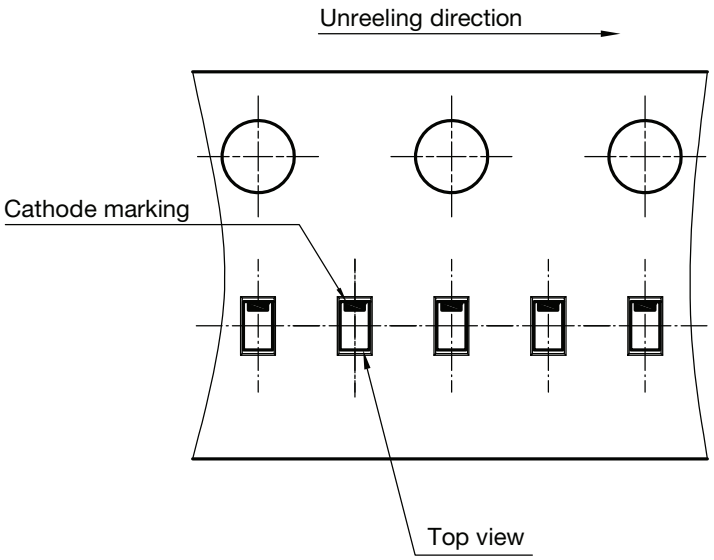


surface resistance: 10^5 - 10^{11} $\frac{\text{OHMS}}{\text{SQ}}$

Cummulative tolerances of 10 sprocket holes is ± 0.2 mm

S8-V-3906.04-063 (4)
created 28.10.2019

ORIENTATION IN CARRIER TAPE DFN1006-2A



S8-V-3906.04-064 (4)
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For technical questions, contact: ESDprotection@vishay.com
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