FFSP2065BDN-F085

Product Preview

Silicon Carbide Schottky Diode, 650 V, 20 A

ON Semiconductor®

www.onsemi.com

Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size & cost.

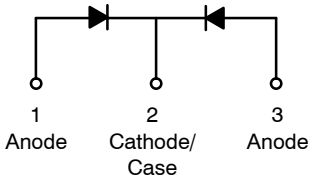
Features

- Max Junction Temperature 175°C
- Avalanche Rated 49 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery
- AEC−Q101 Qualified
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

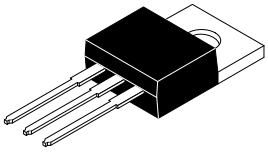
Applications

- Automotive HEV−EV Onboard Chargers
- Automotive HEV−EV DC−DC Converters

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

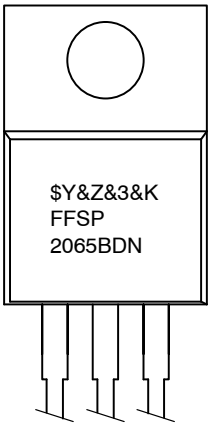


Schottky Diode



TO−220−3LD
CASE 340AT

MARKING DIAGRAM



$Y	= ON Semiconductor Logo
&Z	= Assembly Plant Code
&3	= Numeric Date Code
&K	= Lot Code
FFSP2065BDN	= Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

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ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{RRM}	Peak Repetitive Reverse Voltage		650	V
E_{AS}	Single Pulse Avalanche Energy (Note 1)		49*	mJ
I_F	Continuous Rectified Forward Current @ T_C < 136°C		10*/20**	A
$I_{F, Max}$	Non-Repetitive Peak Forward Surge Current	T_C = 25°C, 10 μs	650	A
		T_C = 150°C, 10 μs	570	A
$I_{F,SM}$	Non-Repetitive Forward Surge Current T_C = 25°C	Half-Sine Pulse, t_p = 8.3 ms	42	A
Ptot	Power Dissipation	T_C = 25°C	60	W
		T_C = 150°C	10	W
T_J, T_{STG}	Operating and Storage Temperature Range		−55 to +175	°C
	TO247 Mounting Torque, M3 Screw		60	Ncm

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
* Per Leg, ** Per Device
1. E_{AS} of 49 mJ is based on starting T_J = 25°C, L = 0.5 mH, I_{AS} = 14 A, V = 50 V.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	2.4*/1.3**	°C/W

* Per Leg, ** Per Device

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted (per leg))

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_F	Forward Voltage	I_F = 10 A, T_C = 25°C	–	1.38	1.7	V
		I_F = 10 A, T_C = 125°C	–	1.6	2.0	
		I_F = 10 A, T_C = 175°C	–	1.72	2.4	
I_R	Reverse Current	V_R = 650 V, T_C = 25°C	–	0.5	40	μA
		V_R = 650 V, T_C = 125°C	–	1	80	
		V_R = 650 V, T_C = 175°C	–	2	160	
Q_C	Total Capacitive Charge	V = 400 V	–	25	–	nC
C	Total Capacitance	V_R = 1 V, f = 100 kHz	–	421	–	pF
		V_R = 300 V, f = 100 kHz	–	40	–	
		V_R = 600 V, f = 100 kHz	–	34	–	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
FFSP2065BDN−F085	FFSP2065BDN	TO−220−3LD (Pb−Free / Halogen Free)	50 Units / Tube

TYPICAL CHARACTERISTICS

(T_J = 25°C unless otherwise noted (per leg))

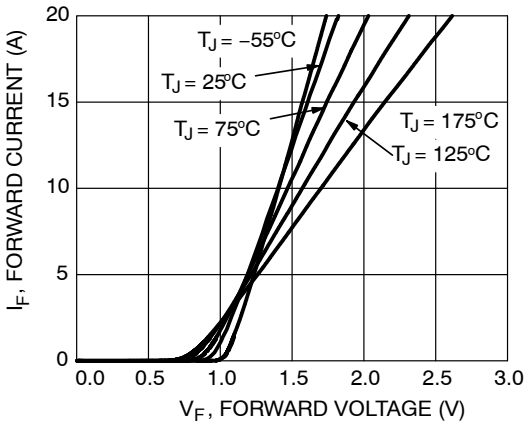


Figure 1. Forward Characteristics

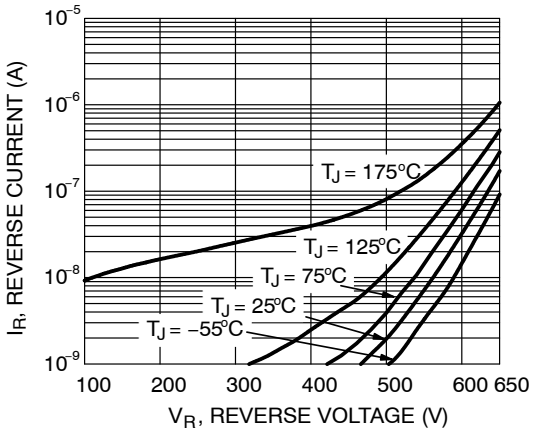


Figure 2. Reverse Characteristics

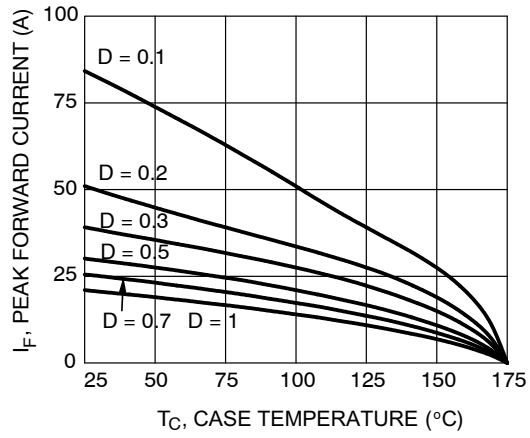


Figure 3. Current Derating

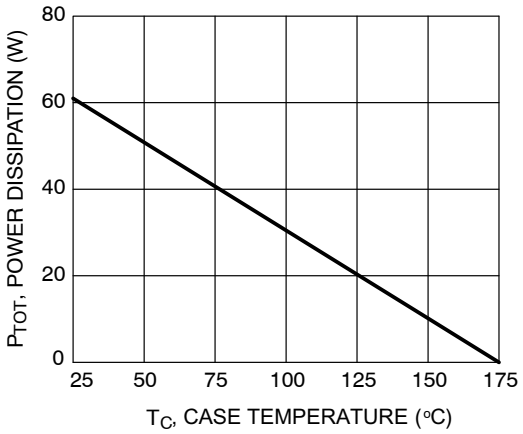


Figure 4. Power Derating

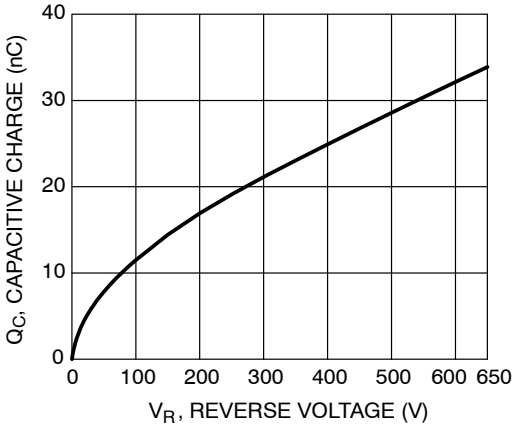


Figure 5. Capacitive Charge vs. Reverse Voltage

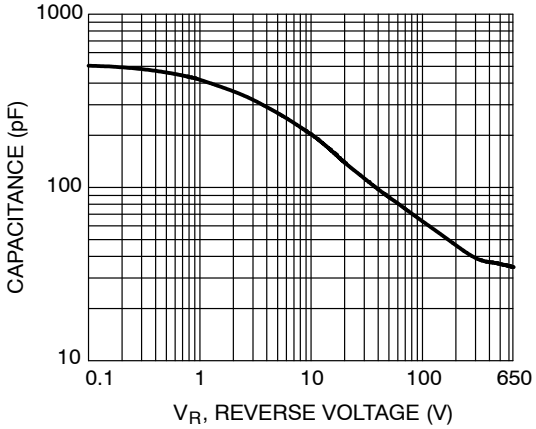


Figure 6. Capacitance vs. Reverse Voltage

TYPICAL CHARACTERISTICS

(T_J = 25°C unless otherwise noted)

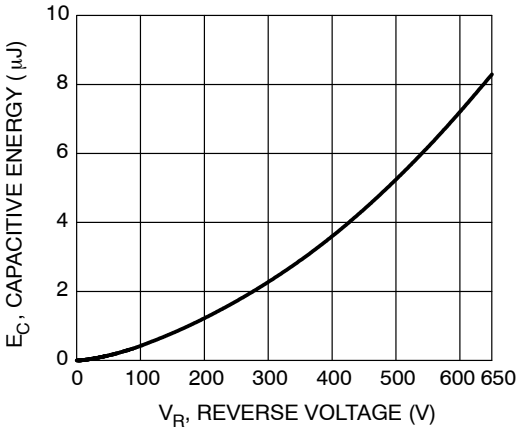


Figure 7. Capacitance Stored Energy

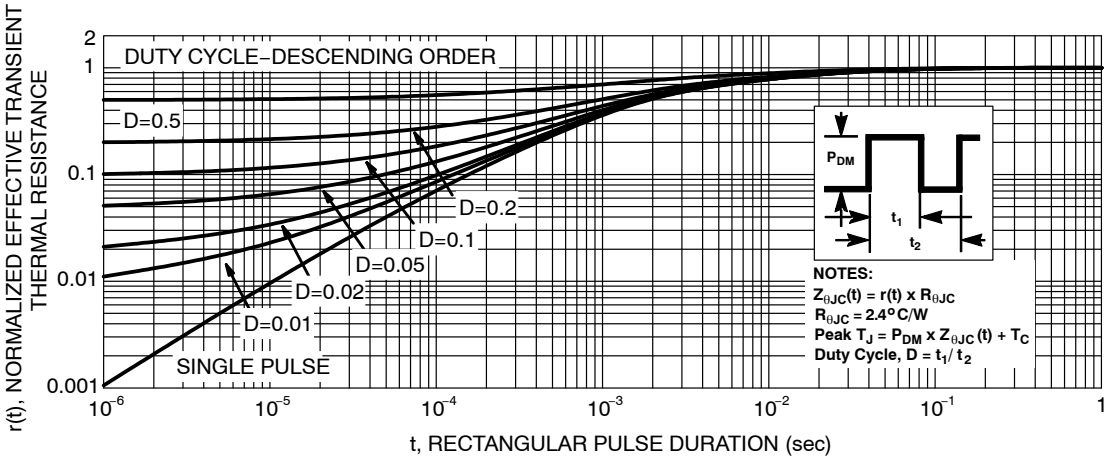


Figure 8. Junction-to-Case Transient Thermal Response Curve

TEST CIRCUIT AND WAVEFORMS

L = 0.5 mH
R < 0.1 Ω
V_{DD} = 50 V
EAVL = 1/2LI2 [$V_{R(AVL)}$ / ($V_{R(AVL)}$ − V_{DD})]
Q1 = IGBT (BV_{CES} > DUT $V_{R(AVL)}$)

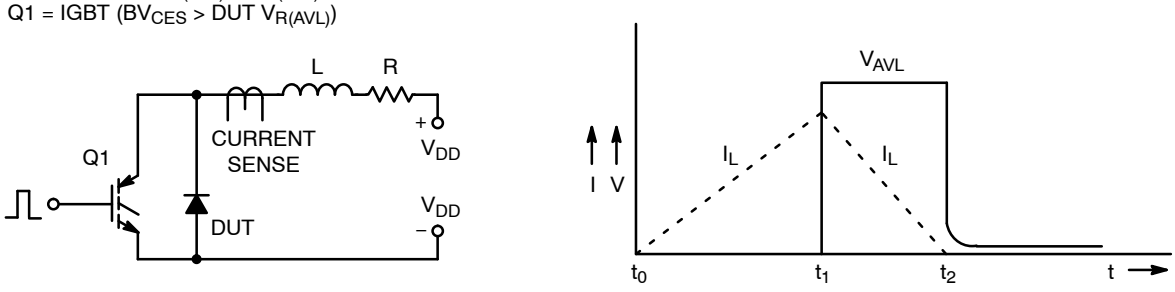


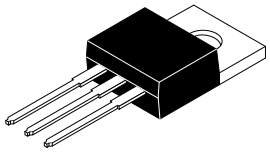
Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS

ON Semiconductor®

TO−220−3LD
CASE 340AT
ISSUE A

DATE 03 OCT 2017



Scale 1:1

NOTES:
A) REFERENCE JEDEC, TO-220, VARIATION AB
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [ ].
D) LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
E DOES NOT COMPLY JEDEC STANDARD VALUE.
F) "A1" DIMENSIONS AS BELOW:
SINGLE GAUGE = 0.51 - 0.61
DUAL GAUGE = 1.10 - 1.45
G PRESENCE IS SUPPLIER DEPENDENT
H) SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK.

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