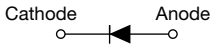
Ultrafast Rectifier, 5 A FRED Pt®



SMC (DO-214AB)



FEATURES

- Ultrafast recovery time, reduced Q_{rr} and soft recovery
- 175 °C maximum operating junction temperature
- For PFC CRM/CCM, snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

LINKS TO ADDITIONAL RESOURCES

3D Models

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMC (DO-214AB)
Molding compound meets UL 94 V-0 flammability rating
Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

Polarity: color band denotes cathode end

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	5 A
V_R	600 V
V_F at I_F	0.92 V
t_{rr} typ.	45 ns
T_J max.	175 °C
Package	SMC (DO-214AB)
Circuit configuration	Single

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current	$I_{F(AV)}$	T_L = 99 °C (1)	5	A
Non-repetitive peak surge current	I_{FSM}	T_J = 25 °C, 10 ms sine pulse	130	
Operating junction and storage temperatures	T_J, T_{Stg}		-55 to +175	°C

Note

(1) Mounted on PCB with minimum pad size

ELECTRICAL SPECIFICATIONS (T_J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	I_R = 100 µA	600	-	-	V
Forward voltage	V_F	I_F = 5 A	-	1.1	1.35	
		I_F = 5 A, T_J = 150 °C	-	0.92	1.15	
Reverse leakage current	I_R	V_R = V_R rated	-	-	3	µA
		T_J = 150 °C, V_R = V_R rated	-	-	100	
Junction capacitance	C_T	V_R = 600 V	-	7.8	-	pF

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DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	I_F = 1.0 A, dI_F/dt = 100 A/µs, V_R = 30 V		-	45	-	ns
		I_F = 1.0 A, dI_F/dt = 50 A/µs, V_R = 30 V		-	50	-	
		I_F = 0.5 A, I_R = 1 A, I_{rr} = 0.25 A		-	-	65	
		T_J = 25 °C	I_F = 5 A dI_F/dt = 200 A/µs V_R = 390 V	-	43	-	
		T_J = 125 °C		-	81	-	
Peak recovery current	I_{RRM}	T_J = 25 °C		-	6.1	-	A
		T_J = 125 °C		-	8.4	-	
Reverse recovery charge	Q_{rr}	T_J = 25 °C		-	133	-	nC
		T_J = 125 °C		-	347	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		-55	-	175	°C
Thermal resistance, junction to mount	R_{thJM} (1)		-	-	14	°C/W
Thermal resistance, junction to ambient	R_{thJA} (1)		-	-	80	
Approximate Weight			0.24			g
			0.008			oz.
Marking device		Case style SMC (DO-214AB)	5U6			

Note

(1) Mounted on PCB with minimum pad size

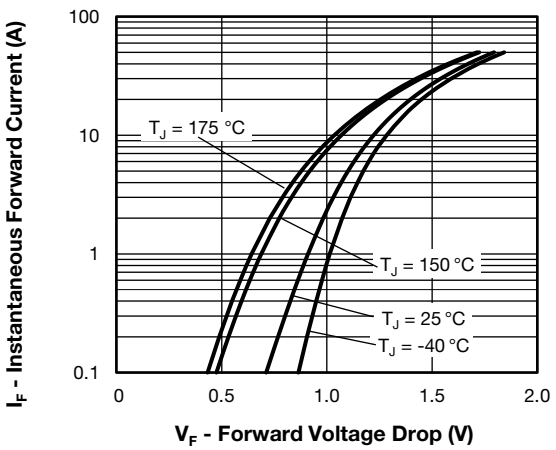


Fig. 1 - Typical Forward Voltage Drop Characteristics

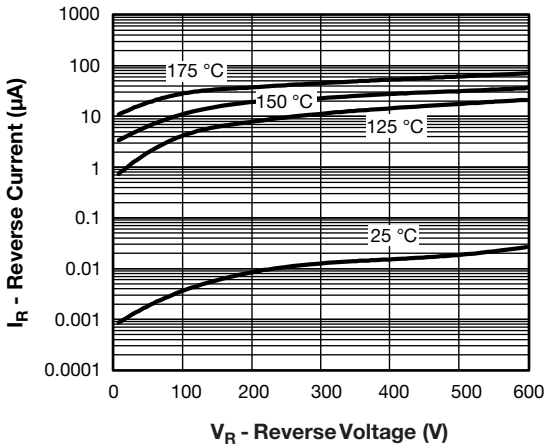


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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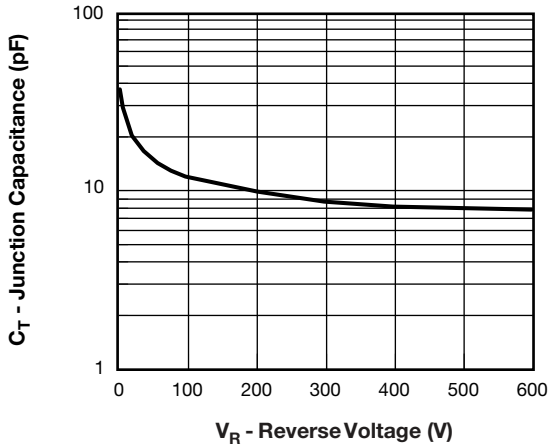


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

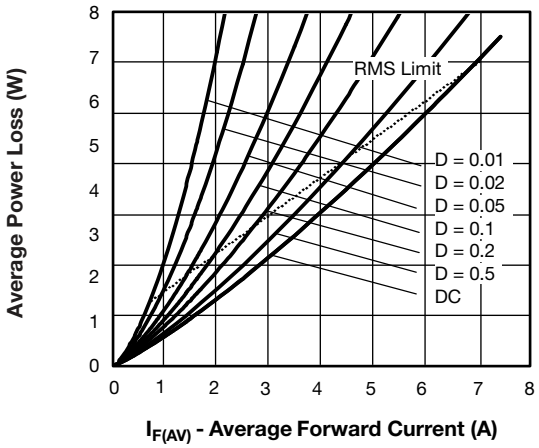


Fig. 5 - Forward Power Loss Characteristics

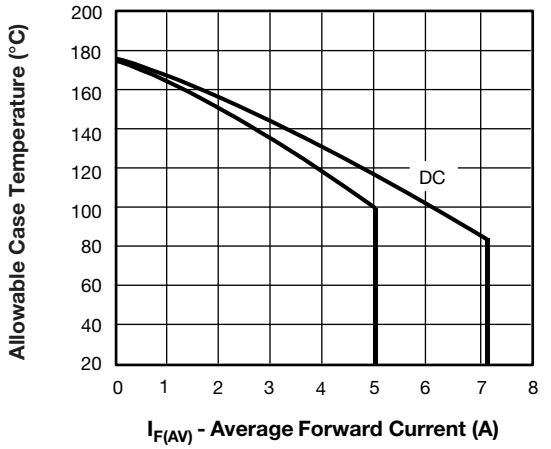


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

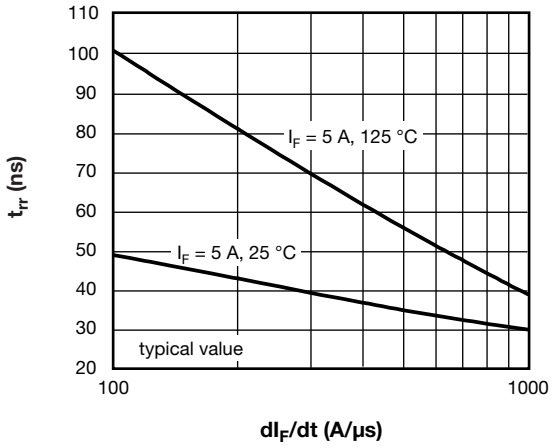


Fig. 6 - Typical Reverse Recovery vs. dI_F/dt

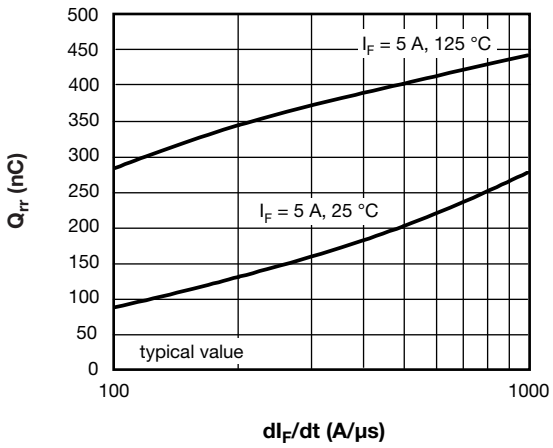
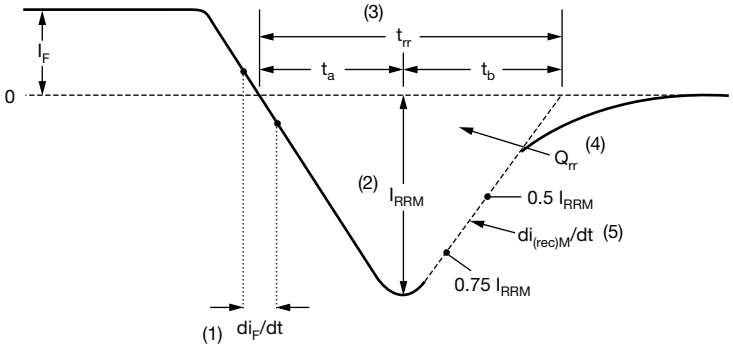


Fig. 7 - Typical Stored Charge vs. dI_F/dt

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(1) di_F/dt - rate of change of current through zero crossing

(2) I_{RRM} - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	VS-	5	E	C	U	06	-M3
	1	2	3	4	5	6	7

1 - Vishay Semiconductors product

2 - Current rating (5 = 5 A)

3 - Circuit configuration:
E = single diode

4 - C = SMC package

5 - Process type
U = ultrafast recovery

6 - Voltage code (06 = 600 V)

7 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	**QUANTITY PER TUBE**	**MINIMUM ORDER QUANTITY**	**PACKAGING DESCRIPTION**
VS-5ECU06-M3/9AT	9AT	3500	13"diameter plastic tape and reel

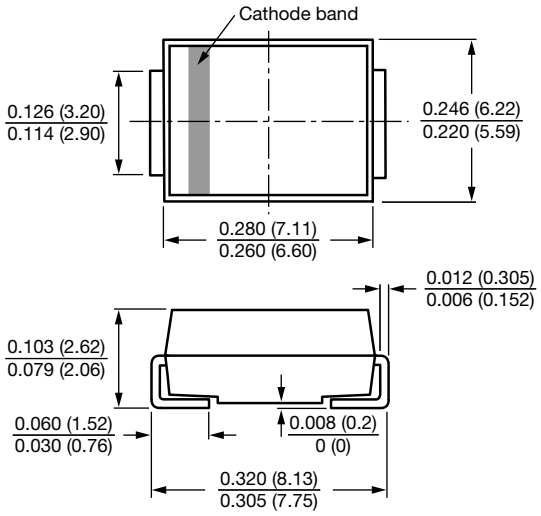
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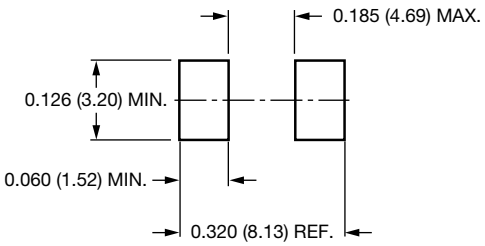
SMC

DIMENSIONS in inches (millimeters)

DO-214AB (SMC)



Mounting Pad Layout



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