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## **FDB088N08** N-Channel PowerTrench<sup>®</sup> MOSFET **75 V, 85 A, 8.8 m**Ω

### **Features**

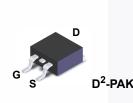
- R<sub>DS(on)</sub> = 7.3 mΩ (Typ.) @ V<sub>GS</sub> = 10 V, I<sub>D</sub> = 75 A
- · Fast Switching Speed
- · Low Gate Charge
- · High Performance Trench Technology for Extremely Low R<sub>DS(on)</sub>
- · High Power and Current Handling Capability
- 100% Internal R<sub>G</sub> Screening for Easy Paralleling Operation
- · RoHS Compliant

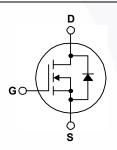
## Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

## Applications

- · Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- · Motor Drives and Uninterruptible Power Supplies





Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

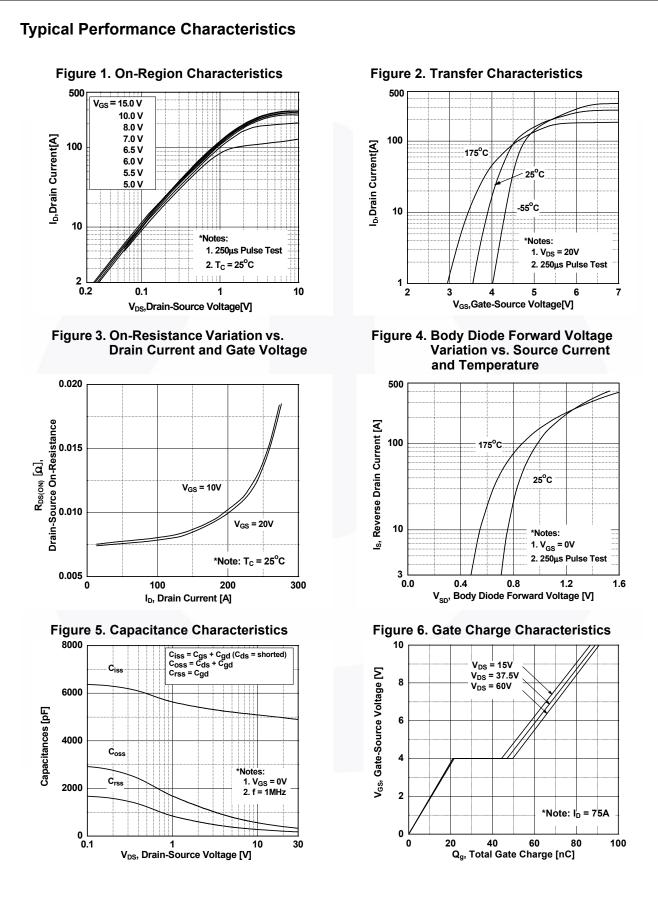
Symbol	Parar	neter	FDB088N08	Unit
V <sub>DSS</sub>	Drain to Source Voltage		75	V
V <sub>GSS</sub>	Gate to Source Voltage		±20	V
	Drain Current - Continuous (	85	Α	
I <sub>D</sub>	- Continuous (	60	A	
	- Continuous (	120	А	
I <sub>DM</sub>	Drain Current - Puls	sed (Note 1)	340	А
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	309	mJ
dv/dt	Peak Diode Recovery dv/dt (Note		10	V/ns
P <sub>D</sub>	Power Dissipation	25°C)	160	W
	- Dera	ate above 25°C	1.06	W/ºC
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	°C
Τ <sub>L</sub>	Maximum Lead Temperature for Solderin	ng, 1/8" from Case for 5 Seconds	300	°C

## **Thermal Characteristics**

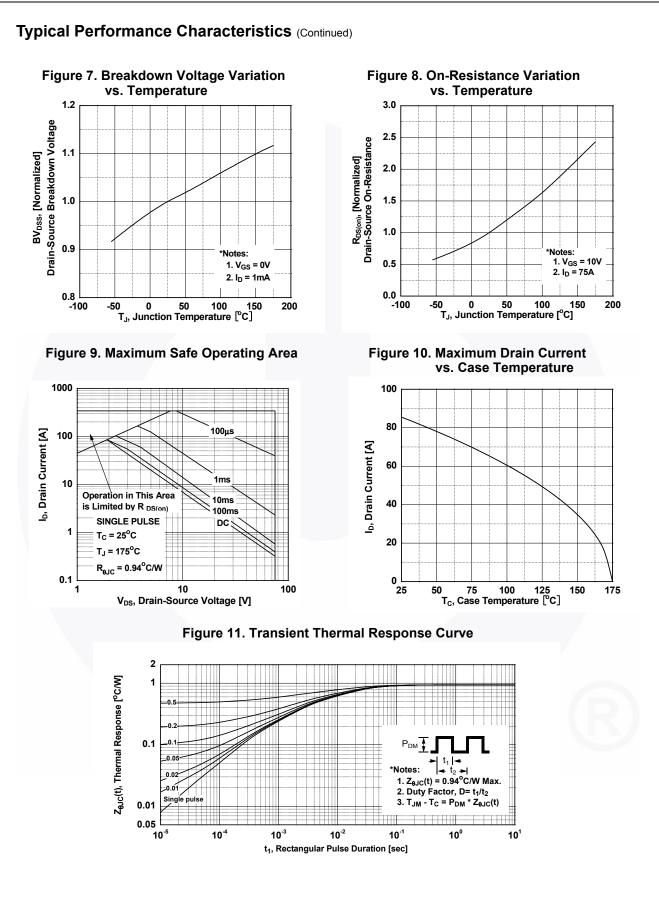
Symbol	Parameter	FDB088N08	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.94	
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	62.5	°C/W
	Thermal Resistance, Junction to Ambient (1 in <sup>2</sup> Pad of 2-oz Copper), Max.	40	

November 2013

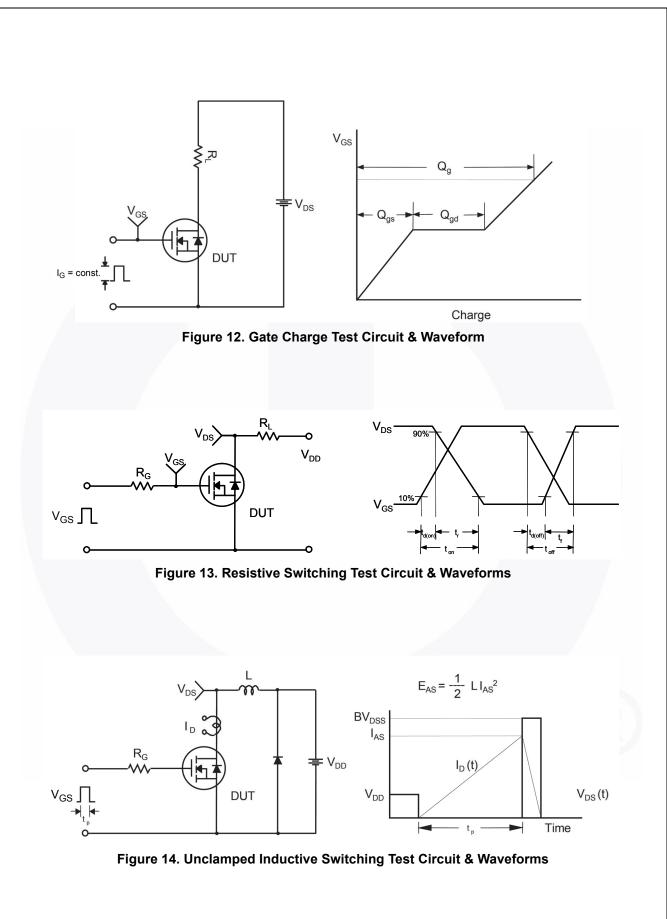
Part Nu	Part Number Top Mark Pa		Package	Packing Method	Reel Size	Тар	e Width	Qua	ntity
FDB08	•		D <sup>2</sup> -PAK		330 mm		4 mm	800 units	
Electrica	al Chara	acteristics T <sub>c</sub> = 2	25°C unless	otherwise noted.		<b>I</b>			
Symbol		Parameter		Test Conditio	ons	Min.	Тур.	Max.	Unit
Off Chara	cteristics	5							
BV <sub>DSS</sub>	Drain to	Source Breakdown Volt	age	I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0 V,	. T <sub>C</sub> = 25 <sup>o</sup> C	75	-	-	V
$\Delta BV_{DSS}$		wn Voltage Temperature	~	$I_D = 250 \ \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$					
$/\Delta T_J$	Coefficie	<b>U</b> 1				-	0.07	-	V/ºC
	Zoro Ga	to Voltago Drain Curren		$V_{DS}$ = 75 V, $V_{GS}$ = 0 V		-	-	1	
IDSS	Zeiu Ga	te Voltage Drain Current		$V_{DS} = 75 \text{ V}, \text{ T}_{C} = 150^{\circ}\text{C}$		-	-	500	μA
I <sub>GSS</sub>	Gate to I	Body Leakage Current		$V_{GS}$ = ±20 V, $V_{DS}$ = 0 V	/	-	-	±100	nA
On Chara	ctoristics								
		reshold Voltage		$1/2 - 1/2 = 250 \mu/2$	<u></u>	2.0	_	4.0	V
V <sub>GS(th)</sub>		reshold voltage		$V_{GS} = V_{DS}, I_D = 250 \mu$ A	<b>`</b>	2.0			-
R <sub>DS(on)</sub>		Transconductance		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 75 A V <sub>DS</sub> = 10 V, I <sub>D</sub> = 37.5 A			7.3 300	8.8	mΩ S
9 <sub>FS</sub>	FOIWaru	Transconductance		$V_{\rm DS} = 10  \text{v},  \text{I}_{\rm D} = 37.3  \text{A}$	•	-	300	-	3
Dynamic	Characte	ristics							
C <sub>iss</sub>	Input Ca	pacitance				-	4960	6595	pF
C <sub>oss</sub>		Capacitance		$V_{DS} = 25 V, V_{GS} = 0 V,$		-	355	470	pF
C <sub>rss</sub>		Transfer Capacitance		f = 1 MHz		-	200	300	pF
Q <sub>g(tot)</sub>		te Charge at 10V		V <sub>DS</sub> = 60 V, I <sub>D</sub> = 75 A,		-	91	118	nC
Q <sub>gs</sub>		Source Gate Charge		$V_{\rm DS} = 00  \text{V},  \text{I}_{\rm D} = 7.3  \text{A},  \text{V}_{\rm GS} = 10  \text{V}$	F	-	22	-	nC
Q <sub>gd</sub>		Drain "Miller" Charge		GS	(Note 4)	-	28	-	nC
R <sub>G</sub>	Gate Re	-		f = 1 MHz		-	-	4	Ω
	Charact	ariation	1		I				1
Switching								100	1
t <sub>d(on)</sub>		Delay Time		$V_{DD}$ = 37.5 V, I <sub>D</sub> = 75 A, R <sub>G</sub> = 25 Ω, V <sub>GS</sub> = 10 V (Note 4)		-	45	100	ns
t <sub>r</sub>		Rise Time				-	158	326	ns
t <sub>d(off)</sub>		Delay Time				-	244	498	ns
t <sub>f</sub>	Turn-Off	Fall Time					102	214	ns
Drain-Soι	irce Diod	e Characteristics							
I <sub>S</sub>	Maximun	n Continuous Drain to S	ource Diode	Forward Current		-	-	85	Α
I <sub>SM</sub>	Maximun	n Pulsed Drain to Source	e Diode Forv	Forward Current		-	-	340	Α
V <sub>SD</sub>	Drain to	Source Diode Forward \	/oltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 75 A		-	-	1.25	V
t <sub>rr</sub>	Reverse	Recovery Time		$V_{GS} = 0 V, I_{SD} = 75 A,$		-	41.1	-	ns
Q <sub>rr</sub>	Reverse	Recovery Charge		$dI_F/dt = 100 \text{ A}/\mu \text{s}$		-	80.7	-	nC



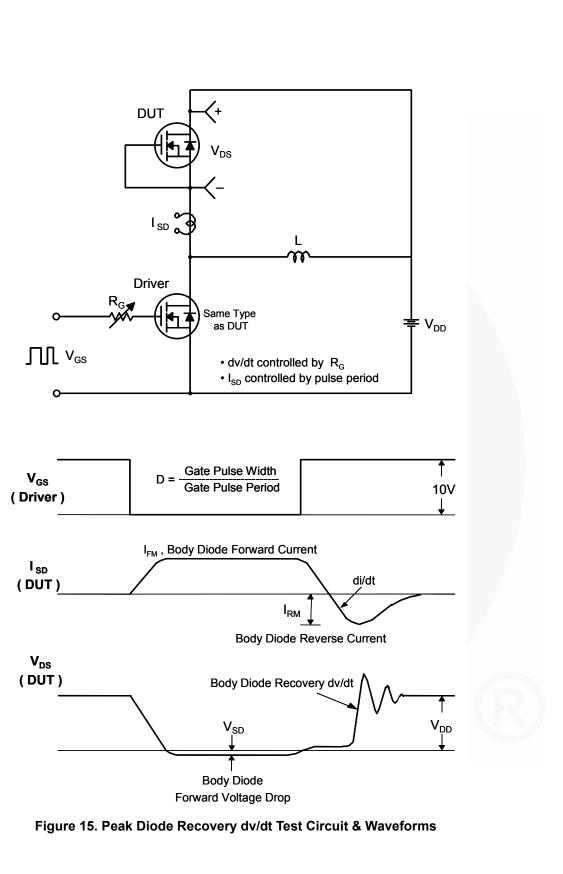
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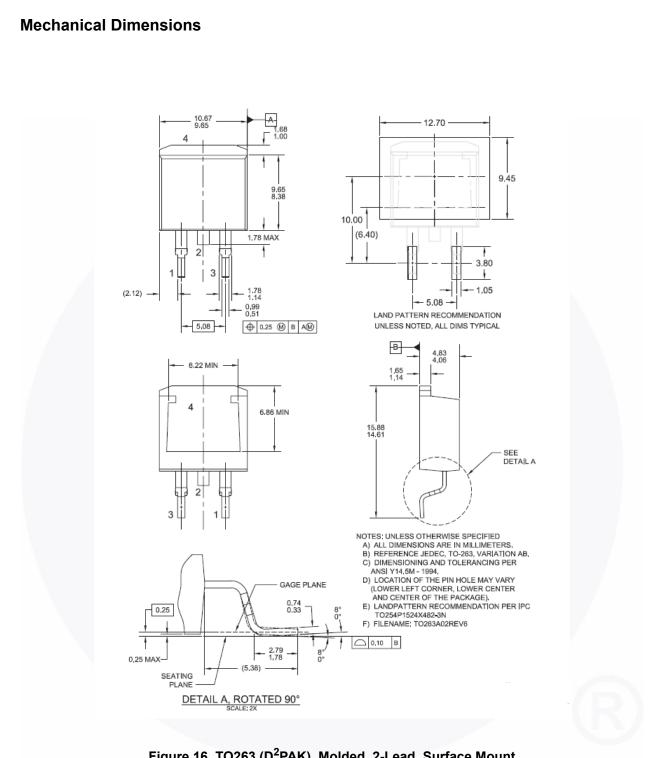


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## Figure 16. TO263 (D<sup>2</sup>PAK), Molded, 2-Lead, Surface Mount

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