### Thyristor Datasheet

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# MCR25DG, MCR25MG, MCR25NG Silicon Controlled Rectifiers



## **Additional Information**





Resources

Accessories

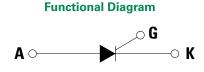
Samples

# Description

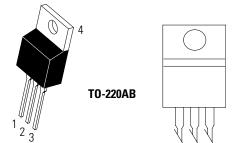
Designed primarily for half-wave ac control applications, such as motor controls, heating controls, and power supplies; or wherever half-wave, silicon gate-controlled devices are needed.

### **Features**

- Blocking Voltage to 800 Volts
- On–State Current Rating of 25 Amperes RMS
- High Surge Current Capability
  300 Amperes
- Rugged Economical TO-220AB Package
- Glass Passivated Junctions for Reliability and Uniformity
- Minimum and Maximum Values of IGT, VGT, and IH Specified for Ease of Design
- High Immunity to dv/dt 100 V/µsec Minimum at 125°C
- These are Pb–Free Devices



Pin Out



#### **Maximum Ratings** (TJ = 25°C unless otherwise noted)

| Rating   | Symbol                                 | Value                | Unit               |    |
|--|--|----------------------|--------------------|----|
| Peak Repetitive Off-State Voltage (Note 1)<br>(- 40 to 125°C, Sine Wave, 50 to 60 Hz, Gate Open) | V <sub>drm</sub> ,<br>V <sub>rrm</sub> | 400<br>600<br>800    | V                  |    |
| On-State RMS Current (180° Conduction Angles; $T_c = 80$ °C)                                     |  | I <sub>T (RMS)</sub> | 25                 | А  |
| Peak Non-Repetitive Surge Current<br>(1/2 Cycle, Sine Wave 60 Hz, T <sub>J</sub> = 125°C)        | I <sub>TSM</sub>                       | 300                  | А                  |    |
| Circuit Fusing Consideration ( $t = 8.3 \text{ ms}$ )  | l²t                                    | 373                  | A <sup>2</sup> sec |    |
| Forward Peak Gate Power (Pulse Width $\leq$ 1.0 $\mu sec, T_c$ = 80°C                            | P <sub>GM</sub>                        | 20.0                 | W                  |    |
| Forward Average Gate Power (t = 8.3 msec, $T_c = 80^{\circ}$ C)                                  | P <sub>GM (AV)</sub>                   | 0.5                  | W                  |    |
| Forward Peak Gate Current (Pulse Width $\leq$ 1.0 µsec, T <sub>c</sub> = 80°C                    | I <sub>GM</sub>                        | 2.0                  | А                  |    |
| Operating Junction Temperature Range   | T,                                     | -40 to 125           | °C                 |    |
| Storage Temperature Range  |  | T <sub>sta</sub>     | -40 to 150         | °C |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Vorent and Verse, for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

#### **Thermal Characteristics**

| Rating   |  | Symbol                               | Value       | Unit |
|--|--|--------------------------------------|-------------|------|
| Thermal Resistance   | Junction-to-Case (AC)<br>Junction-to-Ambient | R <sub>sjc</sub><br>R <sub>sja</sub> | 1.5<br>62.5 | °C/W |
| Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds |  | TL                                   | 260         | °C   |

#### Electrical Characteristics - OFF (T<sub>1</sub> = 25°C unless otherwise noted)

| Characteristic  |                       | Symbol             | Min | Тур | Мах  | Unit |
|---|-----------------------|--------------------|-----|-----|------|------|
| Peak Repetitive Forward or Reverse Blocking Current       | $T_{J} = 25^{\circ}C$ | I <sub>DRM</sub> , | -   | -   | 0.01 | mA   |
| $(V_{AK} = Rated V_{DRM} \text{ or } V_{RRM}, Gate Open)$ | T_ = 125°C            | I <sub>RRM</sub>   | -   | -   | 2.0  | ШA   |

#### Electrical Characteristics - ON (T<sub>1</sub> = 25°C unless otherwise noted)

| Characteristic   | Symbol          | Min | Тур  | Мах | Unit |
|--|-----------------|-----|------|-----|------|
| Peak Forward On–State Voltage (Note 2) ( $I_{TM} = 50$ A)                                    | V <sub>TM</sub> | _   | _    | 1.8 | V    |
| Gate Trigger Current (Continuous dc) (V <sub>D</sub> = 12 V; R <sub>L</sub> = 100 $\Omega$ ) | I <sub>gt</sub> | 4.0 | 12   | 30  | mA   |
| Holding Current (Anode Voltage = 12 V, Initiating Current = 200 mA)                          | I <sub>H</sub>  | 5.0 | 13   | 40  | mA   |
| Latch Current ( $V_{\rm D}$ = 12 V, I <sub>g</sub> = 30 mA)                                  | Ι               | _   | 35   | 80  | mA   |
| Gate Trigger Voltage (Continuous dc) (V $_{\rm D}$ = 12 V, R $_{\rm L}$ = 100 $\Omega)$      | V <sub>gt</sub> | 0.5 | 0.67 | 1.0 | V    |

#### **Dynamic Characteristics**

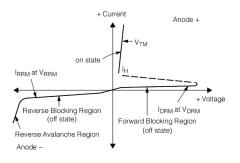
| Characteristic  | Symbol | Min | Тур | Мах | Unit |
|---|--------|-----|-----|-----|------|
| Critical Rate of Rise of Off–State Voltage<br>(V <sub>D</sub> = 67% of Rated V <sub>DRM</sub> , Exponential Waveform, Gate Open, T <sub>J</sub> = 125°C)              | dv/dt  | 100 | 250 | _   | V/µs |
| Critical Rate of Rise of On–State Current<br>( $I_{PK} = 50 \text{ A}, Pw = 30  \mu \text{sec},  \text{diG/dt} = 1  \text{A}/\mu \text{sec},  I_{gt} = 50  \text{mA}$ | di/dt  | -   | _   | 50  | A/µs |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. Pulse Test; Pulse Width ≤ 2.0 msec, Duty Cycle ≤ 2%

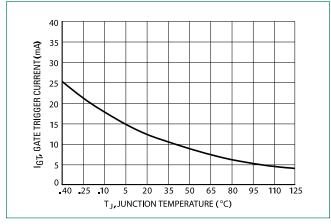
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## **Voltage Current Characteristic of SCR**

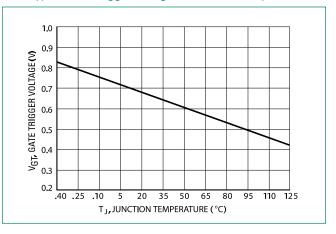
| Symbol           | Parameter                                 |  |  |  |  |
|------------------|---|--|--|--|--|
| V <sub>DRM</sub> | Peak Repetitive Forward Off State Voltage |  |  |  |  |
| I <sub>DRM</sub> | Peak Forward Blocking Current             |  |  |  |  |
| V <sub>RRM</sub> | Peak Repetitive Reverse Off State Voltage |  |  |  |  |
| I <sub>RRM</sub> | Peak Reverse Blocking Current             |  |  |  |  |
| V <sub>TM</sub>  | Maximum On State Voltage                  |  |  |  |  |
| I <sub>H</sub>   | Holding Current                           |  |  |  |  |



**Figure 1.** Typical Gate Trigger Current vs Junction Temperature



**Figure 2.** Typical Gate Trigger Voltage vs Junction Temperature





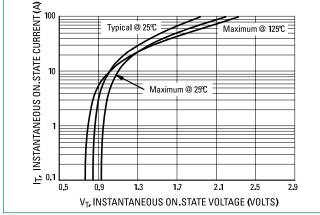
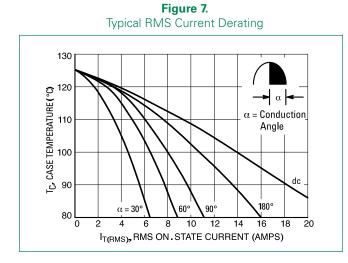


Figure 4. **Transient Thermal Response**  $R_{(t)}$  TRANSIEN T THERMALR (NORMALIZED)  $Z_{\Theta JC(t)} = R_{\Theta JC} \bullet R(t)$ 0.1 0.0 0.1 1000 1•10<sup>4</sup> 1 10 100 t, TIME (ms)

# MCR25DG, MCR25MG, MCR25NG Silicon Controlled Rectifiers



On State Power Dissipation P(AV), AVERAGE POWER DISSIPATION (WATTS) 32 28 180° dc 24 α 90° 60°  $\alpha$  = Conduction 20 Angle 16  $\alpha = 30^{\circ}$ 12 8 4 0 6 8 14 16 2 4 10 12 18 20 0 IT(AV), AVERAGE ON . STATE CURRENT (AMPS)

Figure 8.

**Figure 9.** Typical Exponential Static dv/dt Versus Peak Voltage

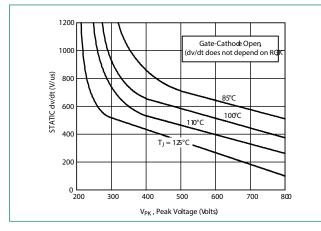
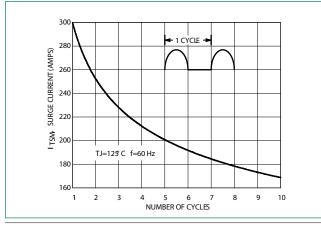
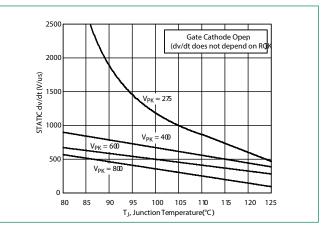


Figure 11. Maximum Non–Repetitive Surge Current



**Figure 10.** Typical Exponential Static dv/dt Vs Junction Temperature



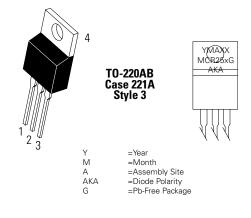
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**Dimensions** 

## в С Т - S Q Α U-H ▲ к 7 J

R

**Part Marking System** 



|     | Inches |       | Millin | neters |  |
|-----|--------|-------|--------|--------|--|
| Dim | Min    | Мах   | Min    | Max    |  |
| Α   | 0.590  | 0.620 | 14.99  | 15.75  |  |
| В   | 0.380  | 0.420 | 9.65   | 10.67  |  |
| С   | 0.178  | 0.188 | 4.52   | 4.78   |  |
| D   | 0.025  | 0.035 | 0.64   | 0.89   |  |
| F   | 0.142  | 0.147 | 3.61   | 3.73   |  |
| G   | 0.095  | 0.105 | 2.41   | 2.67   |  |
| Н   | 0.110  | 0.130 | 2.79   | 3.30   |  |
| J   | 0.018  | 0.024 | 0.46   | 0.61   |  |
| К   | 0.540  | 0.575 | 13.72  | 14.61  |  |
| L   | 0.060  | 0.075 | 1.52   | 1.91   |  |
| Ν   | 0.195  | 0.205 | 4.95   | 5.21   |  |
| ٥   | 0.105  | 0.115 | 2.67   | 2.92   |  |
| R   | 0.085  | 0.095 | 2.16   | 2.41   |  |
| S   | 0.045  | 0.060 | 1.14   | 1.52   |  |
| т   | 0.235  | 0.255 | 5.97   | 6.47   |  |
| U   | 0.000  | 0.050 | 0.00   | 1.27   |  |
| V   | 0.045  |       | 1.15   |        |  |
| Z   |        | 0.080 |        | 2.04   |  |

| Pin Assignment |           |  |  |  |
|----------------|-----------|--|--|--|
| 1              | 1 Cathode |  |  |  |
| 2              | Anode     |  |  |  |
| 3              | Gate      |  |  |  |
| 4 Anode        |           |  |  |  |
|                |           |  |  |  |

### **Ordering Information**

| Device  | Package               | Shipping         |  |  |
|---------|-----------------------|------------------|--|--|
| MCR25DG | <b>TO</b>             |                  |  |  |
| MCR25MG | TO-220AB<br>(Pb-Free) | 1000 Units / Box |  |  |
| MCR25NG |                       |                  |  |  |

1. Dimensioning and tolerancing per ansi y14.5m, 1982.

Controlling dimension: inch.
Dimension z defines a zone where all body and lead irregularities are allowed.

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