



## **Differential LVPECL** Voltage Controlled **Crystal Oscillator**

CVPD-920 Model 9×14 mm SMD, 3.3V, LVPECL

**Frequency Range: Frequency Pulling: Temperature Range:** (Option X)

Storage: Input Voltage: **Control Voltage:** Input Current: Output:

Aging:

Symmetry: **Rise/Fall Time:** Linearity: Logic:

Disable Time: Start-up Time: Phase Jitter: 12kHz to 80MHz Phase Noise: 10Hz 100Hz 1kHz 10kHz 100kHz

0°C to 70°C -40°C to 85°C -45°C to 90°C 3.3V ±0.3V 1.65V ±1.65V 88mA Max **Differential LVPECL** 45/55% Max @ zero crossing point 1ns Max (20% to 80%) ±10% Max Terminated to Vcc-2V into 50 ohms "0" = Vcc-1.85V Min, Vcc-1.62V Max "1" = Vcc-1.02V Min, Vcc-0.81V Max 200ns 1ms Typical, 2ms Max 0.5ps Typical, 1ps RMS Max -65 dBc/Hz Typical -98 dBc/Hz Typical -125 dBc/Hz Typical -140 dBc/Hz Typical -145 dBc/Hz Typical <3ppm 1<sup>st</sup> year, <1ppm every year thereafter

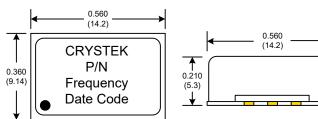
50 MHz to 125 MHz

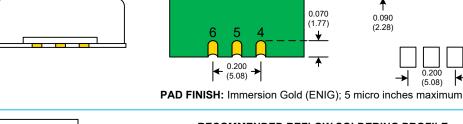
±20ppm APR Min





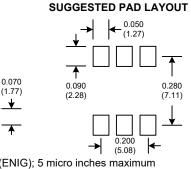
Designed to meet today's requirements for 3.3V Differential LVPECL applications. The CVPD-920 is produced using our cost saving FR5 PCB and UM-1 overtone crystal technology. This design offers considerable cost savings over other HFF VCXO products when broad frequency pulling is not required. Also available in 14 pin dip fully hermetic package.





0.100

(2.54)



**RECOMMENDED REFLOW SOLDERING PROFILE** 900034 (See App Note listed on website) http://www.crystek.com/specification/reflow/900034.pdf

**€**0.040

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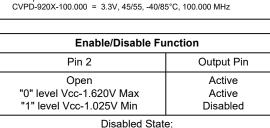
## NOT RECOMMENDED FOR NEW DESIGNS PLEASE USE CVPD-922 FAMILY

http://www.crystek.com/crystal/spec-sheets/vcxo/CVPD-922.pdf

PIN	Function
1	Control Volt
2	E/D
3	GND
4	OUT
5	COUT
6	Vcc

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**Crystek Part Number Guide** 

CVPD-920 X - 100.000 #2 #3

#1 Crystek 9×14 SMD PECL VCXO

#3 Temp. Range: Blank = 0/70°C, X = -40/85°C

#4 Frequency in MHz: 3 or 6 decimal places

#2 Model 920

Example:

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Pin 4 will assume a fixed level of logic "0" Pin 5 will assume a fixed level of logic "1"

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