1N4151W-G

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Vishay Semiconductors

Small Signal Fast Switching Diode



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DESIGN SUPPORT TOOLS



MECHANICAL DATA

Case: SOD-123

Weight: approx. 9.4 mg

Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

FEATURES

- Silicon epitaxial planar diode
- Fast switching diode
- AEC-Q101 qualified available (part number on request)
- Base P/N-G3 green, commercial grade
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
1N4151W-G	1N4151W-G3-08 or 1N4151W-G3-18	Single	AL	Tape and reel	

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	50	V	
Repetitive peak reverse voltage		V _{RRM}	75	V	
Average rectified current half wave rectification with resistive load ⁽¹⁾	f ≥ 50 Hz	I _{F(AV)}	150	mA	
Surge current	t < 1 s and T _j = 25 °C	I _{FSM}	500	mA	
Power dissipation ⁽¹⁾		P _{tot}	410	mW	

THERMAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air ⁽¹⁾		R _{thJA}	450	K/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-65 to +150	°C	
Operating temperature range		T _{op}	-55 to +150	C°	

Note

⁽¹⁾ Valid provided that electrodes are kept at ambient temperature

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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 50 mA	V _F			1.0	V
Lookogo ourront	V _R = 50 V	I _R			50	nA
Leakage current	V _R = 20 V, T _j = 150 °C	I _R			50	μA
Reverse breakdown voltage	I _R = 5 μA (pulsed)	V _(BR)	75			V
Diode capacitance	$V_F = V_R = 0 V$	CD			2	pF
Poveroo rocovery time	$I_F = 10 \text{ mA}, I_R = 10 \text{ mA}$ $i_R = 1 \text{ mA}$	t _{rr}			4	ns
Reverse recovery time	$I_F = 10 \text{ mA}, i_R = 1 \text{ mA}$ $V_R = 6 \text{ V}, R_L = 100 \Omega$	t _{rr}			2	ns

TYPICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)

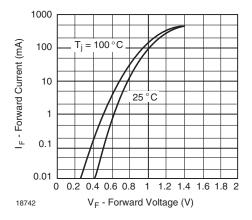


Fig. 1 - Forward Current vs. Forward Voltage

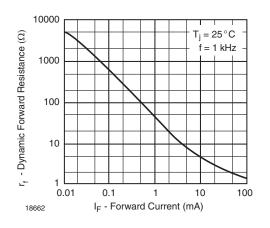


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

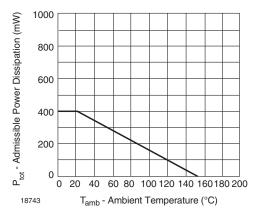


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

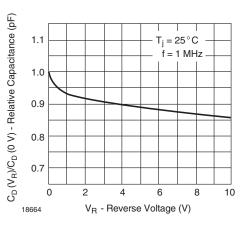


Fig. 4 - Relative Capacitance vs. Reverse Voltage

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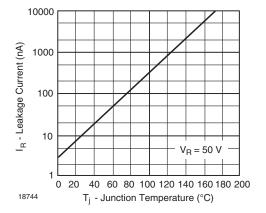


Fig. 5 - Leakage Current vs. Junction Temperature

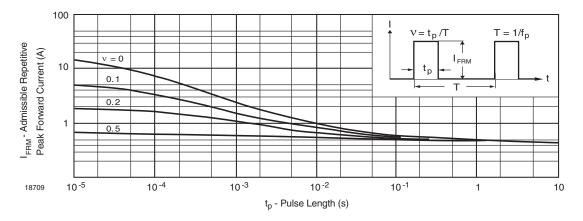
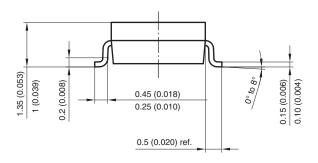


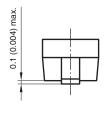
Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

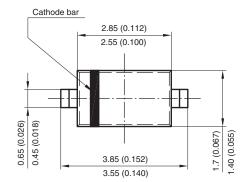


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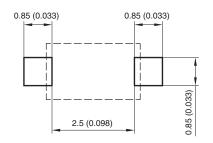
PACKAGE DIMENSIONS in millimeters (inches): SOD-123







Mounting Pad Layout



Rev. 4 - Date: 24. Sep. 2009 Document no.: S8-V-3910.01-001 (4) 17432

Rev. 1.1, 22-Feb-18 **4** Document Number: 85408 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



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