# Low ESR, Resin-Molded Chip J-Lead





#### **FEATURES**

- Compliant to the RoHS3 directive 2015/863/EU
- SMD J-Lead
- Low ESR
- 100% Surge Current Tested

#### **APPLICATIONS**

General Medium Power DC/DC Convertors

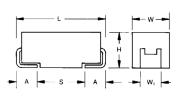




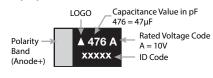
## **CASE DIMENSIONS:** millimeters (inches)

Code		EIA Code	EIA Metric	L ± 0.20 (0.008)	W ± 0.20 (0.008) -0.10 (0.004)	H ± 0.20 (0.008) -0.10 (0.004)	W <sub>1</sub> ± 0.20 (0.008)	A ± 0.30 (0.012) -0.20 (0.008)	S Min.
	В	1210	3528-21	3.50 (0.138)	2.80 (0.110)	1.90 (0.075)	2.20 (0.087)	0.80 (0.031)	1.40 (0.055)
Г	С	2312	6032-28	6.00 (0.236)	3.20 (0.126)	2.60 (0.102)	2.20 (0.087)	1.30 (0.051)	2.90 (0.114)
Г	N	2917	7343-31	7.30 (0.287)	4.30 (0.169)	2.90 (0.114)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)

W<sub>1</sub> dimension applies to the termination width for a dimensional area only







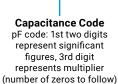
4V	G	16V	С	35V	V
6.3V	J	20V	D		
10V	Α	25V	Е		

<sup>\*</sup>Capacitance code of "P" case products are as shown below.

## **HOW TO ORDER**







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#### **TECHNICAL SPECIFICATIONS**

Category Temperature Range	-55 to +125°C
Rated Temperature	+85°C
Capacitance Tolerance	±20%, ±10% at 120Hz
Dissipation Factor	Refer to next page
ESR 100kHz	Refer to next page
Leakage Current	After 1 minute's application of rated voltage, leakage current at 20°C
	is not more than 0.01CV or 0.5µA, whichever is greater.
	After 1 minute's application of rated voltage, leakage current at 85°C
	is not more than 0.1CV or 5µA, whichever is greater.
	After 1 minute's application of derated voltage, leakage current at
	125°C is not more than 0.125CV or 6.3µA, whichever is greater.
Capacitance Change By Temperature	+15% Max. at +125°C
	+10% Max. at +85°C
	-10% Max. at -55°C

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#### **CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)**

Capacitance		Rated Voltage									
μF	Code	4V (0G)	6.3V (0J)	10V (1A)	16V (1C)	20V (1D)	25V (1E)	35V (1V)			
6.8	685							С			
10	106						С	N			
15	156					С		N			
22	226				В		N	N			
33	336				B/C		N				
47	476			В	N	N	N				
68	686			С							
100	107		С	С	N						
150	157	С	С	N							
220	227	С	C/N	N							
330	337	N	N	N							
470	477	N	N								
680	687	N									

Released ratings

#### **RATINGS & PART NUMBER REFERENCE**

Part Number	Case	Capacitance	Rated	DCL	DF © 120U =	ESR	100kl	MSL		
Part Number	Size	· (μF)	Voltage (V)	(μΑ)	@ 120Hz (%)	@ 100kHz (mΩ)	25°C	85°C	125°C	IVISL
	4 Voit									
F910G157#CC	С	150	4	6.0	12	250	663	597	265	1
F910G227#CC	С	220	4	8.8	12	250	663	597	265	1
F910G337#NC	N	330	4	13.2	10	100	1225	1102	490	1
F910G477#NC	N	470	4	18.8	16	100	1225	1102	490	1
F910G687#NC	N	680	4	27.2	18	100	1225	1102	490	1
	6.3 Volt									
F910J107#CC	С	100	6.3	6.3	8	250	663	597	265	1
F910J157#CC	С	150	6.3	9.5	12	250	663	597	265	1
F910J227#CC	С	220	6.3	13.9	14	250	663	597	265	1
F910J227#NC	N	220	6.3	13.9	10	100	1225	1102	490	1
F910J337#NC	N	330	6.3	20.8	14	100	1225	1102	490	1
F910J477#NC	N	470	6.3	29.6	16	100	1225	1102	490	1
				10	Volt					
F911A476#BA	В	47	10	4.7	8	500	412	371	165	1
F911A686#CC	С	68	10	6.8	8	300	606	545	242	1
F911A107#CC	С	100	10	10.0	10	250	663	597	265	1
F911A157#NC	N	150	10	15.0	10	100	1225	1102	490	1
F911A227#NC	N	220	10	22.0	12	100	1225	1102	490	3
F911A337#NC	N	330	10	33.0	18	100	1225	1102	490	3
				16	Volt					
F911C226#BA	В	22	16	3.5	8	950	299	269	120	1
F911C336#BA	В	33	16	5.3	8	950	299	269	120	1
F911C336#CC	С	33	16	5.3	6	400	524	472	210	1
F911C476#NC	N	47	16	7.6	6	150	1000	900	400	1
F911C107#NC	N	100	16	16	10	100	1225	1102	490	3
				20	Volt					
F911D156#CC	С	15	20	3	6	450	494	445	198	1
F911D476#NC	N	47	20	9.4	8	200	866	779	346	1
	25 Volt									
F911E106#CC	С	10	25	2.5	6	450	494	445	198	1
F911E226#NC	N	22	25	5.5	6	200	866	779	346	1
F911E336#NC	N	33	25	8.3	8	200	866	779	346	1
F911E476#NC	N	47	25	11.8	8	250	775	697	310	1
				35	Volt					
F911V685#CC	С	6.8	35	2.4	6	600	428	385	171	1
F911V106#NC	N	10	35	3.5	6	300	707	636	283	1
F911V156#NC	N	15	35	5.3	6	300	707	636	283	1
F911V226#NC	N	22	35	7.7	8	300	707	636	283	1

#: "M" for  $\pm 20\%$  tolerance, "K" for  $\pm 10\%$  tolerance. Moisture Sensitivity Level (MSL) is defined according to J-STD-020.





## **QUALIFICATION TABLE**

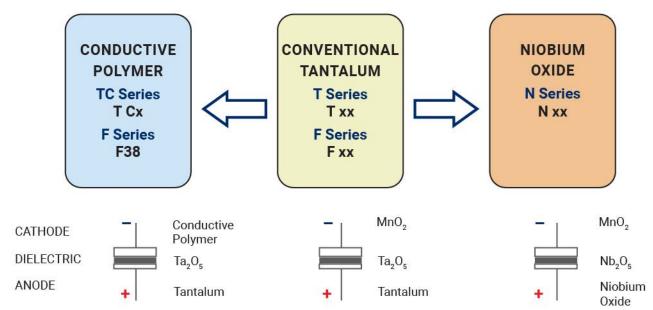
TECT	F91 series (Temperature range -55°C to +125°C)								
TEST	Condition								
Damp Heat (Steady State)	At 40°C, 90 to 95% R.H., 500 hours (No voltage applied) Capacitance Change Within ±10% of the initial value Dissipation Factor Initial specified value or less Leakage Current Initial specified value or less								
Temperature Cycles	-55°C / +125°C, 30 minutes each, 5 cycles Capacitance Change Within ±5% of the initial value Dissipation Factor								
Resistance to Soldering Heat	10 seconds reflow at 260°C, 5 seconds immersion at 260°C. Capacitance Change Within ±5% of the initial value Dissipation Factor Initial specified value or less Leakage Current Initial specified value or less								
Surge	After application of surge voltage in series with a 33Ω resistor at the rate of 30 seconds ON, 30 seconds OFF, for 100 successive test cycles at 85°C, capacitors shall meet the characteristic requirements in the table above.  Capacitance Change								
Endurance	After 2000 hours' application of rated voltage in series with a $3\Omega$ resistor at $85^{\circ}$ C, or derated voltage in series with a $3\Omega$ resistor at $125^{\circ}$ C, capacitors shall meet the characteristic requirements in the table above. Capacitance ChangeWithin $\pm 10\%$ of the initial value Dissipation Factor Initial specified value or less Leakage Current								
Shear Test	After applying the pressure load of 5N for 10±1 seconds horizontally to the center of capacitor side body which has no electrode and has been soldered beforehand on a substrate, there shall be found neither exfoliation nor its sign at the terminal electrode.								
Terminal Strength	Keeping a capacitor surface-mounted on a substrate upside down and supporting the substrate at both of the opposite bottom points 45mm apart from the center of capacitor, the pressure strength is applied with a specified jig at the center of substrate so that the substrate may bend by 1mm as illustrated. Then, there shall be found no remarkable abnormality on the capacitor terminals.								

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## SOLID ELECTROLYTIC CAPACITOR ROADMAP



### **FIVE CAPACITOR CONSTRUCTION STYLES**



#### **SERIES LINE UP: CONVENTIONAL SMD MnO**,

