Vishay General Semiconductor

# **High Current Density Surface-Mount Dual Common Cathode Schottky Rectifiers**



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## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 5.0 A				
V <sub>RRM</sub>	20 V, 30 V				
I <sub>FSM</sub>	200 A				
E <sub>AS</sub>	20 mJ				
$V_F$ at $I_F = 5$ A	0.338 V				
T <sub>J</sub> max.	150 °C				
Package	SMPC (TO-277A)				
Circuit configuration	Common cathode				

### **FEATURES**

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency
- · Low thermal resistance
- Meets MSL per J-STD-020. level 1. LF maximum peak of 260 °C
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **TYPICAL APPLICATIONS**

For use in high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters, and polarity protection application.

## **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3\_X - halogen-free, RoHS-compliant and AEC-Q101 gualified

("\_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	SS10P2CL	SS10P3CL	UNIT	
Device marking code			S102CL	S103CL		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	20	30	V	
Maximum average forward rectified current (fig. 1)	total device	lever.	10		А	
	per diode	I <sub>F(AV)</sub>	5.0			
Peak forward surge current 10 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	200		А	
Non-repetitive avalanche energy at 25 °C, I <sub>AS</sub> = 2 A per diode		E <sub>AS</sub>	20		mJ	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150		°C	

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FREE

SS10P2CL, SS10P3CL



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 2.5 A	T₄ = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.391	-		
	I <sub>F</sub> = 5.0 A			0.440	0.52		
	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 125 °C		0.272	-		
	I <sub>F</sub> = 5.0 A			0.338	0.42		
Reverse current per diode	Rated V <sub>B</sub>	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	95	850	μA	
	naleu v <sub>R</sub>	T <sub>A</sub> = 125 °C		37	55	mA	
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	560	-	pF	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	SS10P2CL	SS10P3CL	UNIT		
Typical thermal resistance per diade	R <sub>θJA</sub> <sup>(1)</sup>	60		°C/W		
Typical thermal resistance per diode	$R_{ extsf{ heta}JL}$	3				

#### Note

<sup>(1)</sup> Units mounted on recommended PCB 1 oz. pad layout

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
SS10P3CL-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel			
SS10P3CL-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel			
SS10P3CLHM3_A/H <sup>(1)</sup>	0.10	Н	1500	7" diameter plastic tape and reel			
SS10P3CLHM3_A/I (1)	0.10	I	6500	13" diameter plastic tape and reel			

Note

(1) AEC-Q101 qualified





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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise specified)

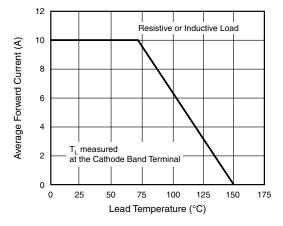


Fig. 1 - Maximum Forward Current Derating Curve

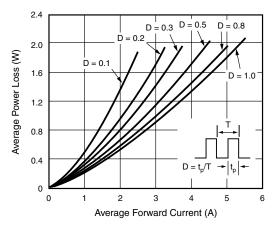


Fig. 2 - Forward Power Loss Characteristics Per Diode

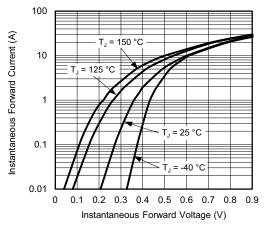


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

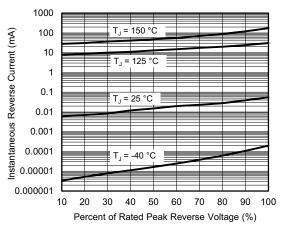


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

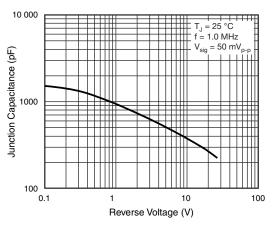


Fig. 5 - Typical Junction Capacitance Per Diode

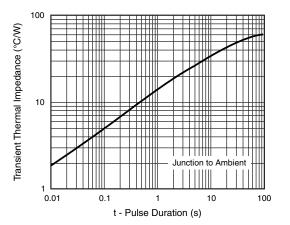


Fig. 6 - Typical Transient Thermal Impedance Per Diode

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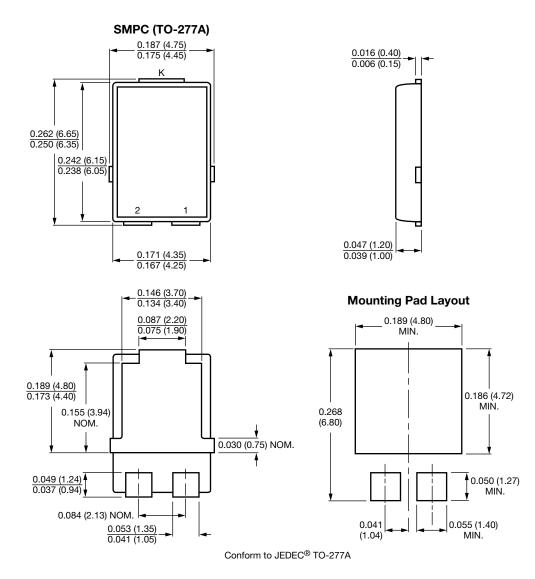
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## PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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