IXYS

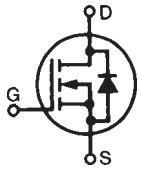
Advance Technical Information

High Voltage Power MOSFET

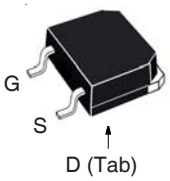
IXTT1N250HV

V_{DSS} = 2500V
I_{D25} = 1.5A
$R_{DS(on)}$ ≤ 40Ω

N-Channel Enhancement Mode
Fast Intrinsic Diode



TO-268S



G = Gate D = Drain
S = Source Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C	2500	V
V_{DGR}	T_J = 25°C to 150°C, R_{GS} = 1MΩ	2500	V
V_{GSS}	Continuous	±20	V
V_{GSM}	Transient	±30	V
I_{D25}	T_C = 25°C	1.5	A
I_{DM}	T_C = 25°C, Pulse Width Limited by T_{JM}	6	A
P_D	T_C = 25°C	250	W
T_J		- 55 ... +150	°C
T_{JM}		150	°C
T_{stg}		- 55 ... +150	°C
T_L	1.6mm (0.062 in.) From Case for 10s	300	°C
T_{SOLD}	Plastic Body for 10s	260	°C
Weight		4	g

Features

- High Blocking Voltage
- High Voltage Package
- Fast Intrinsic Diode
- Low Package Inductance

Advantages

- Easy to Mount
- Space Savings
- High Power Density

Applications

- High Voltage Power Supplies
- Capacitor Discharge
- Pulse Circuits

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0V, I_D = 250μA	2500			V
$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	2.0		4.0	V
I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
I_{DSS}	V_{DS} = 0.8 • V_{DSS}, V_{GS} = 0V			25	μA
	T_J = 125°C		25		μA
$R_{DS(on)}$	V_{GS} = 10V, I_D = 0.5 • I_{D25}, Note 1			40	Ω

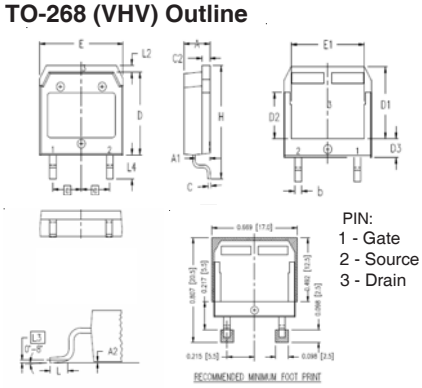
© 2012 IXYS CORPORATION, All Rights Reserved

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
g_{fs}	V_{DS} = 50V, I_D = 0.5A, Note 1	1.0	1.8		mS
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		1660		pF
C_{oss}			77		pF
C_{rss}			23		pF
$t_{d(on)}$	**Resistive Switching Times**		69		ns
t_r	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 1A		25		ns
$t_{d(off)}$	R_G = 5Ω (External)		132		ns
t_f			39		ns
$Q_{g(on)}$	V_{GS} = 10V, V_{DS} = 600V, I_D = 0.5A		41		nC
Q_{gs}			8		nC
Q_{gd}			16		nC
R_{thJC}				0.50	°C/W
R_{thCS}			0.21		°C/W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
I_S	V_{GS} = 0V			1.5	A
I_{SM}	Repetitive, Pulse Width Limited by T_{JM}			6	A
V_{SD}	I_F = 1A, V_{GS} = 0V, Note 1			1.5	V
t_{rr}	I_F = 1A, -di/dt = 100A/μs, V_R = 200V		2.5		μs

TO-268 (VHV) Outline



SYM	INCHES MIN	INCHES MAX	MILLIMETER MIN	MILLIMETER MAX
A	.193	.201	4.90	5.10
A1	.106	.114	2.70	2.90
A2	.001	.010	0.02	0.25
b	.045	.057	1.15	1.45
C	.016	.026	0.40	0.65
C2	.057	.063	1.45	1.60
D	.543	.551	13.80	14.00
D1	.465	.476	11.80	12.10
D2	.295	.307	7.50	7.80
D3	.114	.126	2.90	3.20
E	.624	.632	15.85	16.05
E1	.524	.535	13.30	13.60
e	.215 BSC		5.45 BSC	
H	.736	.752	18.70	19.10
L	.067	.079	1.70	2.00
L2	.039	.045	1.00	1.15
L3	.010 BSC		0.25 BSC	
L4	.150	.161	3.80	4.10

Note 1. Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.

ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or moreof the following U.S. patents: 4,835,592 4,860,072 4,881,106 4,931,844 5,017,508 5,034,796 5,049,961 5,063,307 5,187,117 5,237,481 5,381,025 5,486,715 6,162,665 6,259,123 B1 6,306,728 B1 6,404,065 B1 6,534,343 6,583,505 6,683,344 6,710,405 B2 6,710,463 6,727,585 6,759,692 6,771,478 B2 7,005,734 B2 7,063,975 B2 7,071,537 7,157,338B2

Fig. 1. Output Characteristics @ T_J = @ 25ºC

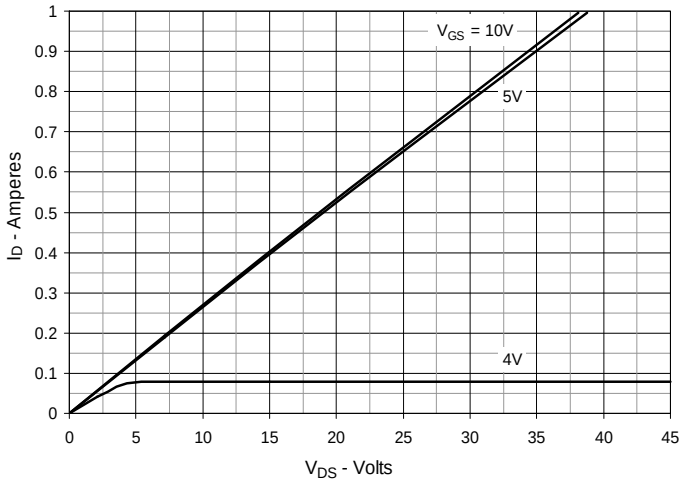


Fig. 2. Output Characteristics @ T_J = 125ºC

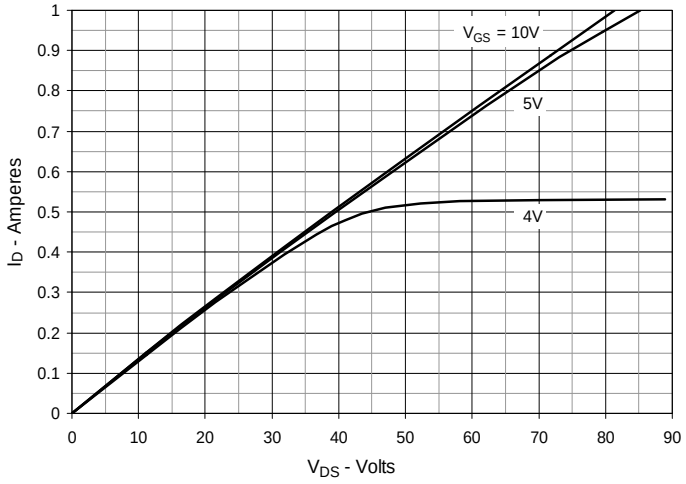


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 0.5A Value vs. Junction Temperature

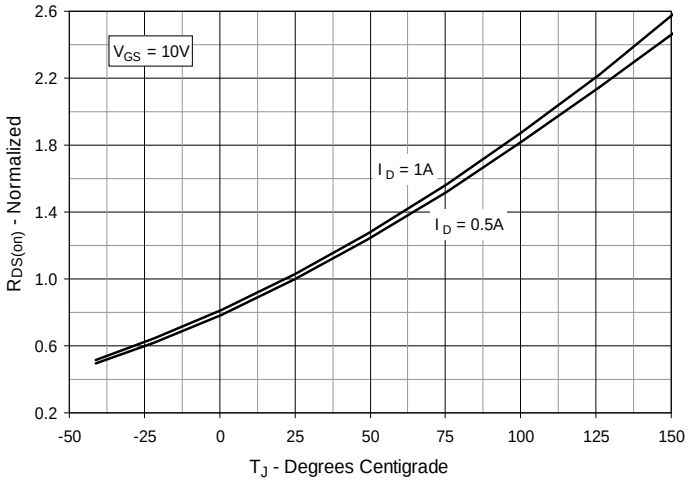


Fig. 3. $R_{DS(on)}$ Normalized to I_D = 0.5A Value vs. Drain Current

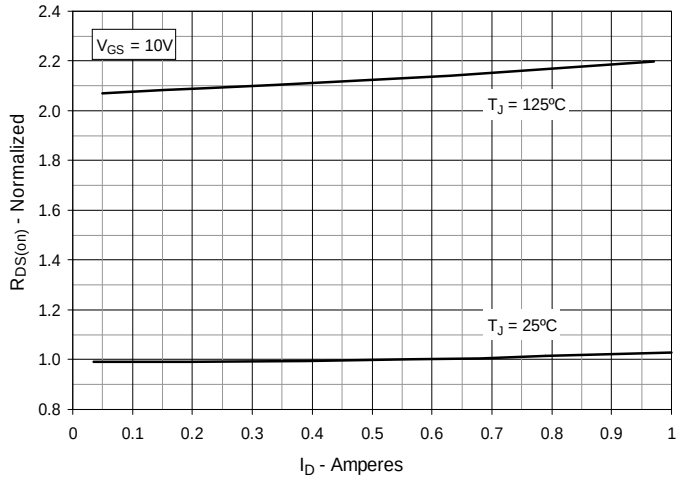


Fig. 5. Maximum Drain Current vs. Case Temperature

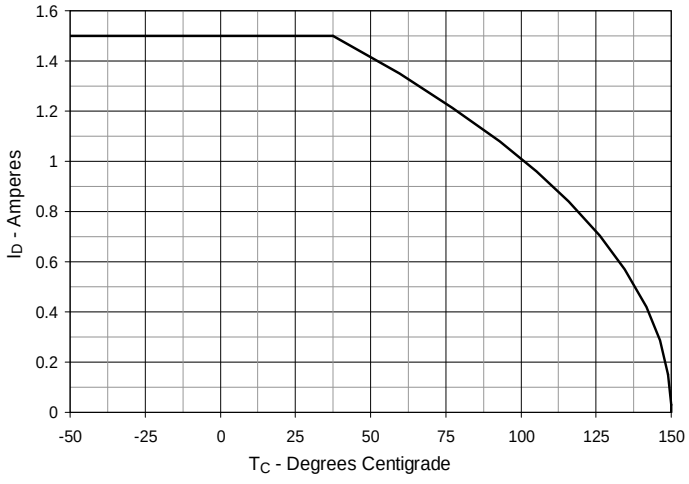
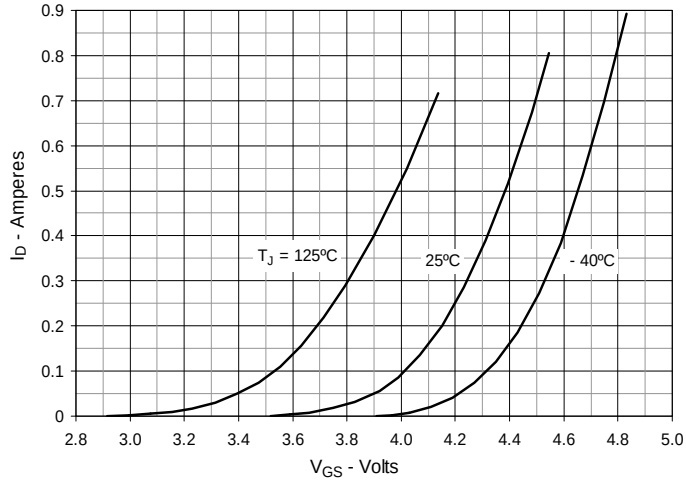


Fig. 6. Input Admittance



© 2012 IXYS CORPORATION, All Rights Reserved

Fig. 7. Transconductance

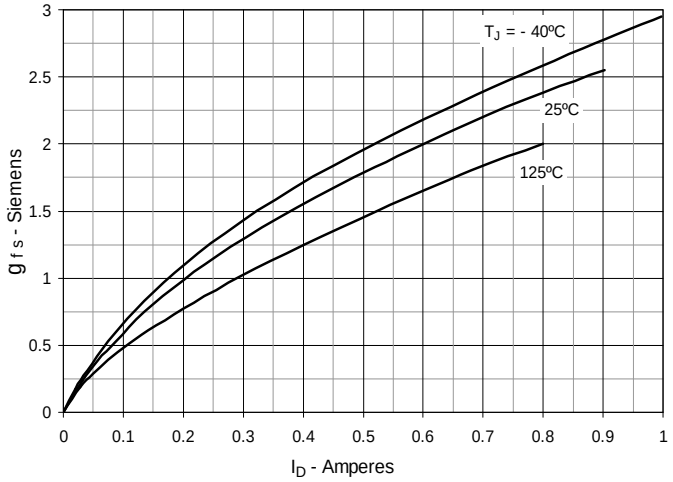


Fig. 8. Forward Voltage Drop of Intrinsic Diode

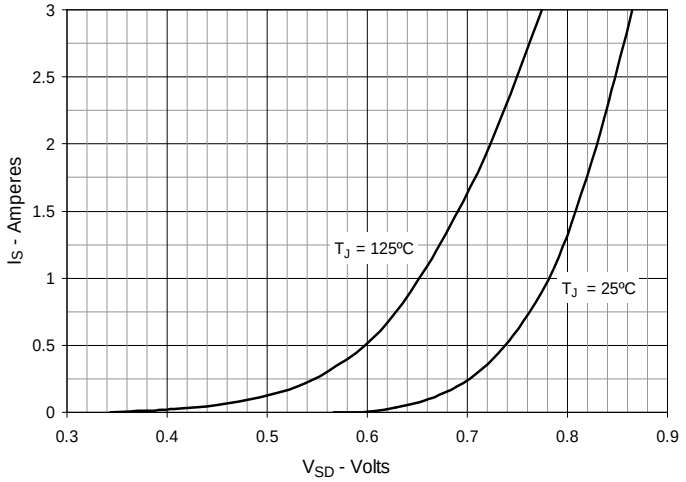


Fig. 9. Gate Charge

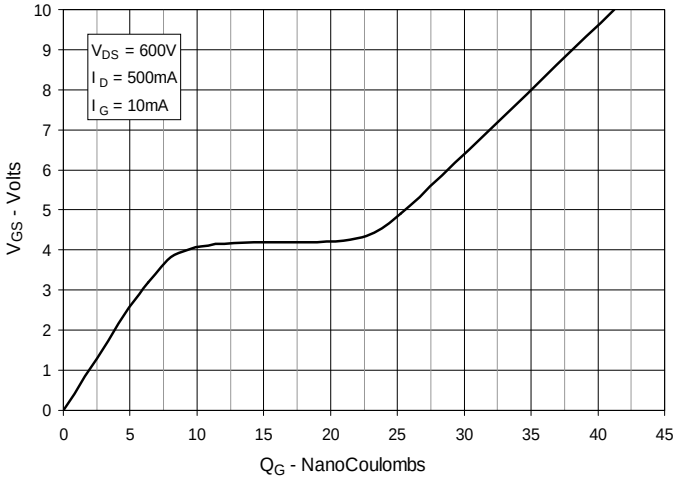


Fig. 10. Capacitance

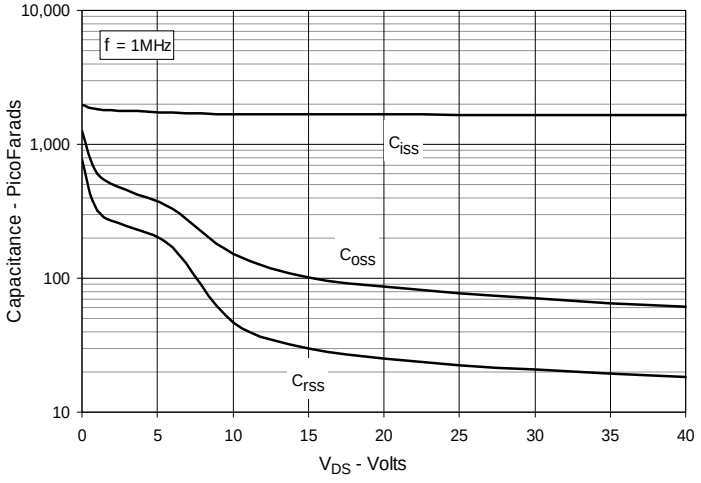


Fig. 11. Forward-Bias Safe Operating Area @ T_C = 25ºC

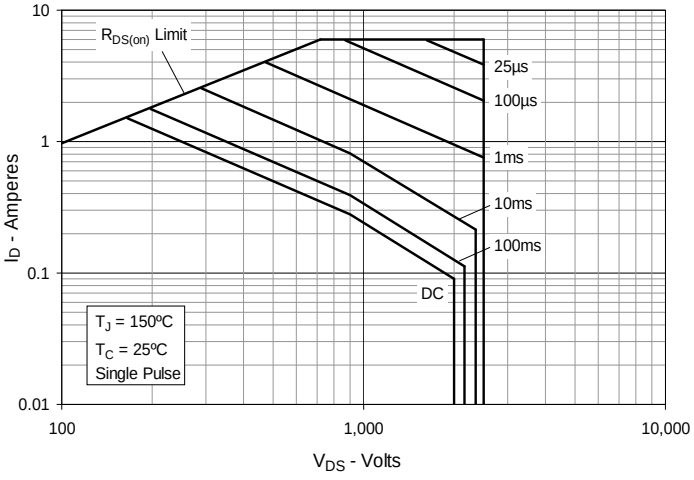
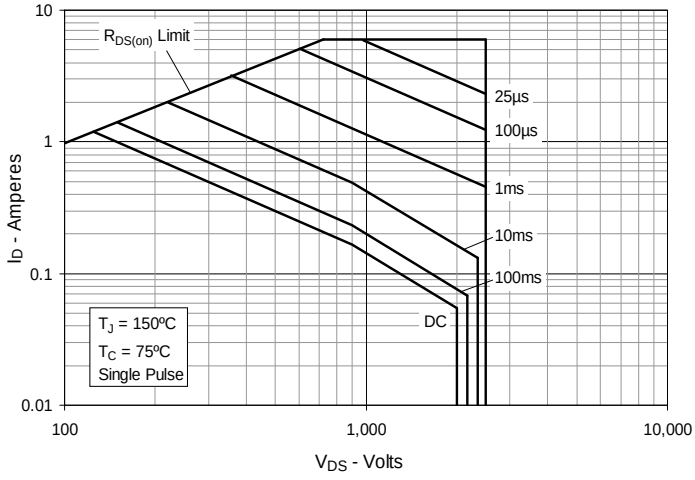
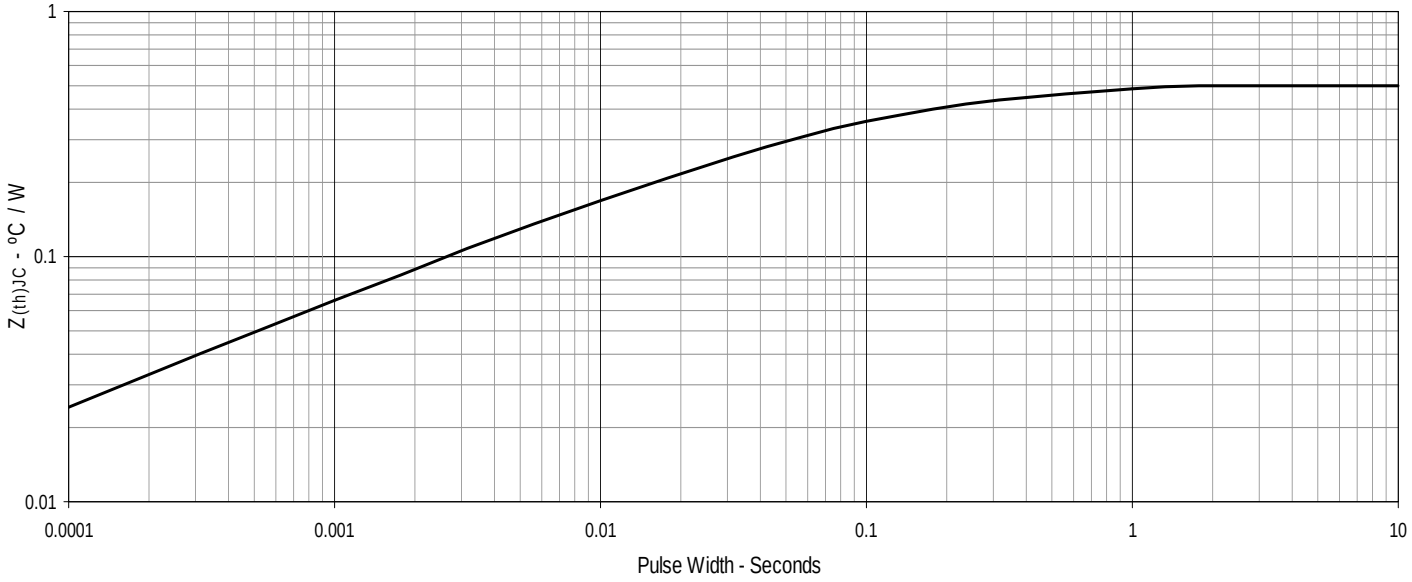


Fig. 12. Forward-Bias Safe Operating Area @ T_C = 75ºC



IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

Fig. 13. Maximum Transient Thermal Impedance



© 2012 IXYS CORPORATION, All Rights Reserved

Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at www.littelfuse.com/disclaimer-electronics.