

Parameter	Ratings	Units
Blocking Voltage	100	$V_p$
Load Current	300	$mA_{rms} / mA_{DC}$
On-Resistance (max)	8	$\Omega$

## Features

- 3750V<sub>rms</sub> Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- Higher Reliability than Electromechanical Relays
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Small 8-Pin Package
- Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Versions Available

## Applications

- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Industrial Controls

## Description

IXYS Integrated Circuits Division's LAA108 is a 100V, 300mA, 8 $\Omega$  dual single-pole, normally open (1-Form-A) Solid State Relay that has two independently controlled, optically coupled outputs. The output MOSFET switches and photovoltaic die employ optically coupled MOSFET technology to provide 3750 V<sub>rms</sub> of input-to-output isolation.

The relay outputs, that use patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

This dual single-pole OptoMOS relay provides a more compact design solution than two discrete single-pole relays in a variety of applications, saving board space by incorporating both switches in a single 8-Pin package.

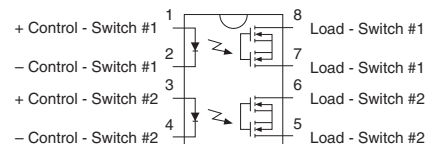
## Approvals

- UL Recognized Component: File # E76270
- CSA Certified Component: Certificate # 1175739
- TUV EN 62368-1: Certificate # B 082667 0008

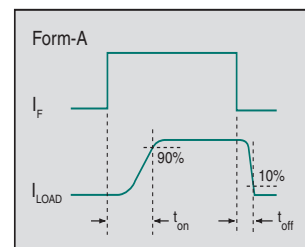
## Ordering Information

Part #	Description
LAA108	8-Pin DIP (50/Tube)
LAA108S	8-Pin Surface Mount (50/Tube)
LAA108STR	8-Pin Surface Mount (1,000/Reel)
LAA108P	8-Pin Flat Pack (50/Tube)
LAA108PTR	8-Pin Flat Pack (1,000/Reel)

## Pin Configuration



## Switching Characteristics of Normally Open (Form A) Devices



### Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	100	V <sub>P</sub>
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation <sup>1</sup>	150	mW
Total Power Dissipation <sup>2</sup>	800	mW
Isolation Voltage, Input to Output	3750	V <sub>rms</sub>
Operational Temperature, Ambient	-40 to +85	°C
Storage Temperature	-40 to +125	°C

<sup>1</sup> Derate linearly 1.33 mW / °C

<sup>2</sup> Derate output power linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

### Electrical Characteristics @ 25°C (Unless Otherwise Noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Blocking Voltage	I <sub>L</sub> =1μA	V <sub>DRM</sub>	100	-	-	V
Load Current						
Continuous <sup>1</sup>	-	I <sub>L</sub>	-	-	300	mA <sub>rms</sub> / mA <sub>DC</sub>
Peak	t=10ms	I <sub>LPK</sub>	-	-	±400	mA <sub>P</sub>
On-Resistance <sup>2</sup>	I <sub>L</sub> =300mA	R <sub>ON</sub>	-	4.5	8	Ω
Off-State Leakage Current	V <sub>L</sub> =100V <sub>P</sub>	I <sub>LEAK</sub>	-	-	1	μA
Switching Speeds						
Turn-On	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	0.43	3	ms
Turn-Off		t <sub>off</sub>	-	0.17	3	
Output Capacitance	I <sub>F</sub> =0mA, V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	110	-	pF
<b>Input Characteristics</b>						
Input Control Current to Activate <sup>3</sup>	I <sub>L</sub> =300mA	I <sub>F</sub>	-	0.5	2	mA
Input Control Current to Deactivate	-	-	0.2	0.3	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.36	1.5	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA
<b>Common Characteristics</b>						
Input to Output Capacitance	V <sub>IO</sub> =0V, f=1MHz	C <sub>IO</sub>	-	3	-	pF

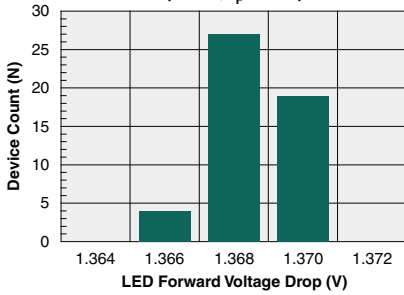
<sup>1</sup> If both poles operate simultaneously, the load current must be derated so as not to exceed the package total power dissipation value.

<sup>2</sup> Measurement taken within one (1) second of on-time.

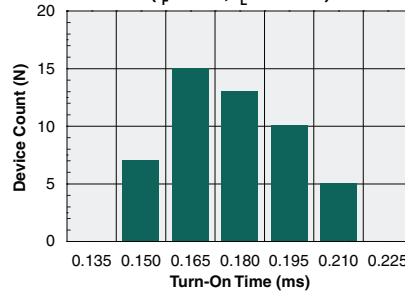
<sup>3</sup> For applications requiring high-temperature operation (T>60°C), a LED drive current of 4mA is recommended.

**PERFORMANCE DATA\***

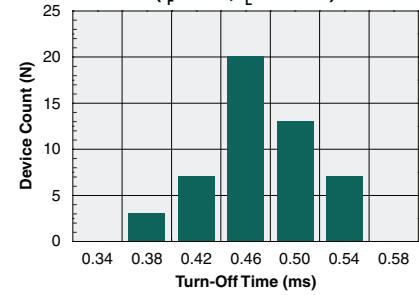
**Typical LED Forward Voltage Drop**  
(N=50,  $I_F=5\text{mA}$ )



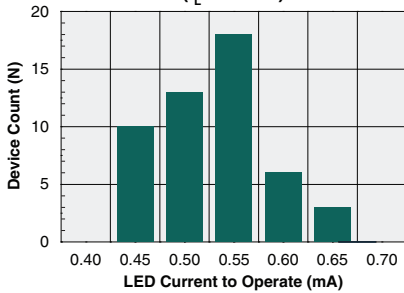
**Turn-On Time Distribution**  
( $I_F=5\text{mA}$ ,  $I_L=200\text{mA}$ )



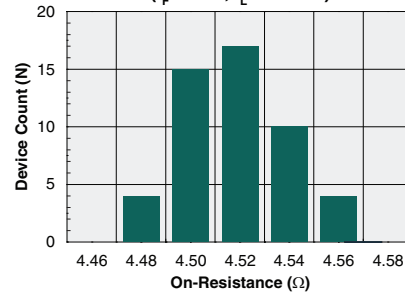
**Turn-Off Time Distribution**  
( $I_F=5\text{mA}$ ,  $I_L=200\text{mA}$ )



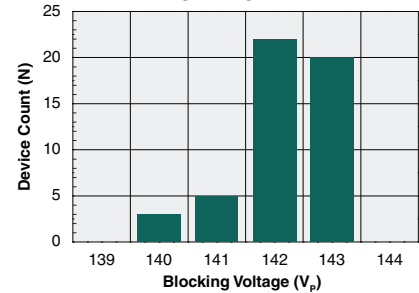
**LED Current to Operate Distribution**  
( $I_L=100\text{mA}$ )



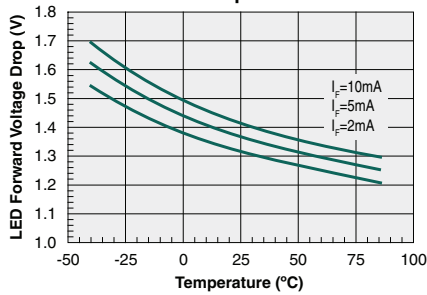
**On-Resistance Distribution**  
( $I_F=2\text{mA}$ ,  $I_L=200\text{mA}$ )



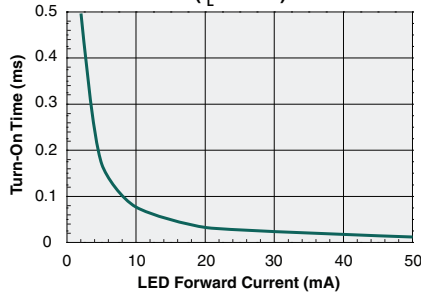
**Blocking Voltage Distribution**



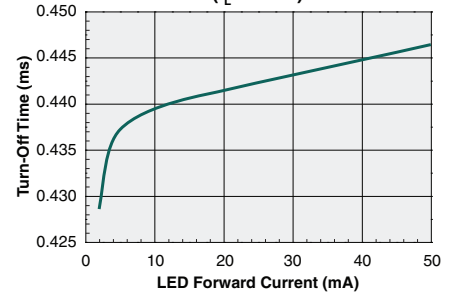
**Typical LED Forward Voltage Drop vs. Temperature**



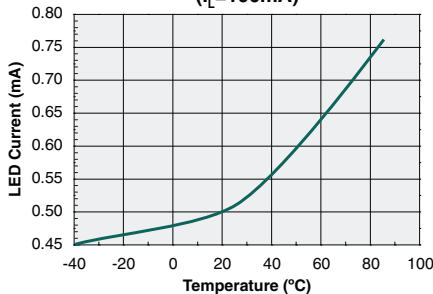
**Turn-On Time vs. LED Forward Current**  
( $I_L=80\text{mA}$ )



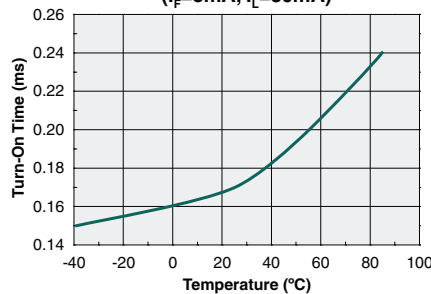
**Turn-Off Time vs. LED Forward Current**  
( $I_L=80\text{mA}$ )



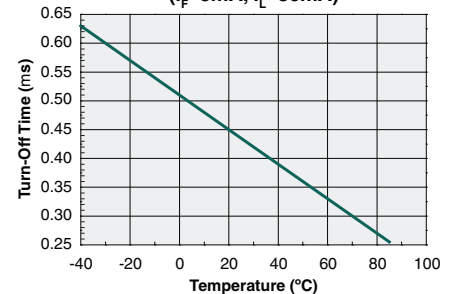
**LED Current to Operate vs. Temperature**  
( $I_L=100\text{mA}$ )



**Turn-On Time vs. Temperature**  
( $I_F=5\text{mA}$ ,  $I_L=80\text{mA}$ )

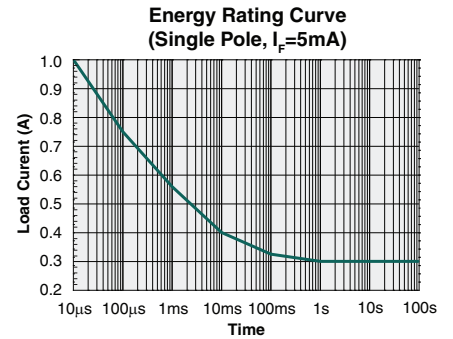
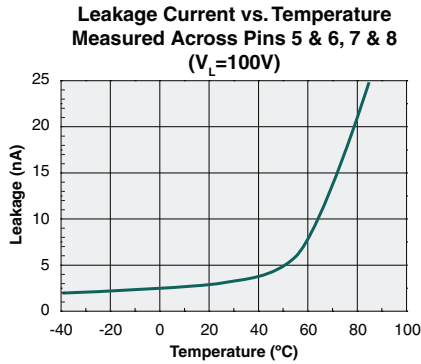
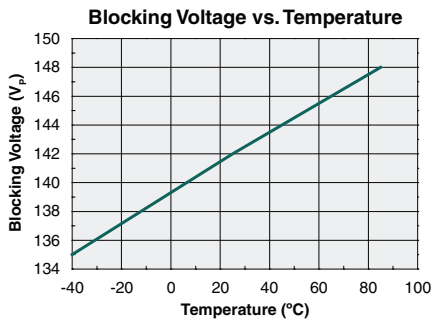
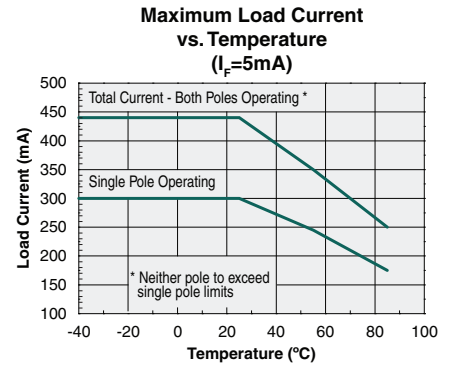
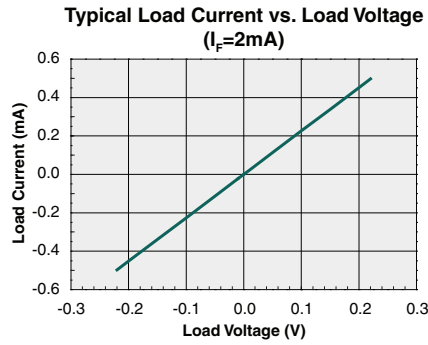
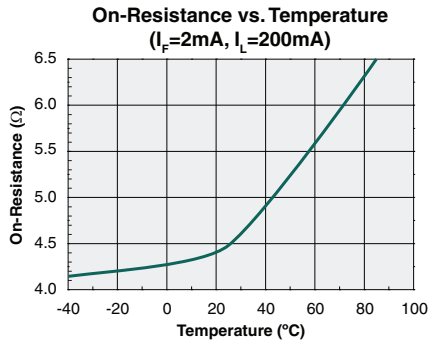


**Turn-Off Time vs. Temperature**  
( $I_F=5\text{mA}$ ,  $I_L=80\text{mA}$ )



\*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

PERFORMANCE DATA\*



\*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

## Manufacturing Information

### Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
LAA108S / LAA108P	MSL 1

### ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

### Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature ( $T_C$ ) and the maximum dwell time the body temperature of these surface mount devices may be ( $T_C - 5$ )°C or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

Device	Classification Temperature ( $T_C$ )	Dwell Time ( $t_p$ )	Max Reflow Cycles
LAA108S	250°C	30 Seconds	3
LAA108P	260°C	30 seconds	3

For through-hole devices, the maximum pin temperature and maximum dwell time through all solder waves is provided in the table below. Dwell time is the interval beginning when the pins are initially immersed into the solder wave until they exit the solder wave. For multiple waves, the dwell time is from entering the first wave until exiting the last wave. During this time, pin temperatures must not exceed the maximum temperature given in the table below. Body temperature of the device must not exceed the limit shown in the table below at any time during the soldering process.

Device	Maximum Pin Temperature	Maximum Body Temperature	Maximum Total Dwell Time	Wave Cycles
LAA108	260°C	250°C	10 seconds	1

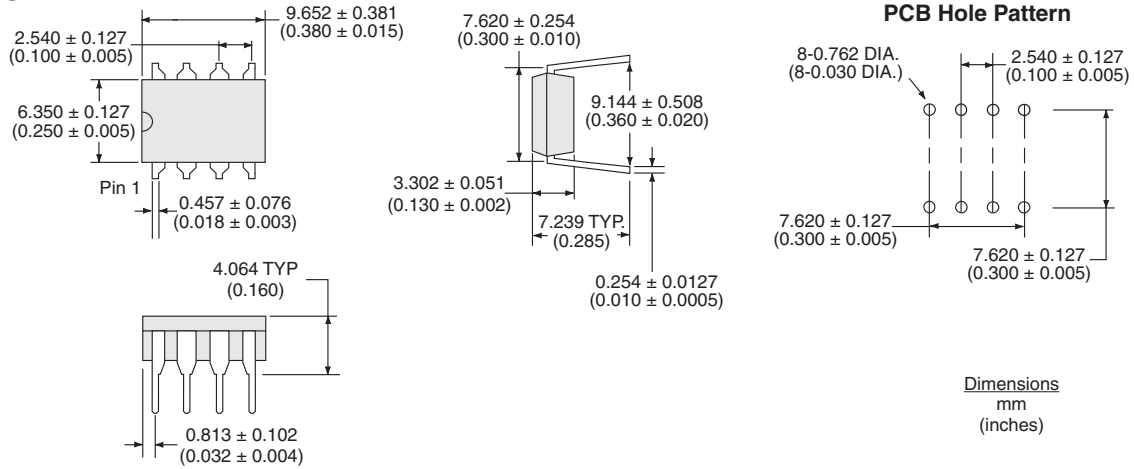
### Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.

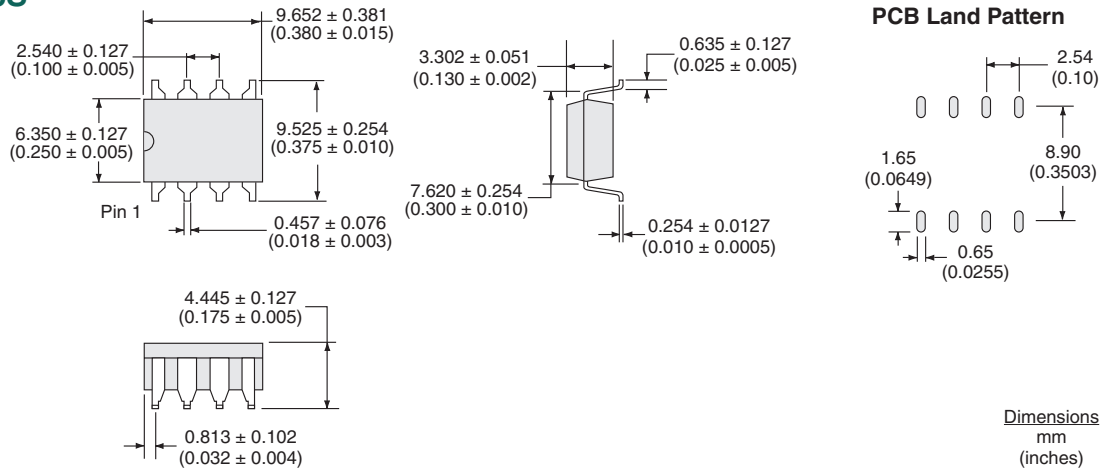


**MECHANICAL DIMENSIONS**

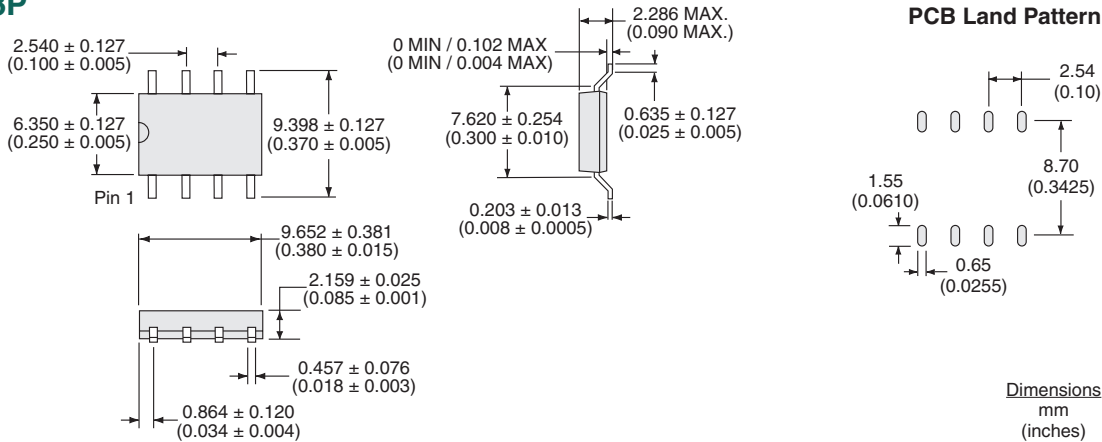
**LAA108**



**LAA108S**

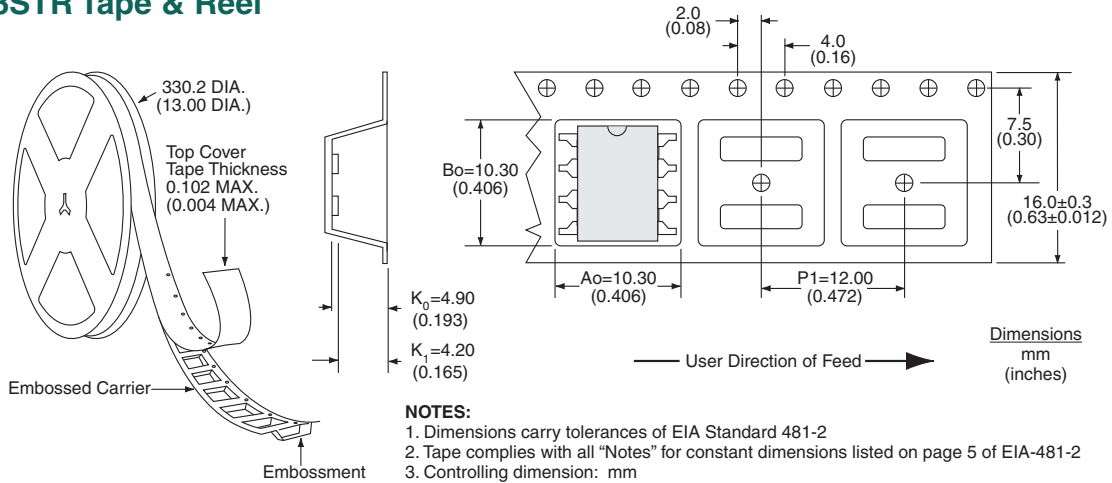


**LAA108P**

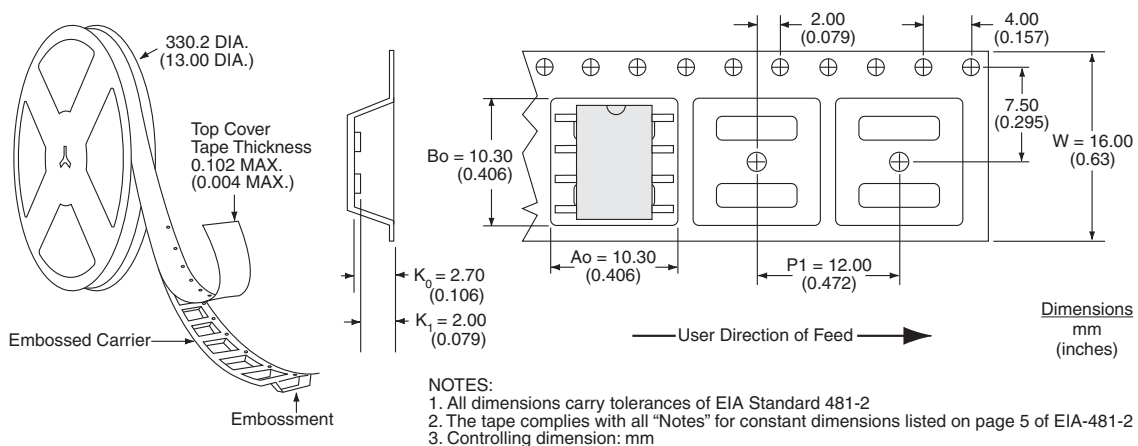


### MECHANICAL DIMENSIONS

#### LAA108STR Tape & Reel



#### LAA108PTR Tape & Reel



For additional information please visit our website at: <https://www.ixysic.com>