



Ultra Low Profile 0805 3 dB, 90° Hybrid Coupler

Description

The C1720J5003AHF is a low cost, low profile sub-miniature high performance 3 dB coupler in an easy to use surface mount package. It is designed for PCS, DCS, DECT, and WCDMA-3G applications. The C1720J5003AHF is ideal for balanced power and low noise amplifiers, plus signal distribution and other applications where low insertion loss and tight amplitude and phase balance are required. The C1720J5003AHF is available on tape and reel for pick and place high volume manufacturing.

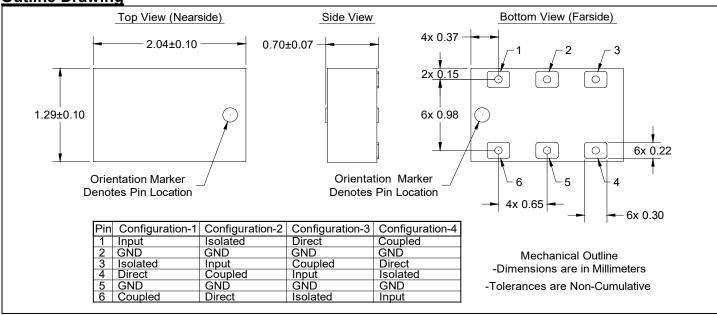
All of the Xinger components are constructed from ceramic filled PTFE composites which possess excellent electrical and mechanical stability. All parts have been subjected to rigorous qualification testing and units are 100% RF tested.

Detailed Electrical Specifications: Specifications subject to change without notice.

		ROOM (25°C)			
Features:	Parameter	Min.	Тур.	Мах	Unit
• 1700 – 2000 MHz	Frequency	1700		2000	MHz
0.7mm Height Profile	Port Impedance		50		Ω
 PCS, DCS, DECT, & WCDMA-3G 	Return Loss	21	27		dB
High Isolation & Low Loss	Isolation	24	36		dB
 Surface Mountable Tape & Reel 	Insertion Loss*		0.3	0.4	dB
Non-conductive Surface	Amplitude Balance		0.2	1.0	dB
RoHS Compliant	Phase Balance (relative to 90°)		1	5	Degrees
Halogen-Free	Power Handling @85C			4	Watts
	Power Handling @105C			2.5	Watts
	Operating Temperature	-55		+140	°C

* Insertion Loss stated at room temperature (Insertion Loss is approximately 0.1 dB higher at +85 °C)

Outline Drawing

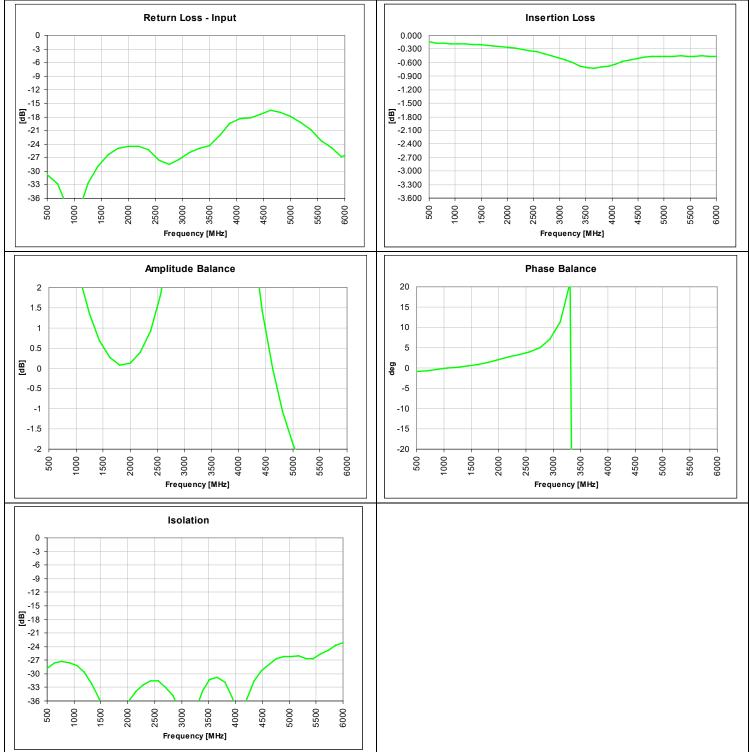




Visit us at www.anaren.com



Typical Broadband Performance: 500 MHz. to 6000 MHz.

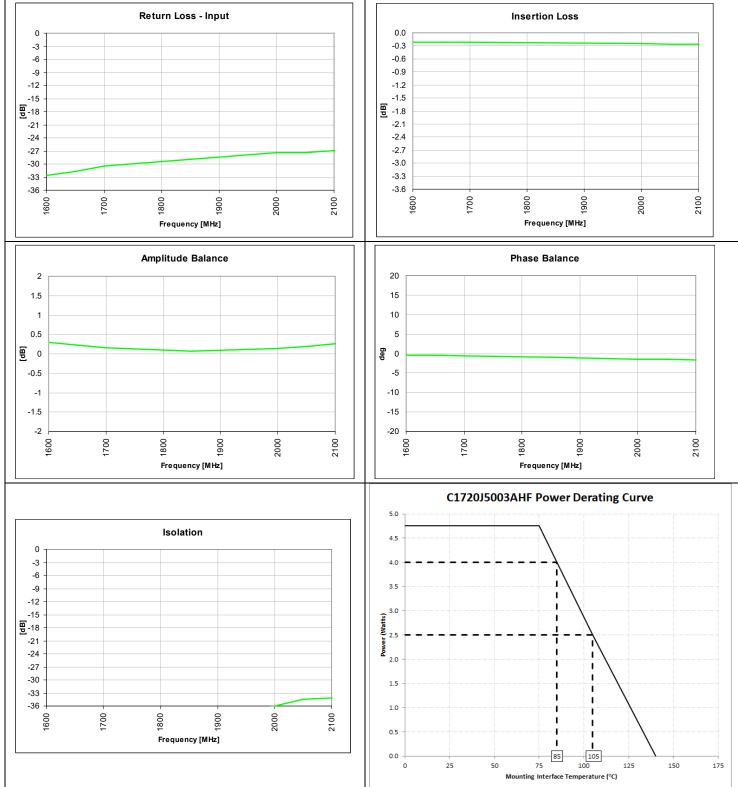




Visit us at www.anaren.com



Typical Performance: 1600 MHz. to 2100 MHz.





Visit us at www.anaren.com



Definition of Measured Specifications

Parameter	Definition	Mathematical Representation <i>i</i> , <i>j</i> , <i>k</i> , <i>m</i> is denoted as the port index of input, isolated, direct and coupled port for specific pin configuration shown in the table
Return Loss	The impedance match of the coupler to a 50Ω system. Return Loss is an alternate means to express VSWR.	$20\log_{10}(\left S_{ii}\right)$
Isolation	The input power divided by the sum of the power at the two output ports.	$20\log_{10}\left S_{ji}\right $
Insertion Loss	The input power divided by the sum of the power at the two output ports.	$10\log_{10}(S_{mi} ^2 + S_{ki} ^2)$
Amplitude Balance	The difference in power between the two outputs.	$20\log_{10}(\left \frac{S_{ki}}{S_{mi}}\right)$
Phase Balance	The difference in phase angle between the two output ports.	$\angle S_{ki} - \angle S_{mi} + 90^{\circ}$

*100% RF test is performed per spec definition for pin configuration 1 and port 1 (input port) is connected to pin1, port 2 (isolated port) is connected to pin 3, port 3 (direct port) is connected to pin 4 and port 4 (isolated) is connected to pin 6.



Visit us at www.anaren.com

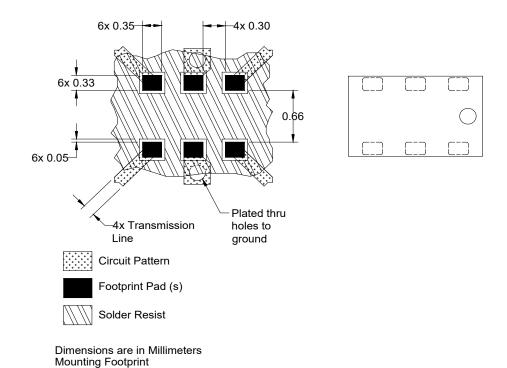


Mounting Configuration:

In order for Xinger surface mount components to work optimally, the proper impedance transmission lines must be used to connect to the RF ports. If this condition is not satisfied, insertion loss, Isolation and VSWR may not meet published specifications.

All of the Xinger components are constructed from organic PTFE based composites which possess excellent electrical and mechanical stability. Xinger components are compliant to a variety of ROHS and Green standards and ready for Pb-free soldering processes. Pads are Gold plated with a Nickel barrier.

An example of the PCB footprint used in the testing of these parts is shown below. In specific designs, the transmission line widths need to be adjusted to the unique dielectric coefficients and thicknesses as well as varying pick and place equipment tolerances.



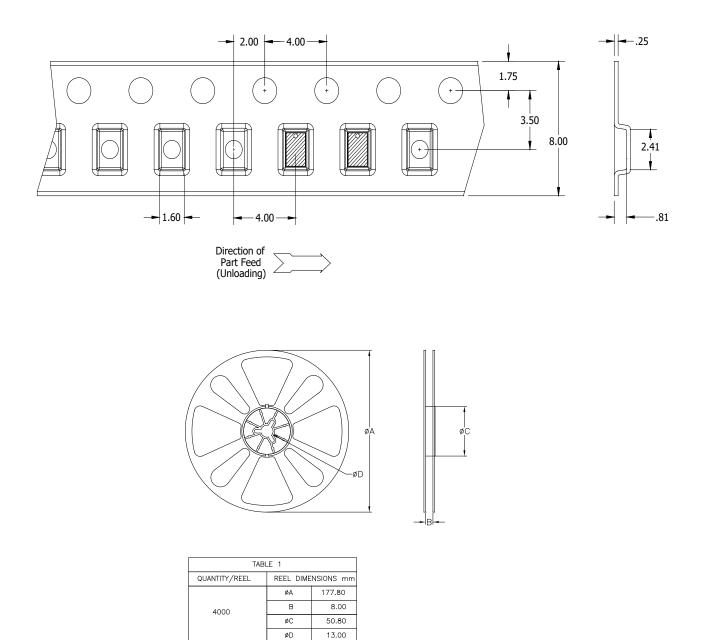


Visit us at www.anaren.com



Packaging and Ordering Information

Parts are available in reel and are packaged per EIA 481-D. Parts are oriented in tape and reel as shown below. Minimum order quantities are 4000 per reel.



A	Ina	are	n	ĺ
		we thin		

Visit us at www.anaren.com